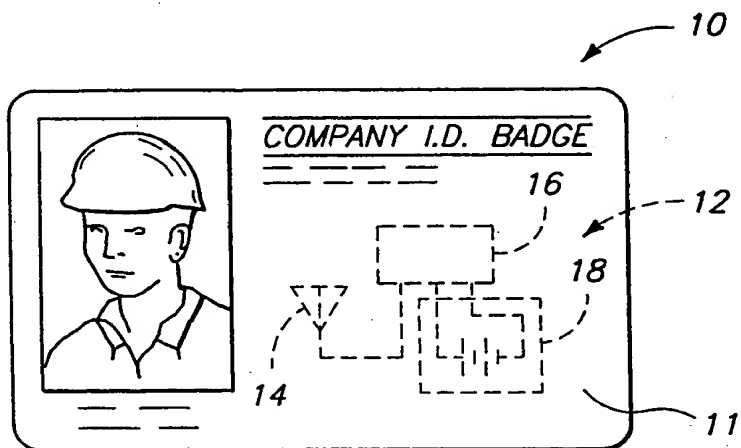
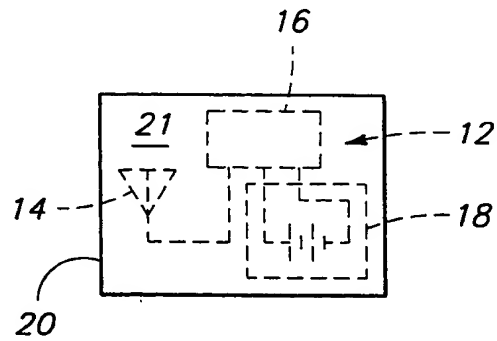


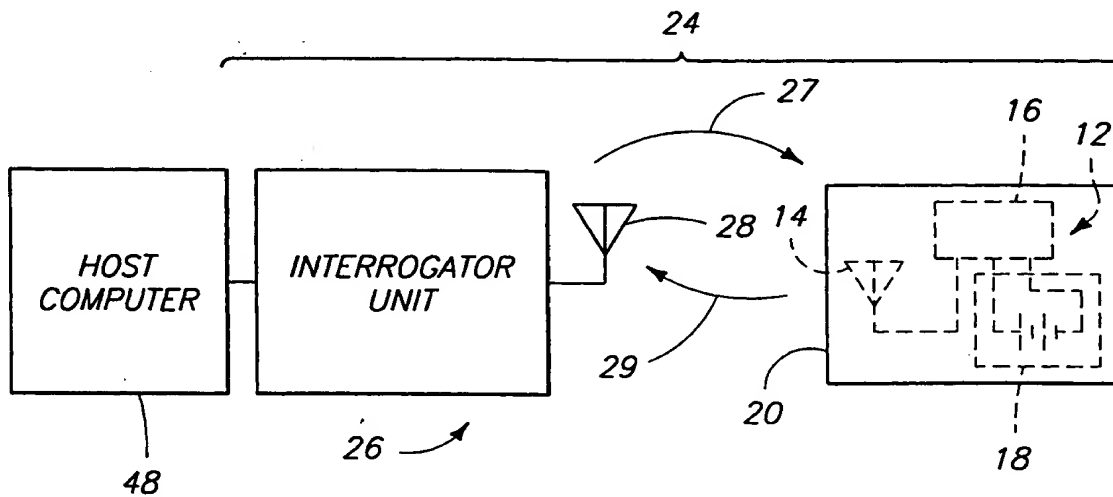
II II II II



Их аяг ая



Итого



Итого

# 2025年12月25日

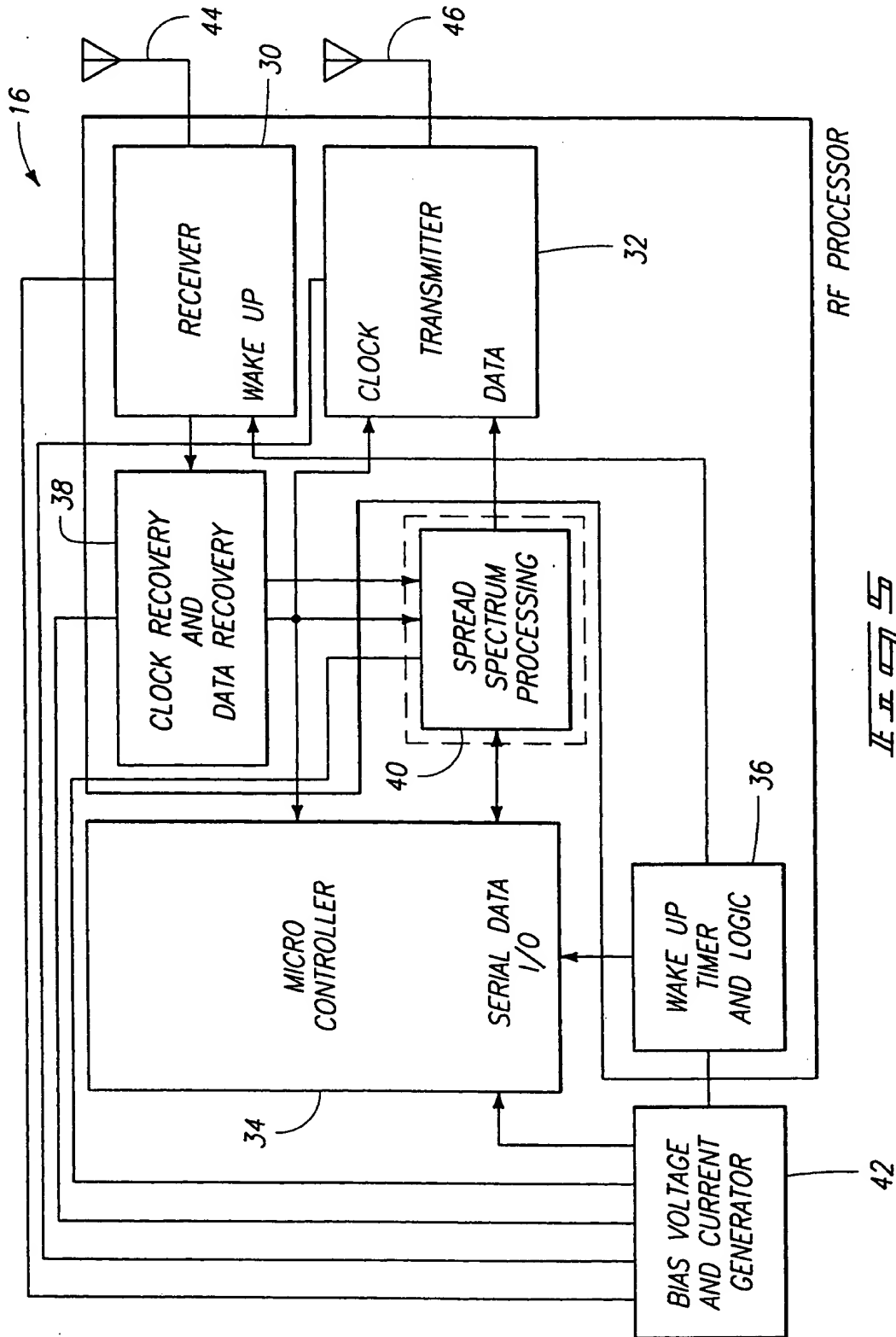


FIG. 1

6AA	6AB	6AC	6AD	6AE	6AF	6AG	6AH	6AI	6AJ	6AK
6BA	6BB	6BC	6BD	6BE	6BF	6BG	6BH	6BI	6BJ	6BK
6CA	6CB	6CC	6CD	6CE	6CF	6CG	6CH	6CI	6CJ	6CK
6DA	6DB	6DC	6DD	6DE	6DF	6DG	6DH	6DI	6DJ	6DK
6EA	6EB	6EC	6ED	6EE	6EF	6EG	6EH	6EI	6EJ	6EK

9 6 8 11



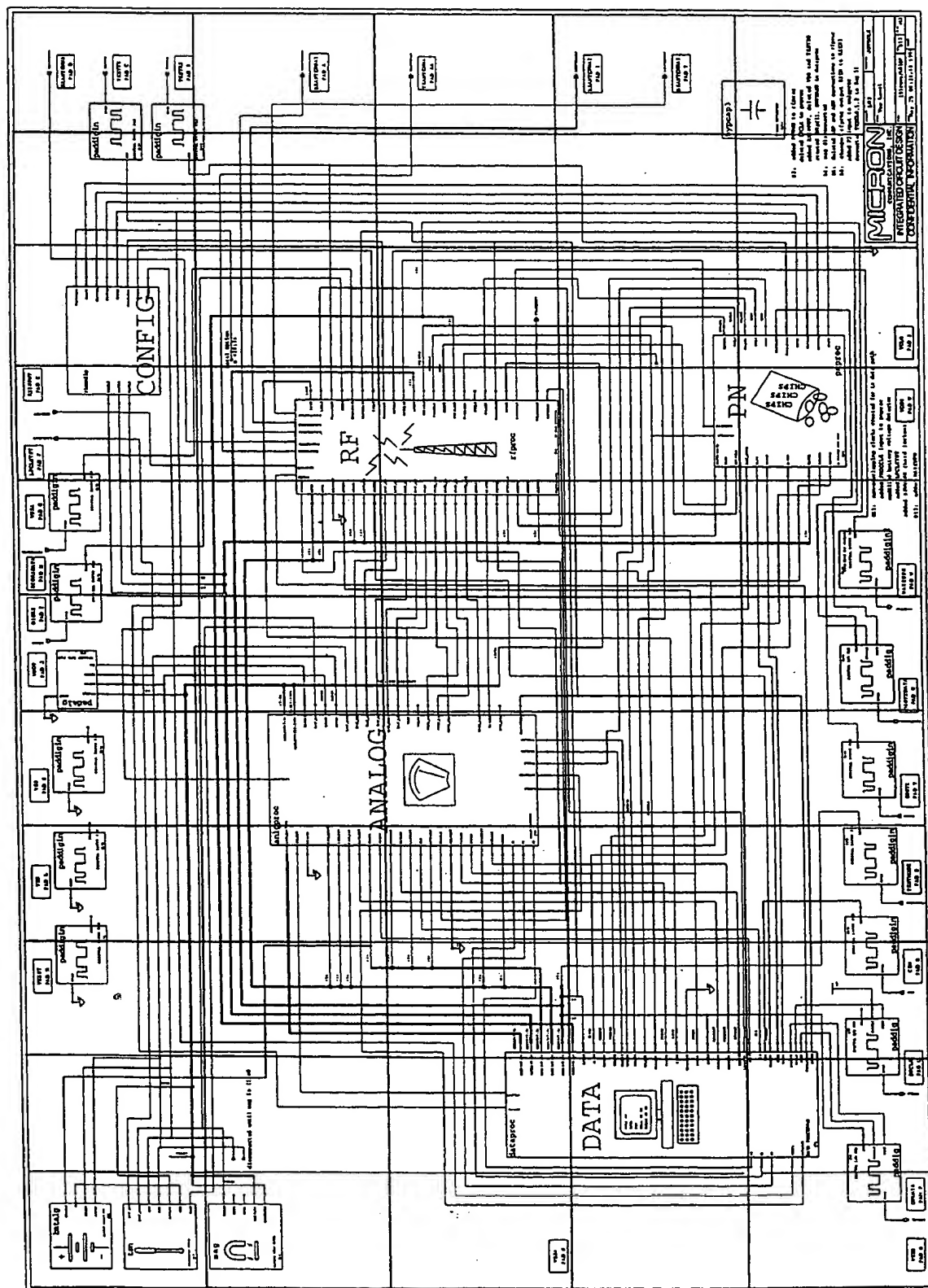
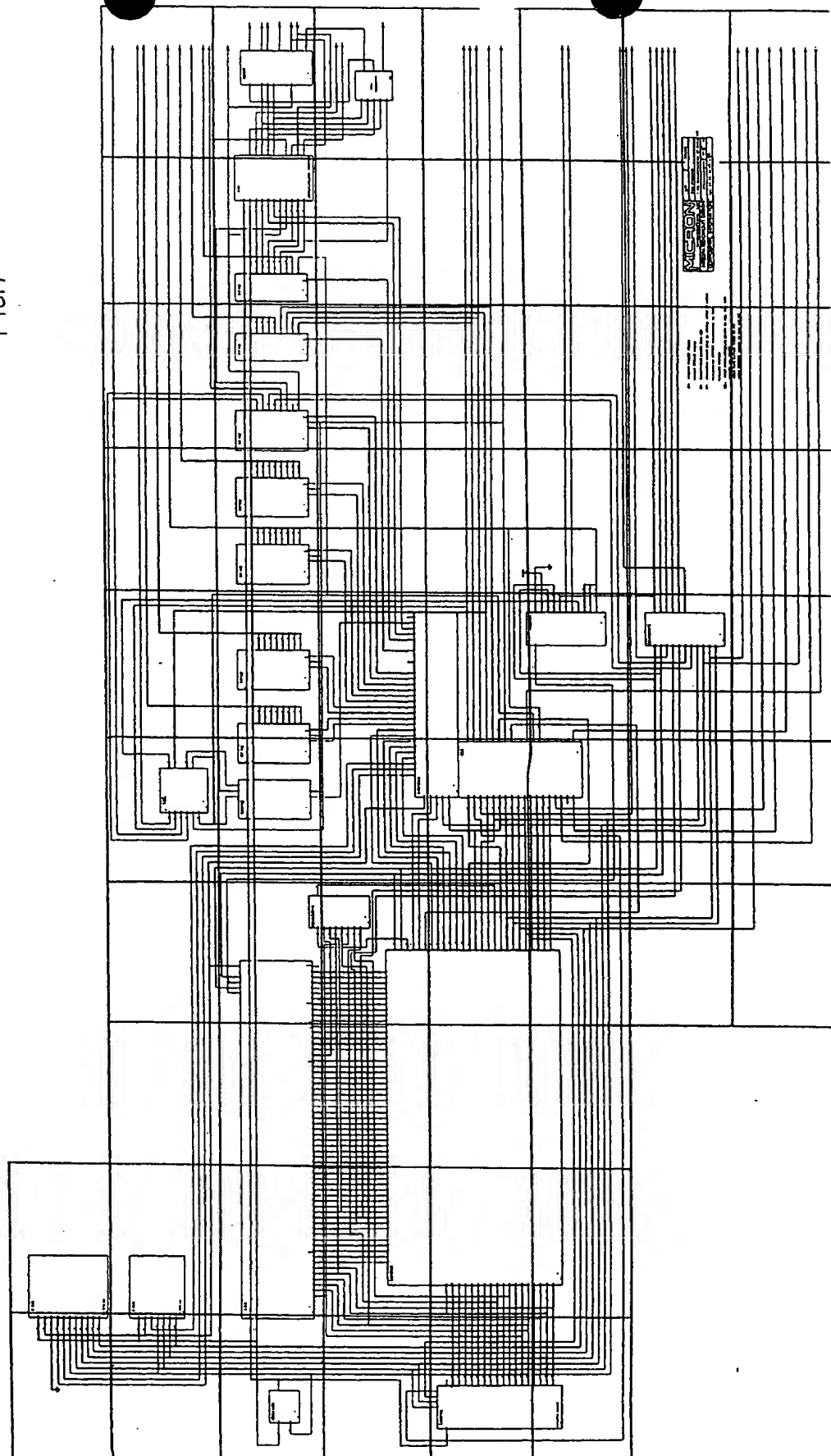
[illegible]

FIG. 6AA-EK



$$\frac{\mathbb{E}[\pi]}{\mathbb{E}[\pi^2]}$$

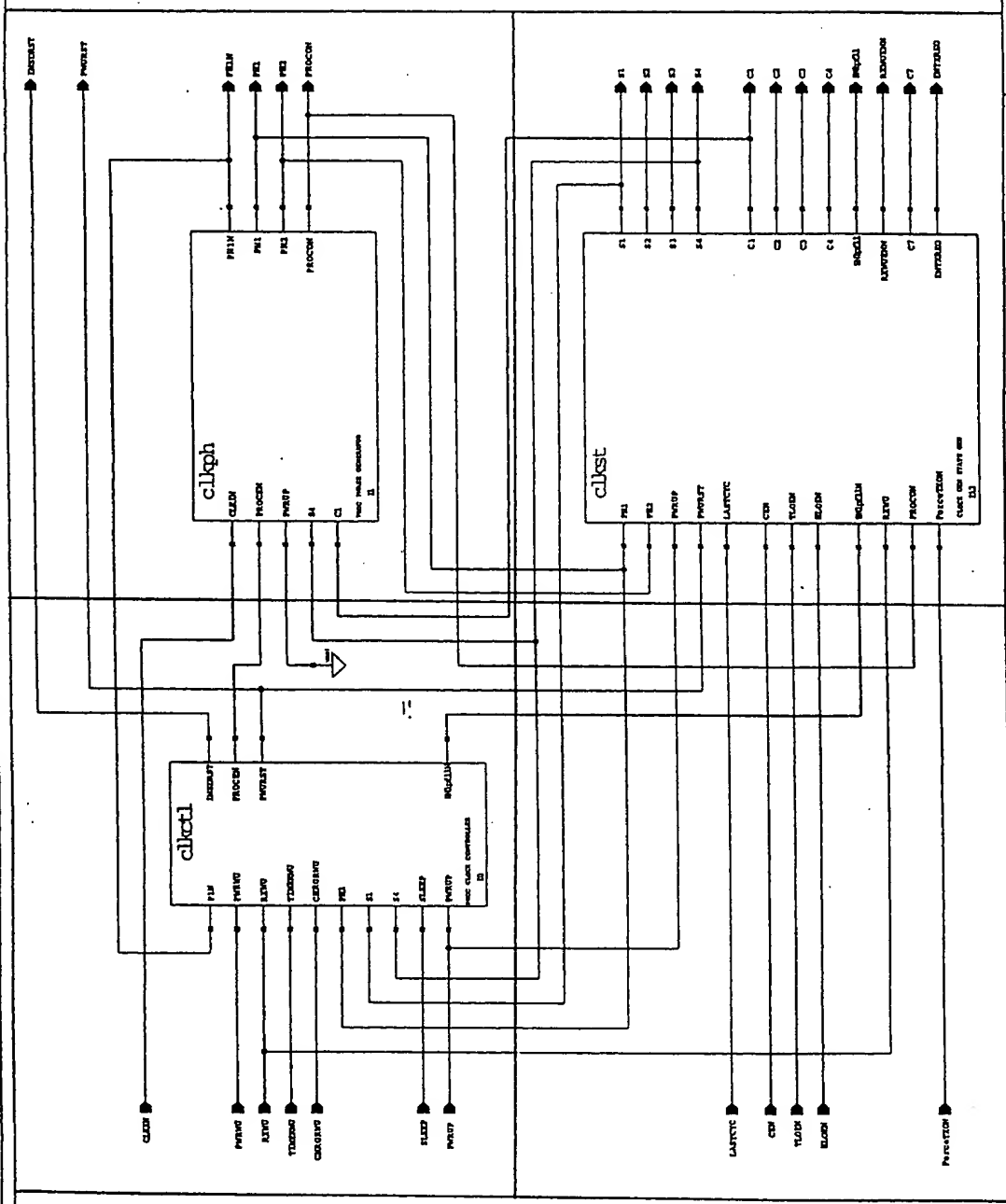
FIG. 7



7.01AA	7.01AB
7.01BA	7.01BB

1. 2. 3. 4. 5. 6. 7. 8. 9. 10. 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. 24. 25. 26. 27. 28. 29. 30. 31. 32. 33. 34. 35. 36. 37. 38. 39. 40. 41. 42. 43. 44. 45. 46. 47. 48. 49. 50. 51. 52. 53. 54. 55. 56. 57. 58. 59. 60. 61. 62. 63. 64. 65. 66. 67. 68. 69. 70. 71. 72. 73. 74. 75. 76. 77. 78. 79. 80. 81. 82. 83. 84. 85. 86. 87. 88. 89. 90. 91. 92. 93. 94. 95. 96. 97. 98. 99. 100.

Fig. 7.01



|                           |  |                            |                   |
|---------------------------|--|----------------------------|-------------------|
| MICRON                    |  | PRODUCT: L03               | REVISION: Rev2.01 |
| COMMUNICATIONS, INC.      |  | Processor Clock Generator  |                   |
| INTEGRATED CIRCUIT DESIGN |  | 2-Phase/4-State/8-Cycle    |                   |
| CONFIDENTIAL INFORMATION  |  | DATE: 10/19/94             | BY: B11           |
|                           |  | DATE: Apr 13 09:58:52 1996 | BY: B11           |

E2: created ENUPRLL signal  
 added ENUPRLL logic  
 B11: added hard lockout to clkst

|          |          |
|----------|----------|
| 7.0101AA | 7.0101AB |
| 7.0101BA | 7.0101BB |

EX-11

|                                   |                |                            |       |
|-----------------------------------|----------------|----------------------------|-------|
| <b>MICRON</b>                     |                |                            |       |
| COMMUNICATIONS, INC.              |                |                            |       |
| INTEGRATED CIRCUIT DESIGN         |                |                            |       |
| CONFIDENTIAL INFORMATION          |                |                            |       |
| PROJECT: L03                      | DATE: 06/26/95 | PAGE: 1                    |       |
| TITLE: Processor Clock Controller |                | DATE: Jul 26 11:15:27 1995 |       |
| NAME:                             | 10/rows/clock: | B2:                        | roll: |

82: created ENIPFLN signal



|          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0102BA | 7.0102BB | 7.0102BC | 7.0102BD | 7.0102BE | 7.0102BF | 7.0102AG | 7.0102AH | 7.0102AI | 7.0102AJ |
| 7.0102CA | 7.0102CB | 7.0102CC | 7.0102CD | 7.0102CE | 7.0102CF | 7.0102CG | 7.0102CH | 7.0102CI | 7.0102CJ |
| 7.0102DA | 7.0102DB | 7.0102DC | 7.0102DD | 7.0102DE | 7.0102DF | 7.0102DG | 7.0102DH | 7.0102DI | 7.0102DJ |

FILED "E 300000"

MICROCOM COMMUNICATIONS INC.

|                           |  |
|---------------------------|--|
| Processor Phase Generator |  |
|---------------------------|--|

## 2-Phase Non-overlapping

|         |              |         |    |
|---------|--------------|---------|----|
| Speed : | 103kva/clkph | REV. B8 | 13 |
|---------|--------------|---------|----|

DATE Dec 5 17:55:56 1995 PAGE 1

**B2: Pin name changes**

B8: Added 6 inverters to non-overlap time

**Make the number of inverters adjustable on metal**

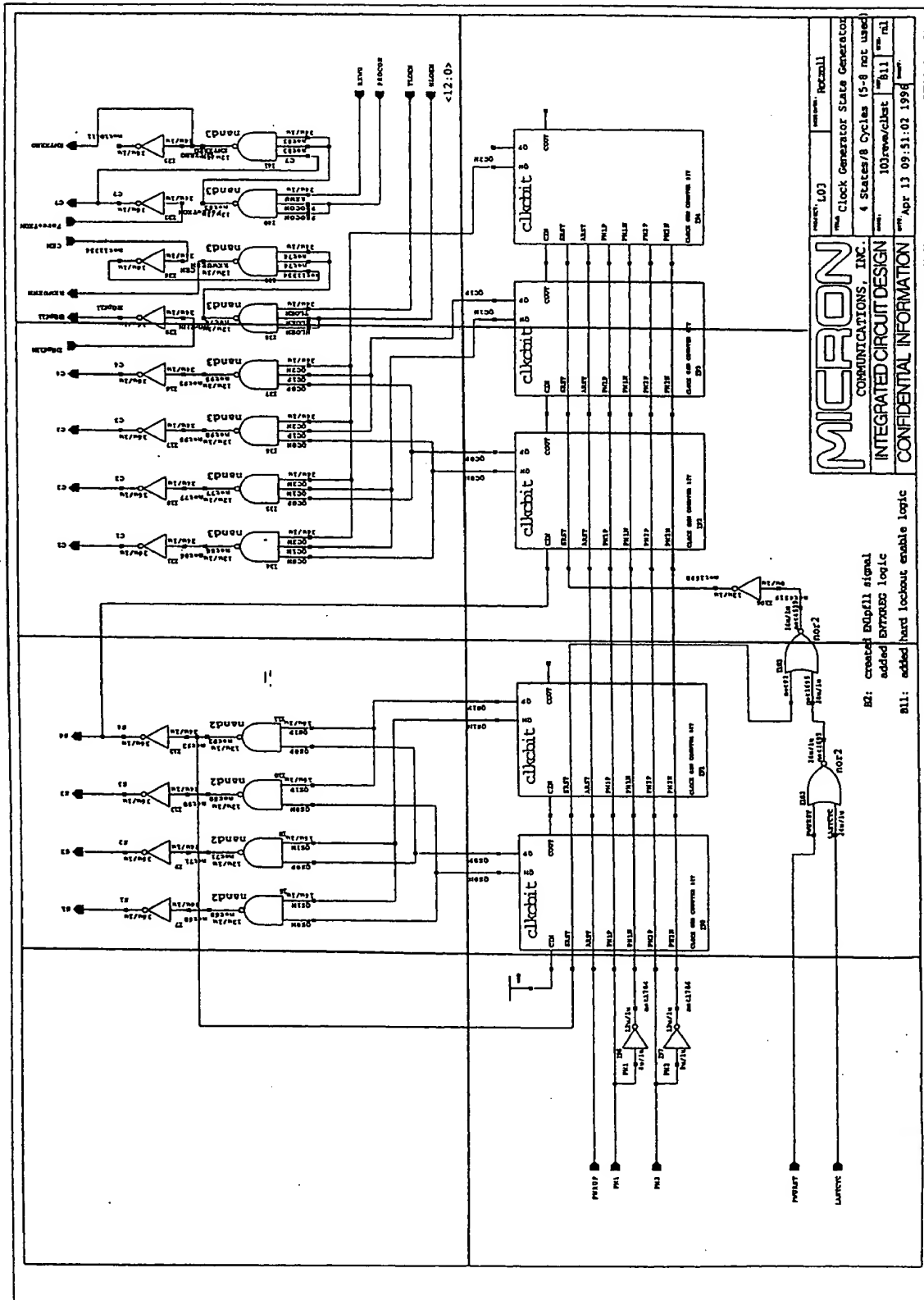
[illegible]

MI40-030

|          |          |          |          |
|----------|----------|----------|----------|
| 7.0103AA | 7.0103AB | 7.0103AC | 7.0103AD |
| 7.0103BA | 7.0103BB | 7.0103BC | 7.0103BD |

# ENTRÉE

Fig. 7.0103



**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

Part: L03      Rev: 103      Recall

File: Clock Generator State Generation

4 States/8 Cycles (5-8 not used)

100new/clock      811      ml

Apr 13 09:51:02 1996

7.010301AA 7.010301AB

MI40-030

|            |            |
|------------|------------|
| 7.010301AA | 7.010301AB |
| 7.010301BA | 7.010301BB |

7.010301BB

CONFIDENTIAL

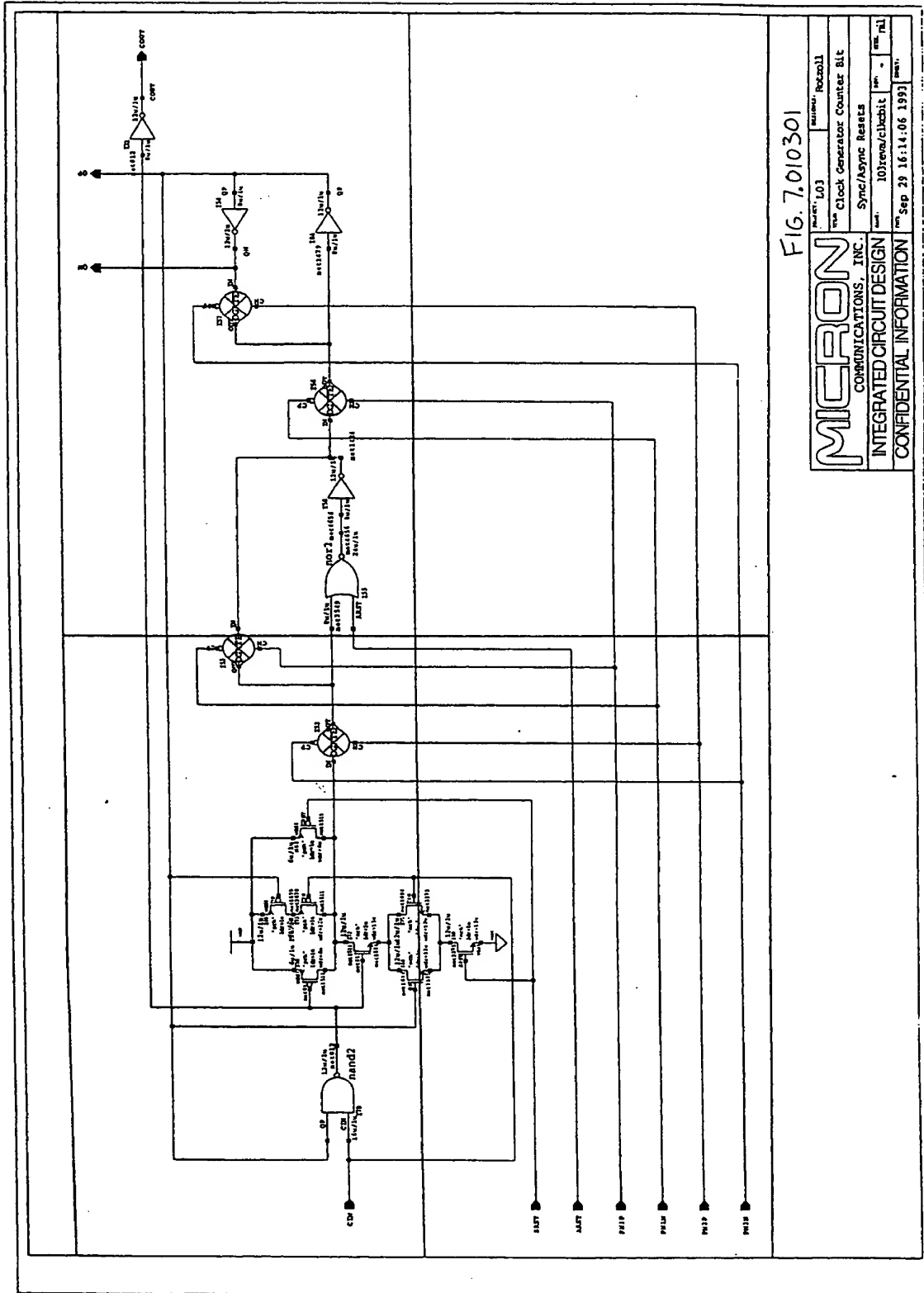


FIG. 7.010301

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|
| 7.02AA | 7.02AB | 7.02AC | 7.02AD | 7.02AE | 7.02AF |
| 7.02BA | 7.02BB | 7.02BC | 7.02BD | 7.02BE | 7.02BF |

CONFIDENTIAL

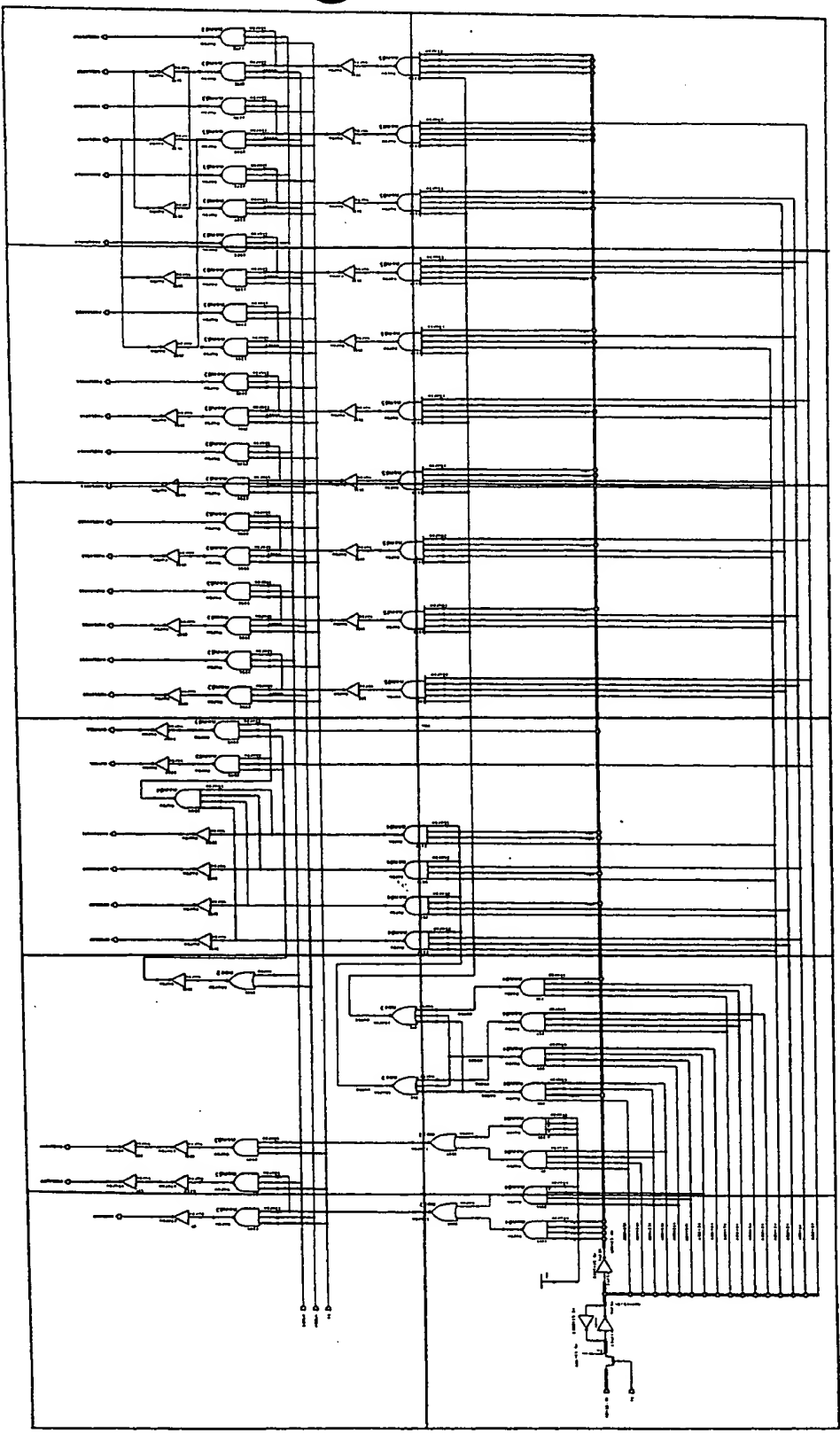


Fig. 7.02

|                           |         |
|---------------------------|---------|
| <b>MICRON</b>             |         |
| COMMUNICATIONS            |         |
| INTEGRATED CIRCUIT DESIGN |         |
| CONFIDENTIAL INFORMATION  |         |
| DATE                      | 10/1/77 |
| DESIGNED BY               | ...     |
| CHECKED BY                | ...     |
| APPROVED BY               | ...     |
| DATE                      | 10/1/77 |

MS. Integrated Drive at Wiegand and Wiegand



|        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|
| 7.03AA | 7.03AB | 7.03AC | 7.03AD | 7.03AE | 7.03AF | 7.03AG | 7.03AH |
| 7.03BA | 7.03BB | 7.03BC | 7.03BD | 7.03BE | 7.03BF | 7.03BG | 7.03BH |
| 7.03CA | 7.03CB | 7.03CC | 7.03CD | 7.03CE | 7.03CF | 7.03CG | 7.03CH |
| 7.03DA | 7.03DB | 7.03DC | 7.03DD | 7.03DE | 7.03DF | 7.03DG | 7.03DH |
| 7.03EA | 7.03EB | 7.03EC | 7.03ED | 7.03EE | 7.03EF | 7.03EG | 7.03EH |

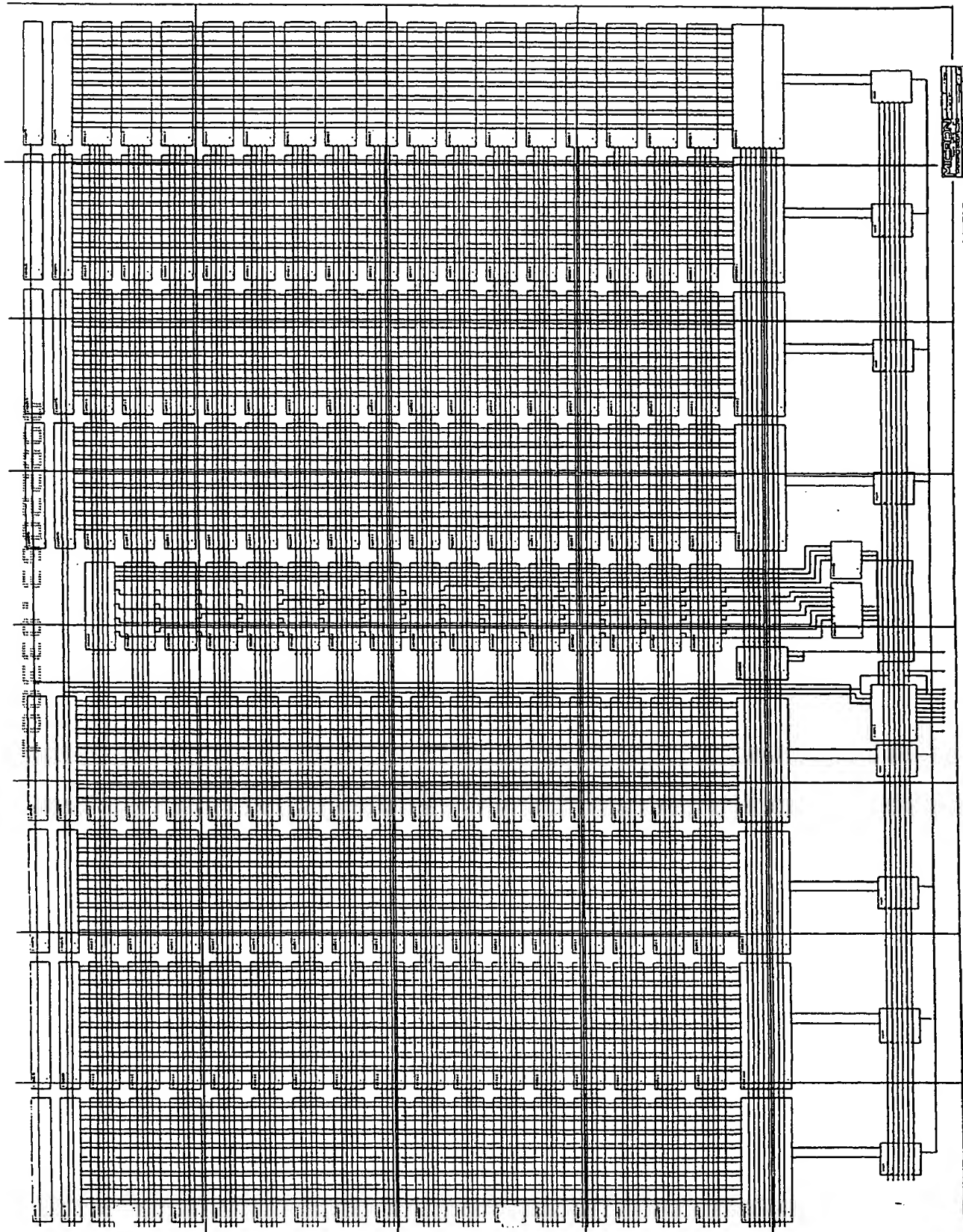


Fig 7.03

|          |          |
|----------|----------|
| 7.0301AA | 7.0301AB |
| 7.0301BA | 7.0301BB |



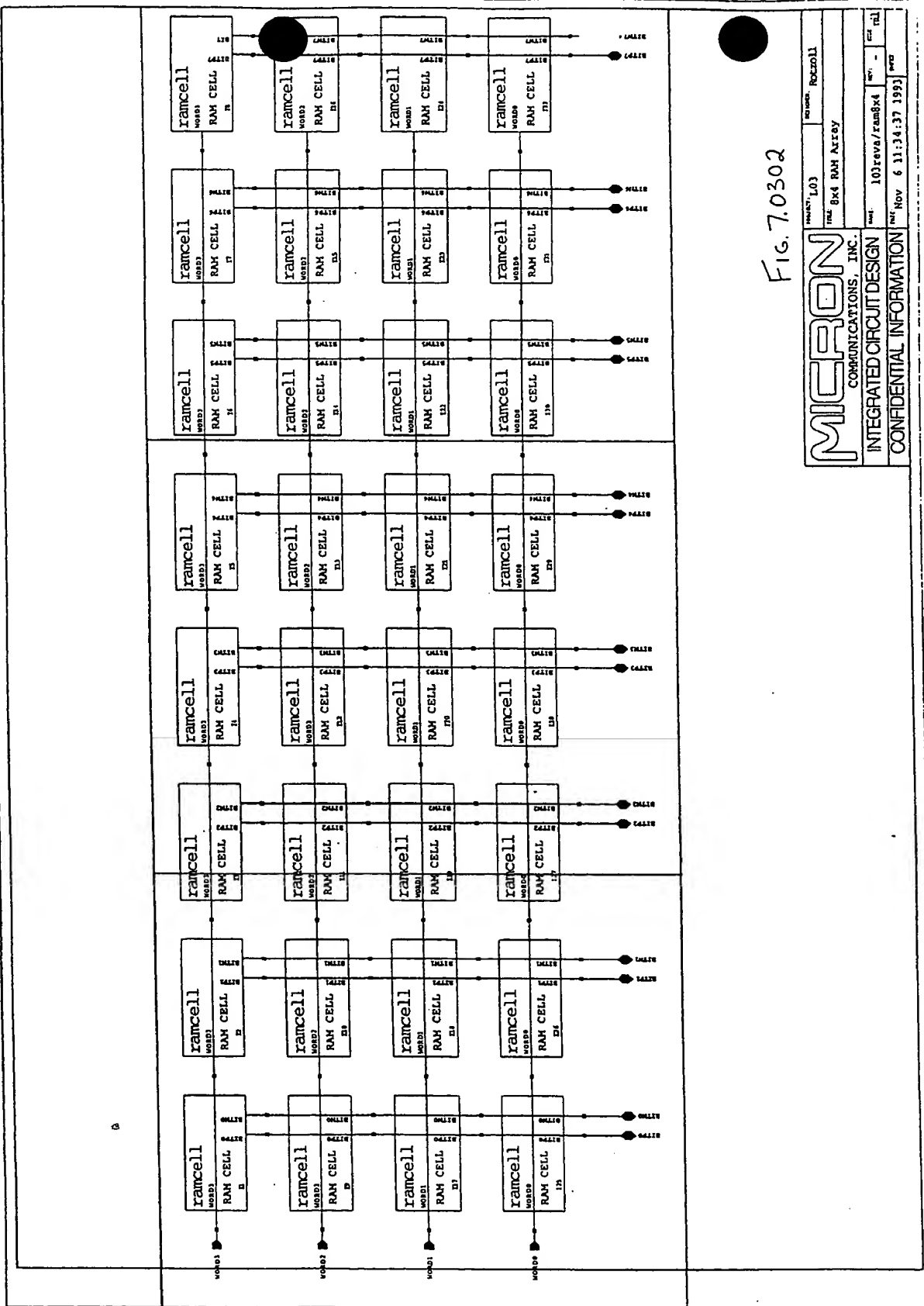
7.0302AA 7.0302AB 7.0302AC

MI40-030

|          |          |          |
|----------|----------|----------|
| 7.0302AA | 7.0302AB | 7.0302AC |
|----------|----------|----------|

7.0302

CONFIDENTIAL



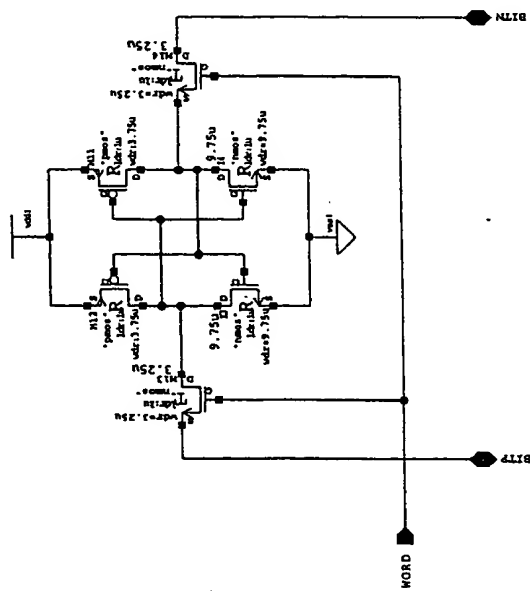


FIG. 7.03020

|        |  |                           |  |                   |  |
|--------|--|---------------------------|--|-------------------|--|
| MICRON |  | PROJECT: L03              |  | DESIGNER: Rotzoll |  |
|        |  | TITLE: 6T RAM Cell        |  |                   |  |
|        |  |                           |  |                   |  |
|        |  | NAME: 103reva/rancell     |  | STEP: A           |  |
|        |  | DATE: Nov 6 11:34:48 1993 |  | SHEET:            |  |

|        |  |                           |  |                   |  |
|--------|--|---------------------------|--|-------------------|--|
| MICRON |  | PROJECT: L03              |  | DESIGNER: Rotzoll |  |
|        |  | TITLE: 6T RAM Cell        |  |                   |  |
|        |  |                           |  |                   |  |
|        |  | NAME: 103reva/rancell     |  | STEP: A           |  |
|        |  | DATE: Nov 6 11:34:48 1993 |  | SHEET:            |  |

|        |  |                           |  |                   |  |
|--------|--|---------------------------|--|-------------------|--|
| MICRON |  | PROJECT: L03              |  | DESIGNER: Rotzoll |  |
|        |  | TITLE: 6T RAM Cell        |  |                   |  |
|        |  |                           |  |                   |  |
|        |  | NAME: 103reva/rancell     |  | STEP: A           |  |
|        |  | DATE: Nov 6 11:34:48 1993 |  | SHEET:            |  |

|        |  |                           |  |                   |  |
|--------|--|---------------------------|--|-------------------|--|
| MICRON |  | PROJECT: L03              |  | DESIGNER: Rotzoll |  |
|        |  | TITLE: 6T RAM Cell        |  |                   |  |
|        |  |                           |  |                   |  |
|        |  | NAME: 103reva/rancell     |  | STEP: A           |  |
|        |  | DATE: Nov 6 11:34:48 1993 |  | SHEET:            |  |

|        |  |                           |  |                   |  |
|--------|--|---------------------------|--|-------------------|--|
| MICRON |  | PROJECT: L03              |  | DESIGNER: Rotzoll |  |
|        |  | TITLE: 6T RAM Cell        |  |                   |  |
|        |  |                           |  |                   |  |
|        |  | NAME: 103reva/rancell     |  | STEP: A           |  |
|        |  | DATE: Nov 6 11:34:48 1993 |  | SHEET:            |  |

7.0303AD 7.0303AC 7.0303AB 7.0303AA

MI40-030

|          |          |          |          |
|----------|----------|----------|----------|
| 7.0303AA | 7.0303AB | 7.0303AC | 7.0303AD |
|----------|----------|----------|----------|

7.0303





TABLE 2-50

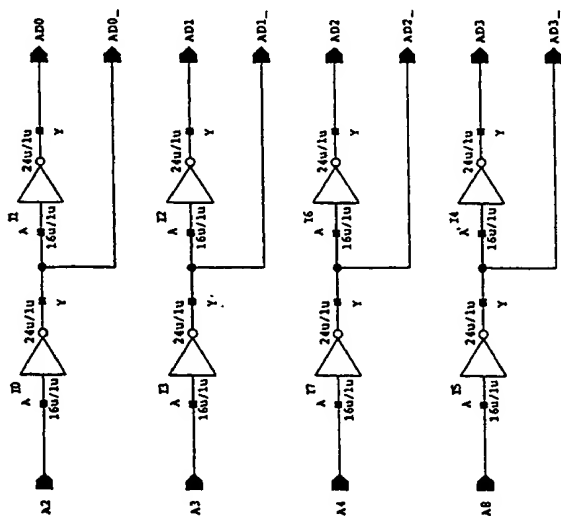
MI40-030

|          |          |          |          |
|----------|----------|----------|----------|
| 7.0304AA | 7.0304AB | 7.0304AC | 7.0304AD |
|----------|----------|----------|----------|

ITEM 7.0304



FIG. 7.0305



**NICEARON**  
COMMUNICATIONS, INC.

COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

**PROJECT: L03**

**Rotzoll**

| TIME | RAM Address Buffer |
|------|--------------------|
| 0000 | 0000               |
| 0001 | 0001               |
| 0002 | 0002               |
| 0003 | 0003               |
| 0004 | 0004               |
| 0005 | 0005               |
| 0006 | 0006               |
| 0007 | 0007               |
| 0008 | 0008               |
| 0009 | 0009               |
| 0010 | 0010               |
| 0011 | 0011               |
| 0012 | 0012               |
| 0013 | 0013               |
| 0014 | 0014               |
| 0015 | 0015               |
| 0016 | 0016               |
| 0017 | 0017               |
| 0018 | 0018               |
| 0019 | 0019               |
| 0020 | 0020               |
| 0021 | 0021               |
| 0022 | 0022               |
| 0023 | 0023               |
| 0024 | 0024               |
| 0025 | 0025               |
| 0026 | 0026               |
| 0027 | 0027               |
| 0028 | 0028               |
| 0029 | 0029               |
| 0030 | 0030               |
| 0031 | 0031               |
| 0032 | 0032               |
| 0033 | 0033               |
| 0034 | 0034               |
| 0035 | 0035               |
| 0036 | 0036               |
| 0037 | 0037               |
| 0038 | 0038               |
| 0039 | 0039               |
| 0040 | 0040               |
| 0041 | 0041               |
| 0042 | 0042               |
| 0043 | 0043               |
| 0044 | 0044               |
| 0045 | 0045               |
| 0046 | 0046               |
| 0047 | 0047               |
| 0048 | 0048               |
| 0049 | 0049               |
| 0050 | 0050               |
| 0051 | 0051               |
| 0052 | 0052               |
| 0053 | 0053               |
| 0054 | 0054               |
| 0055 | 0055               |
| 0056 | 0056               |
| 0057 | 0057               |
| 0058 | 0058               |
| 0059 | 0059               |
| 0060 | 0060               |
| 0061 | 0061               |
| 0062 | 0062               |
| 0063 | 0063               |
| 0064 | 0064               |
| 0065 | 0065               |
| 0066 | 0066               |
| 0067 | 0067               |
| 0068 | 0068               |
| 0069 | 0069               |
| 0070 | 0070               |
| 0071 | 0071               |
| 0072 | 0072               |
| 0073 | 0073               |
| 0074 | 0074               |
| 0075 | 0075               |
| 0076 | 0076               |
| 0077 | 0077               |
| 0078 | 0078               |
| 0079 | 0079               |
| 0080 | 0080               |
| 0081 | 0081               |
| 0082 | 0082               |
| 0083 | 0083               |
| 0084 | 0084               |
| 0085 | 0085               |
| 0086 | 0086               |
| 0087 | 0087               |
| 0088 | 0088               |
| 0089 | 0089               |
| 0090 | 0090               |
| 0091 | 0091               |
| 0092 | 0092               |
| 0093 | 0093               |
| 0094 | 0094               |
| 0095 | 0095               |
| 0096 | 0096               |
| 0097 | 0097               |
| 0098 | 0098               |
| 0099 | 0099               |

**NAME:**

103reva/ramadb

|       |   |
|-------|---|
| Size: | A |
|-------|---|

DATE, Sep 29 16:04:01 1993

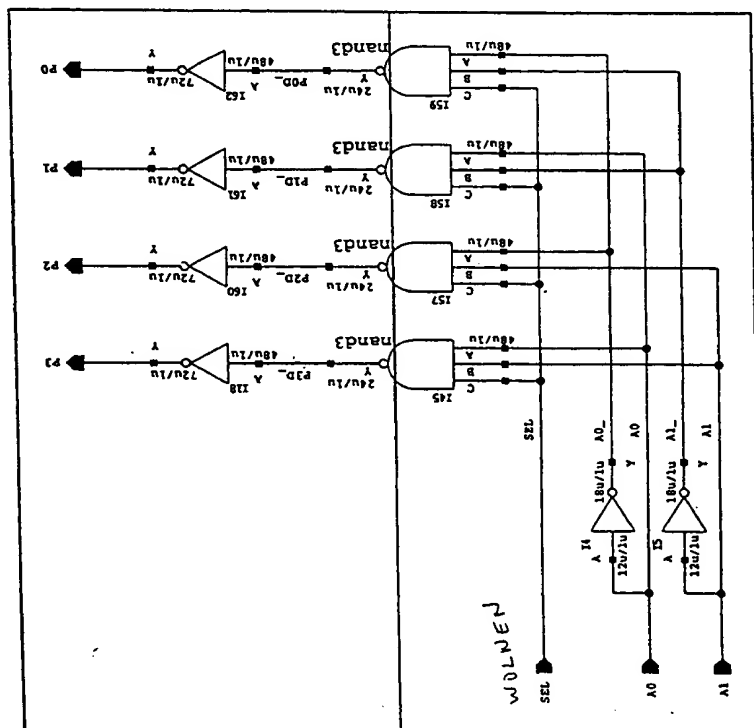
7.0306AA

7.0306BA

7.0306

7.0306AA 7.0306BA

FIG. 7.0306



**NOOR**  
COMMUNICATIONS INC.

**COMMUNICATIONS, INC.**  
**INTEGRATED CIRCUIT DESIGN**

**CONFIDENTIAL INFORMATION**

|              |                   |
|--------------|-------------------|
| PROJECT: L03 | DESIGNER: Rotzoll |
|--------------|-------------------|

**RAM Word Line Driver**

| NAME:          | REV: | SIZE | A |
|----------------|------|------|---|
| 103revs/ramwdr | -    |      |   |

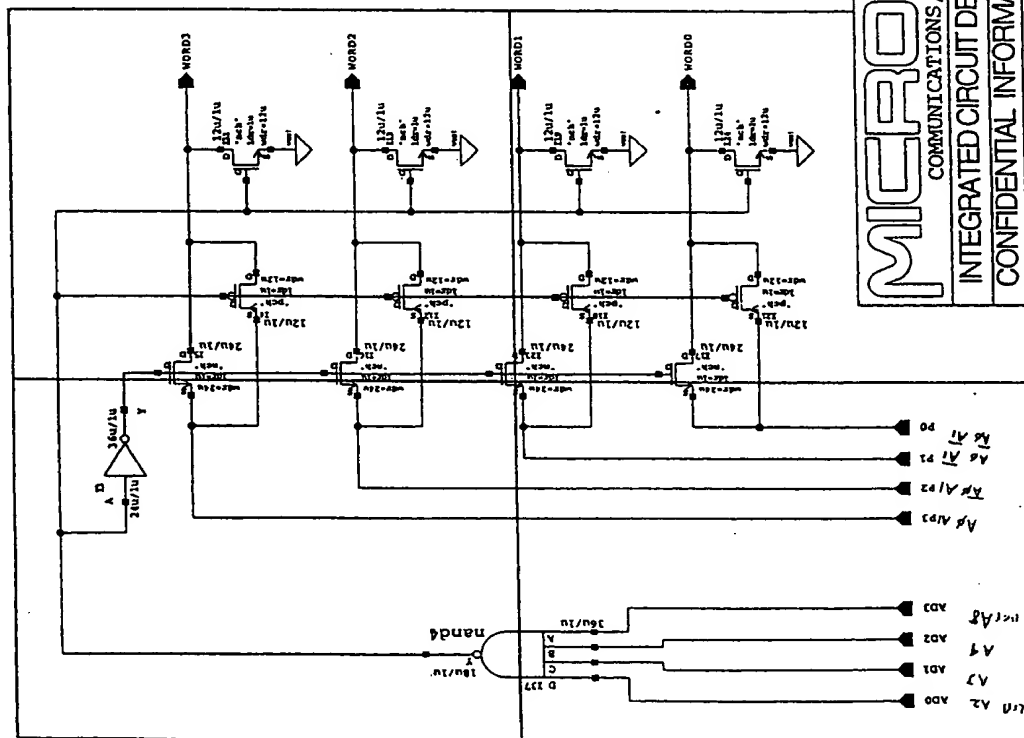
|                            |        |
|----------------------------|--------|
| DATE: Sep 29 16:04:16 1993 | SHEET: |
|----------------------------|--------|

70620" 902200

|          |          |
|----------|----------|
| 7.0307AA | 7.0307AB |
| 7.0307BA | 7.0307BB |

7.0307

FIG. 7.0307



|                           |  |                      |  |             |  |         |  |
|---------------------------|--|----------------------|--|-------------|--|---------|--|
| <b>MICRON</b>             |  | PRODUCT# L03         |  | SERIES#0001 |  | R02coll |  |
| COMMUNICATIONS, INC.      |  |                      |  |             |  |         |  |
| INTEGRATED CIRCUIT DESIGN |  |                      |  |             |  |         |  |
| CONFIDENTIAL INFORMATION  |  |                      |  |             |  |         |  |
| NAME#                     |  | 103-reva/ramwdec     |  | REV# -      |  | PAGE# A |  |
| DATE#                     |  | Sep 29 15:41:08 1993 |  |             |  |         |  |
| SHEET#                    |  |                      |  |             |  |         |  |



7.0308BB

7.0308AA

7.0308AB

7.0308BA

7.0308BB

7.0308

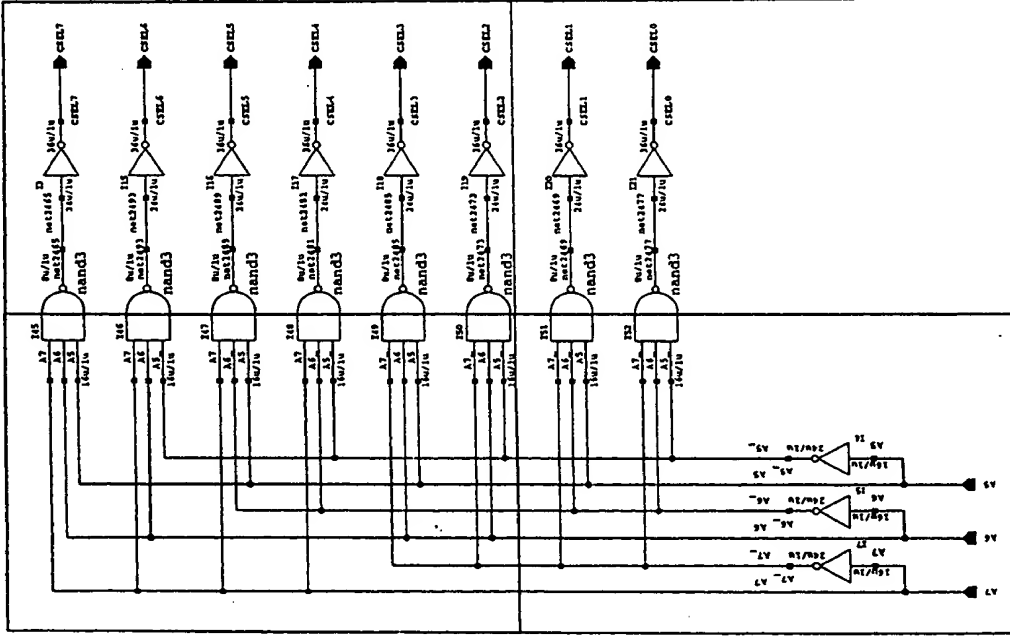


FIG. 7.0308

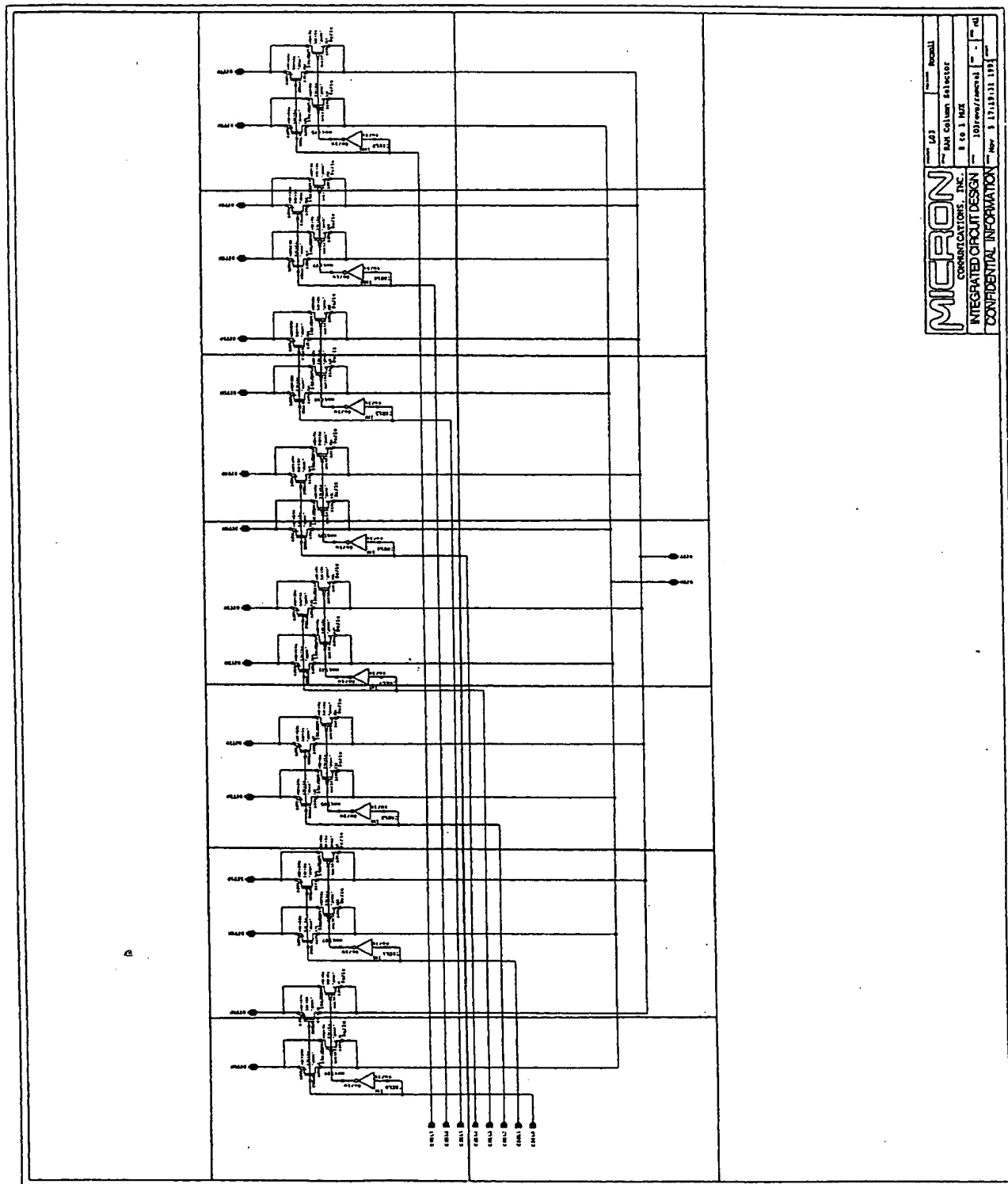
|                           |  |          |                         |          |                     |
|---------------------------|--|----------|-------------------------|----------|---------------------|
| MICRON                    |  | REVISION | Rev. 1.03               | DATE     | Nov 5 17:21:07 1993 |
| COMMUNICATIONS, INC.      |  | PROJECT  | RM Column Select Decode | DESIGNER | 3 to 8              |
| INTEGRATED CIRCUIT DESIGN |  | REVISION | 101rev1/rancdec         | DATE     | Nov 5 17:21:07 1993 |
| CONFIDENTIAL INFORMATION  |  |          |                         |          |                     |



|          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|
| 7.0309AA | 7.0309AB | 7.0309AC | 7.0309AD | 7.0309AE | 7.0309AF | 7.0309AG |
| 7.0309BA | 7.0309BB | 7.0309BC | 7.0309BD | 7.0309BE | 7.0309BF | 7.0309BG |

SOLENO. II

At the





|          |          |
|----------|----------|
| 7.0310AA | 7.0310AB |
| 7.0310BA | 7.0310BB |

МІЖНАРОД. ПРАВО

FIG. 7.0310

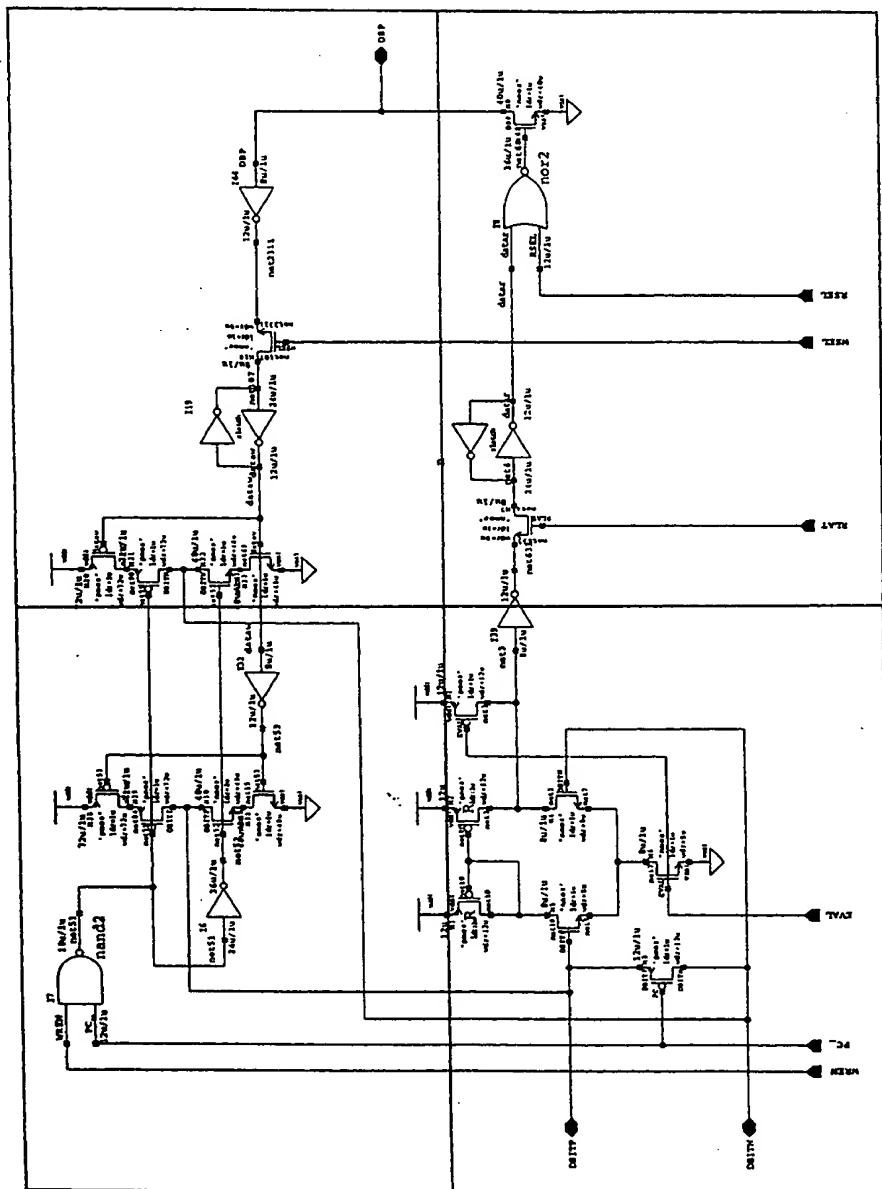


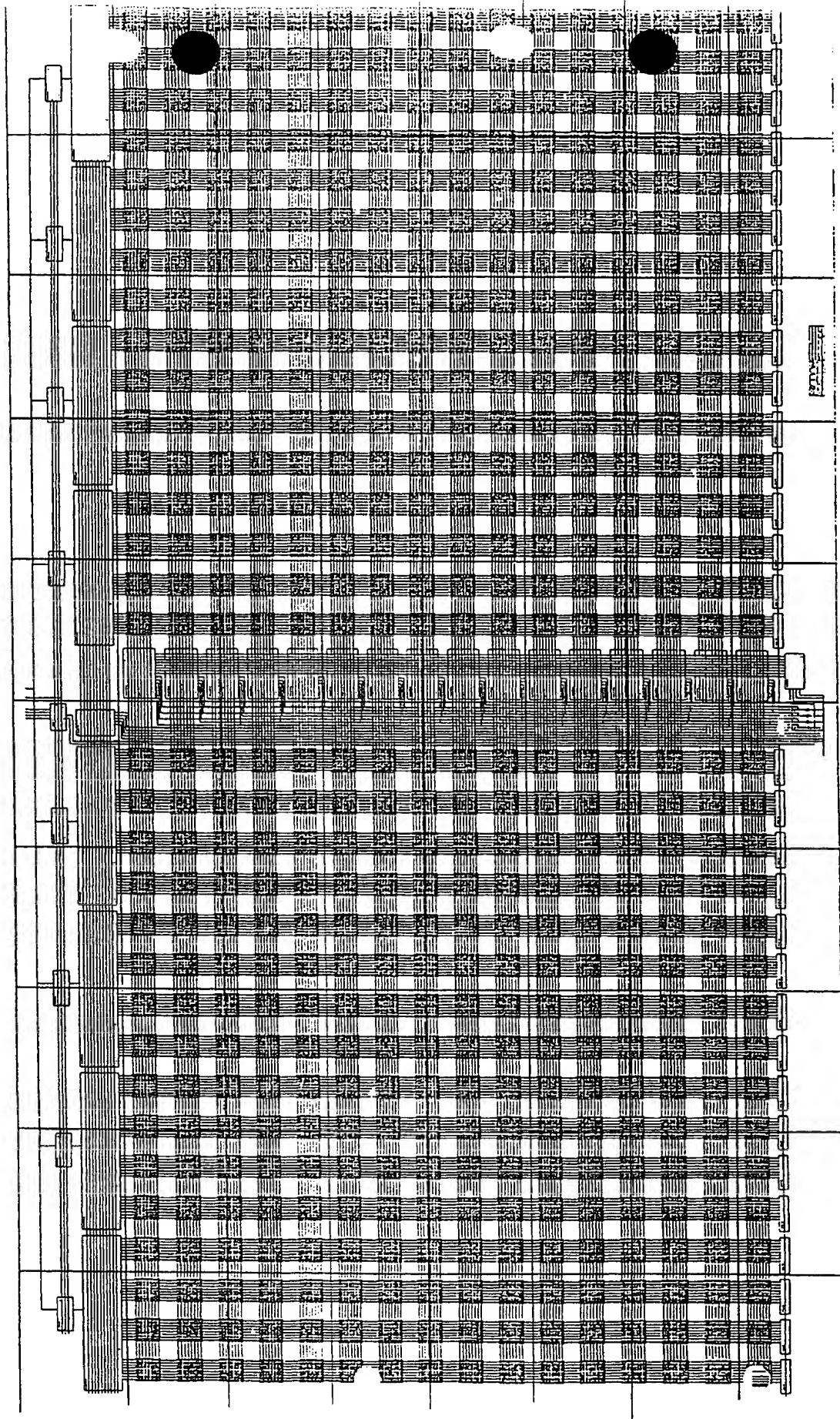
FIG. 7.0310

|                                  |  |                              |                   |
|----------------------------------|--|------------------------------|-------------------|
| <b>MICRON</b>                    |  | PROJECT: L03                 | REVISION: Rotzoll |
| <b>COMMUNICATIONS, INC.</b>      |  | REAL: RAM Database Interface |                   |
| <b>INTEGRATED CIRCUIT DESIGN</b> |  | MODEL: 103rev/a/ramdb        | REV: -            |
| <b>CONFIDENTIAL INFORMATION</b>  |  | DATE: Oct 6 12:08:33 1993    |                   |
| <b>CONFIDENTIAL INFORMATION</b>  |  | MOUNT:                       |                   |

|        |        |        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|
| 7.04AA | 7.04AB | 7.04AC | 7.04AD | 7.04AE | 7.04AF | 7.04AG | 7.04AH | 7.04AI | 7.04AJ |
| 7.04BA | 7.04BB | 7.04BC | 7.04BD | 7.04BE | 7.04BF | 7.04BG | 7.04BH | 7.04BI | 7.04BJ |
| 7.04CA | 7.04CB | 7.04CC | 7.04CD | 7.04CE | 7.04CF | 7.04CG | 7.04CH | 7.04CI | 7.04CJ |
| 7.04DA | 7.04DB | 7.04DC | 7.04DD | 7.04DE | 7.04DF | 7.04DG | 7.04DH | 7.04DI | 7.04DJ |
| 7.04EA | 7.04EB | 7.04EC | 7.04ED | 7.04EE | 7.04EF | 7.04EG | 7.04EH | 7.04EI | 7.04EJ |
| 7.04FA | 7.04FB | 7.04FC | 7.04FD | 7.04FE | 7.04FF | 7.04FG | 7.04FH | 7.04FI | 7.04FJ |
| 7.04GA | 7.04GB | 7.04GC | 7.04GD | 7.04GE | 7.04GF | 7.04GG | 7.04GH | 7.04GI | 7.04GJ |
| 7.04HA | 7.04HB | 7.04HC | 7.04HD | 7.04HE | 7.04HF | 7.04HG | 7.04HH | 7.04HI | 7.04HJ |

TOP OF "E9022800"

FIG. 7.04





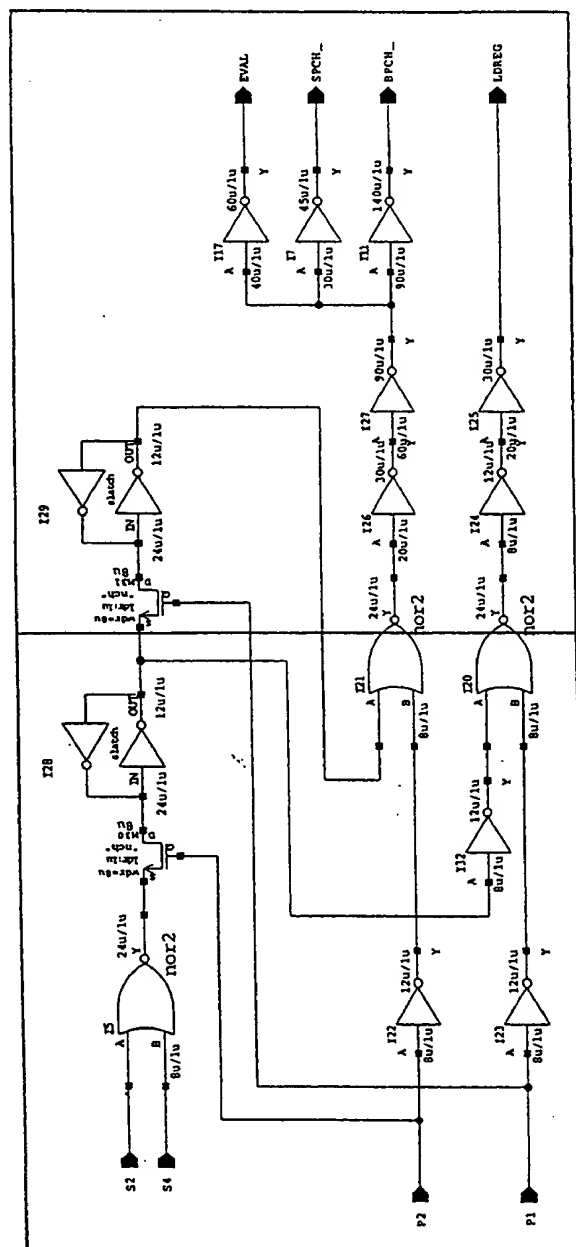


|          |          |
|----------|----------|
| 7.0401AA | 7.0401AB |
|----------|----------|

7.0401AA

IIIIII. II IIII

FIG. 7.0401



# 2100

**COMMUNICATIONS, INC.**  
**INTEGRATED CIRCUIT DESIGN**

**CONFIDENTIAL INFORMATION**

|              |                 |
|--------------|-----------------|
| PROJECT: L03 | DESIGN: Rotzoll |
|--------------|-----------------|

## ROM Control Logic

|                |        |       |
|----------------|--------|-------|
| NAME:          | REV: - | SIZE: |
| 103reva/romctl |        |       |

|       |                     |       |
|-------|---------------------|-------|
| DATE: | Oct 3 13:16:28 1993 | PORT: |
|-------|---------------------|-------|

TABLE 2.0

|          |          |
|----------|----------|
| 7.0402AA | 7.0402AB |
|----------|----------|

TABLE 2.0

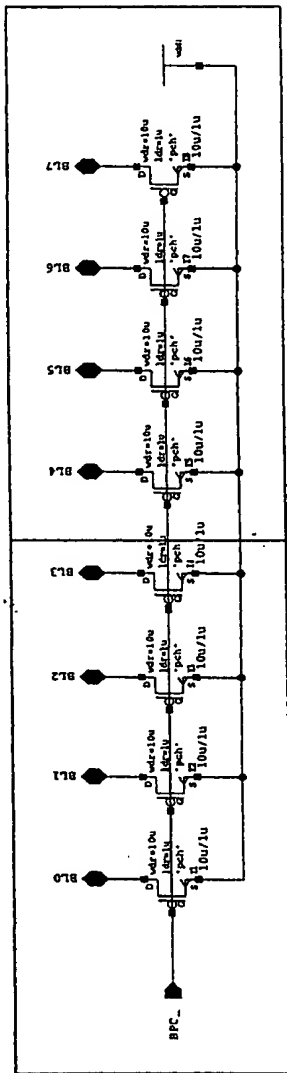


Fig. 7.0902

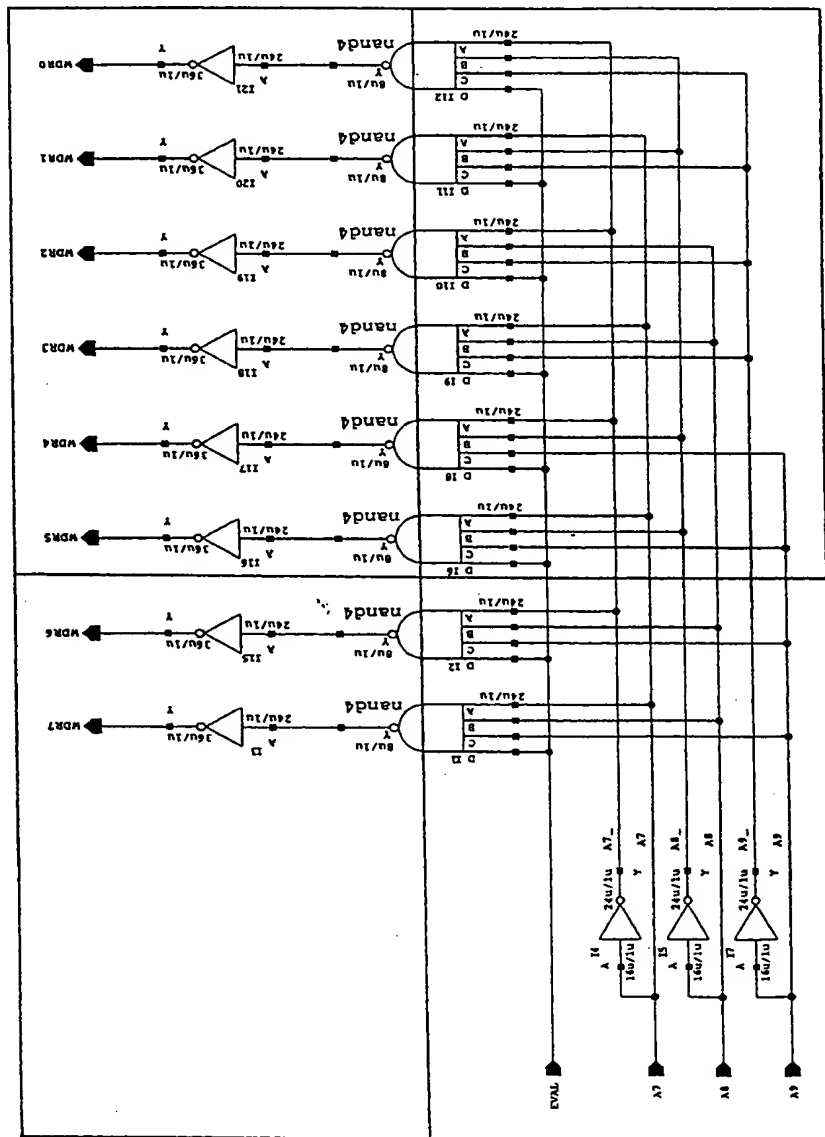
|                           |  |                               |                   |
|---------------------------|--|-------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                  | DESIGNER: Rotzoll |
| COMMUNICATIONS, INC.      |  | TITLE: ROM Bit Line Precharge |                   |
| INTEGRATED CIRCUIT DESIGN |  | NOV: 103reva/rompch           | REV: -            |
| CONFIDENTIAL INFORMATION  |  | DATE: Oct 7 18:09:48 1993     | SHEET: A          |

7.0403AA

7.0403BA

ΕΠΙΦΩΝΙΟΝ

FIG. 7.0903



|                           |  |                             |                |
|---------------------------|--|-----------------------------|----------------|
| MICRON                    |  | PROJECT: L03                | DATE: 10/26/91 |
| COMMUNICATIONS, INC.      |  | TITLE: ROM Word Line Driver |                |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/romedr        | REV: -         |
| CONFIDENTIAL INFORMATION  |  | DATE: Oct 7 18:11:34 1993   | SHEET: A       |

7.0404BA 7.0404BB 7.0404BC

|          |          |          |
|----------|----------|----------|
|          | 7.0404AB | 7.0404AC |
| 7.0404BA | 7.0404BB | 7.0404BC |
|          | 7.0404CB | 7.0404CC |
|          | 7.0404DB | 7.0404DC |

7.0404 7.0404

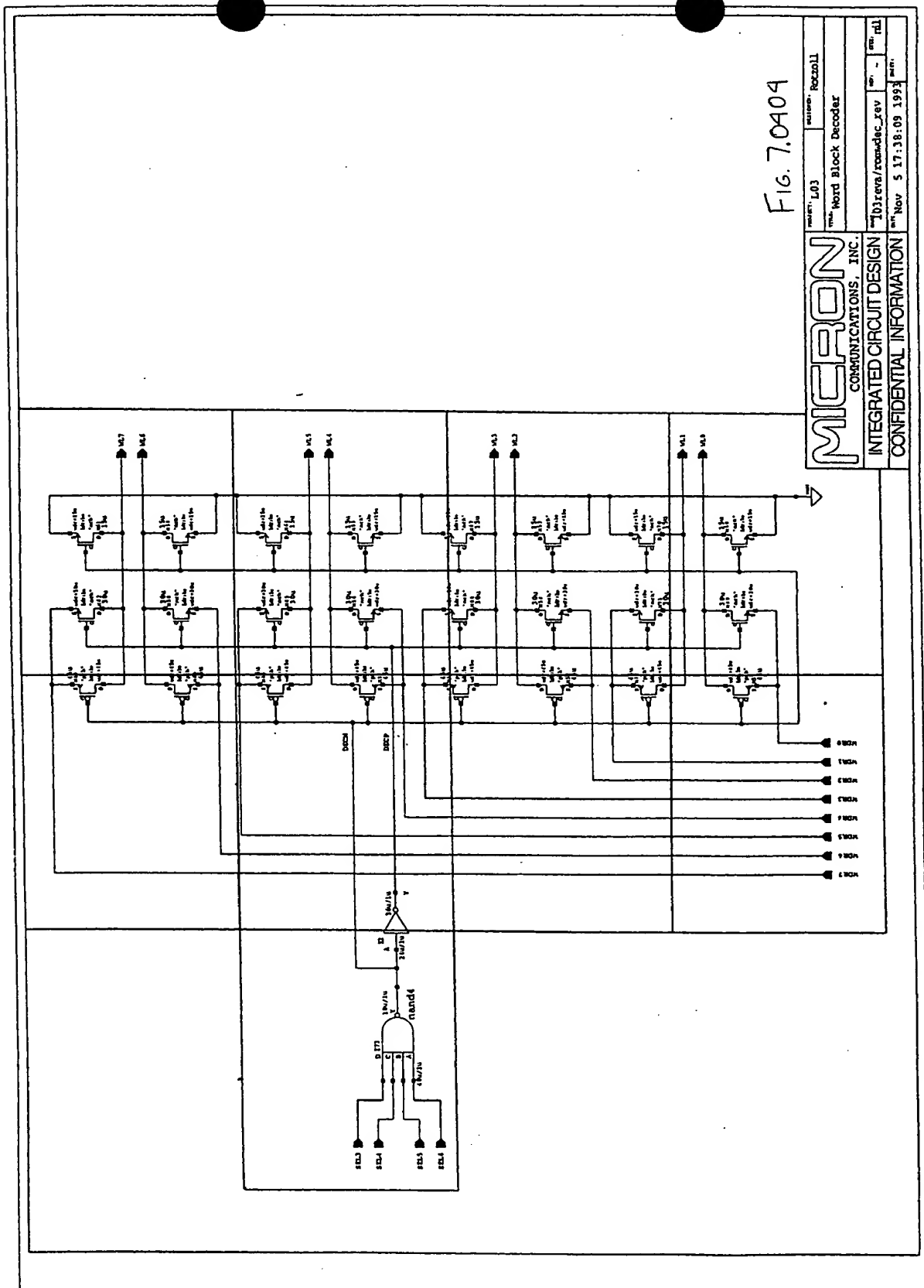


Fig. 7.0404

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|                          |                   |
|--------------------------|-------------------|
| DESIGN: L03              | DESIGNER: R022011 |
| NAME: Word Block Decoder |                   |
| DATE: 10/17/93           | REV: 1            |
| FILE: 10/17/93           | REV: 1            |
| DATE: 5/17/93            | REV: 1            |

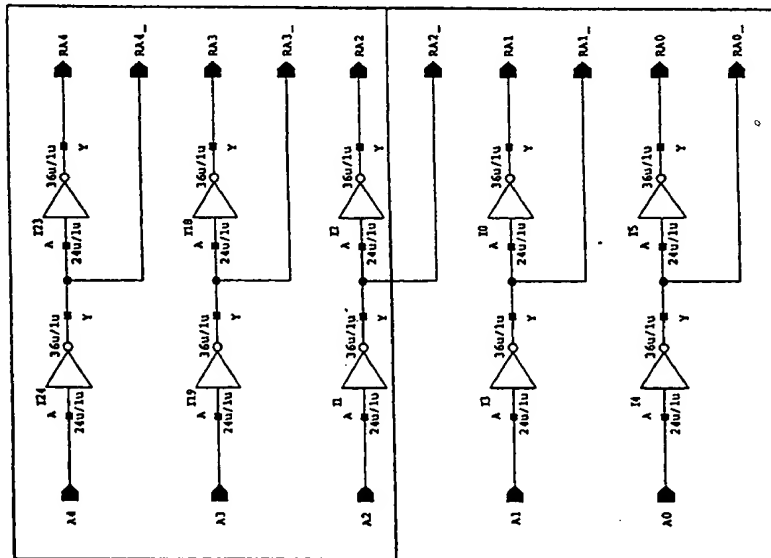


7.0405AA

7.0405BA

7.0405

7.0405AA 7.0405BA



**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|                                    |                   |
|------------------------------------|-------------------|
| PROJECT: L03                       | DESIGNER: Rotzoll |
| TITLE: ROM Bit Line Address Driver |                   |
| NAME: 103reva/rombldr              | REV: -            |
| DATE: Oct 7 12:08:42 1993          | SHEET: A          |

FIG. 7.0405

|          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0406AA | 7.0406AB | 7.0406AC | 7.0406AD | 7.0406AE | 7.0406AF | 7.0406AG | 7.0406AH | 7.0406AI | 7.0406AJ |          |
| 7.0406BA | 7.0406BB | 7.0406BC | 7.0406BD | 7.0406BE | 7.0406BF | 7.0406BG | 7.0406BH | 7.0406BI | 7.0406BJ | 7.0406BK |
| 7.0406CA | 7.0406CB | 7.0406CC | 7.0406CD | 7.0406CE | 7.0406CF | 7.0406CG | 7.0406CH | 7.0406CI | 7.0406CJ | 7.0406CK |



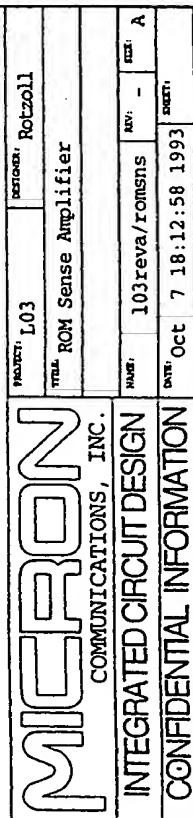
|          |          |
|----------|----------|
| 7.0407AA | 7.0407AB |
|----------|----------|

7.0407AA

II. III. IV. V. VI. VII. VIII. IX. X. XI. XII.

[illegible]

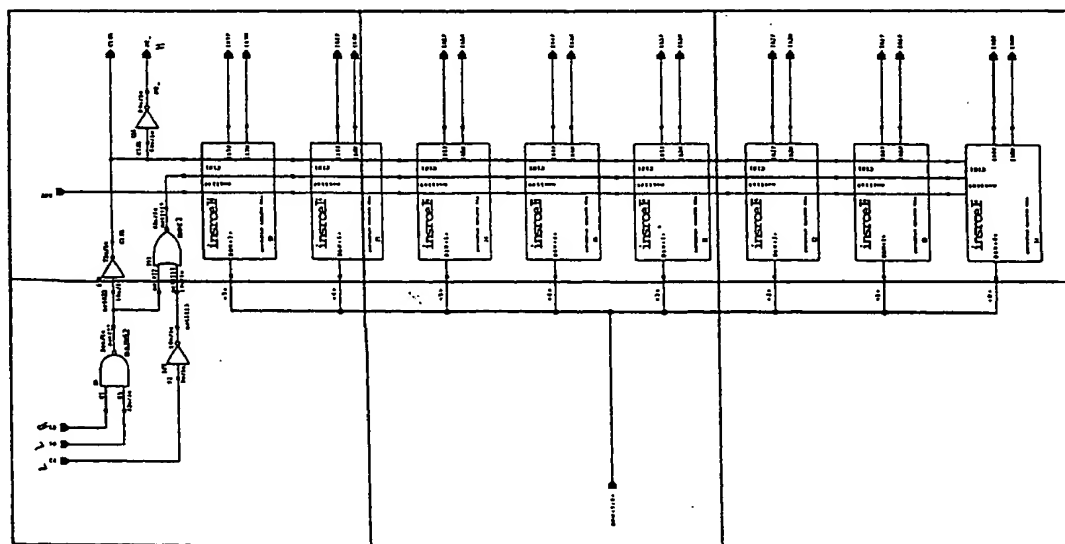
FIG. 7.0407



|        |        |
|--------|--------|
| 7.05AA | 7.05AB |
| 7.05BA | 7.05BB |
| 7.05CA | 7.05CB |

1107 1.05

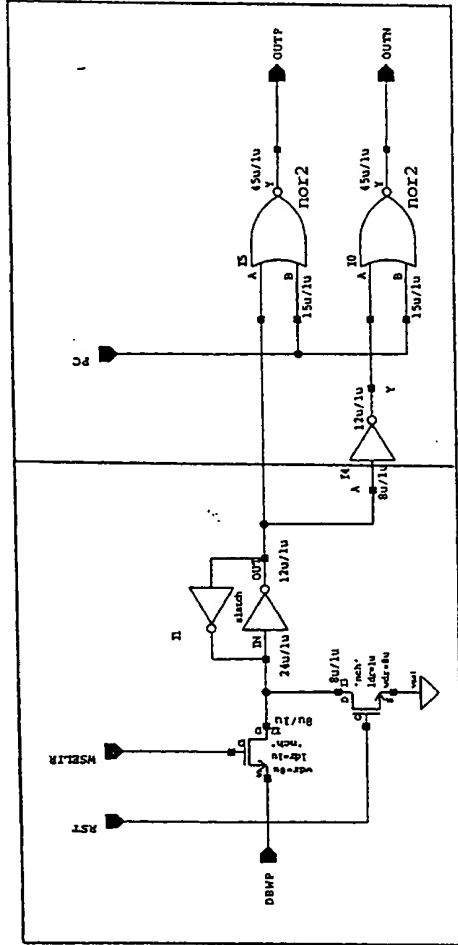
FIG. 7.05



|                              |  |  |  |  |  |
|------------------------------|--|--|--|--|--|
| <b>MICRON</b>                |  |  |  |  |  |
| COMMUNICATIONS, INC.         |  |  |  |  |  |
| INTEGRATED CIRCUIT DESIGN    |  |  |  |  |  |
| CONFIDENTIAL INFORMATION     |  |  |  |  |  |
| DATE Oct. 1, 1970            |  |  |  |  |  |
| TO Mr. J. E. Sullivan        |  |  |  |  |  |
| FROM Mr. J. E. Sullivan      |  |  |  |  |  |
| SUBJECT Instruction Register |  |  |  |  |  |
| PAGE 1 of 1                  |  |  |  |  |  |







|                                 |  |                           |  |
|---------------------------------|--|---------------------------|--|
| PROJECT: L03                    |  | DESIGNER: Rotzoll         |  |
| NAME: Instruction Register Cell |  | REV: -                    |  |
| DATE: Oct 5 20:12:49 1993       |  | REV: A                    |  |
| MICRON                          |  | INTEGRATED CIRCUIT DESIGN |  |
| COMMUNICATIONS, INC.            |  | CONFIDENTIAL INFORMATION  |  |

Fig. 7.0501

|        |        |        |        |        |        |        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|
| 7.064A | 7.06AB | 7.06AC | 7.06AD | 7.06AE | 7.06AF | 7.06AG | 7.06AH | 7.06AI | 7.06AJ | 7.06AK | 7.06AL | 7.06AM | 7.06AN |
|        | 7.06BB | 7.06BC | 7.06BD | 7.06BE | 7.06BF | 7.06BG | 7.06BH | 7.06BI | 7.06BJ | 7.06BK | 7.06BL | 7.06BM | 7.06BN |
| 7.06CA | 7.06CB | 7.06CC | 7.06CD | 7.06CE | 7.06CF | 7.06CG | 7.06CH | 7.06CI | 7.06CJ | 7.06CK | 7.06CL | 7.06CM | 7.06CN |

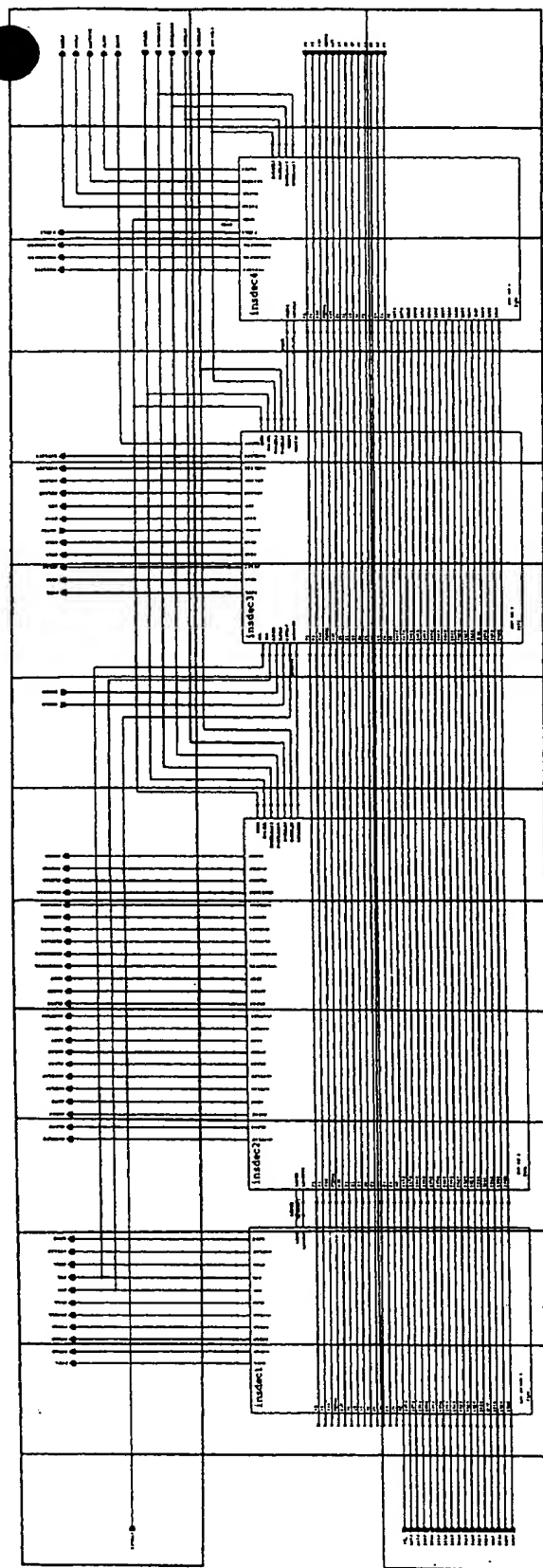
[illegible]

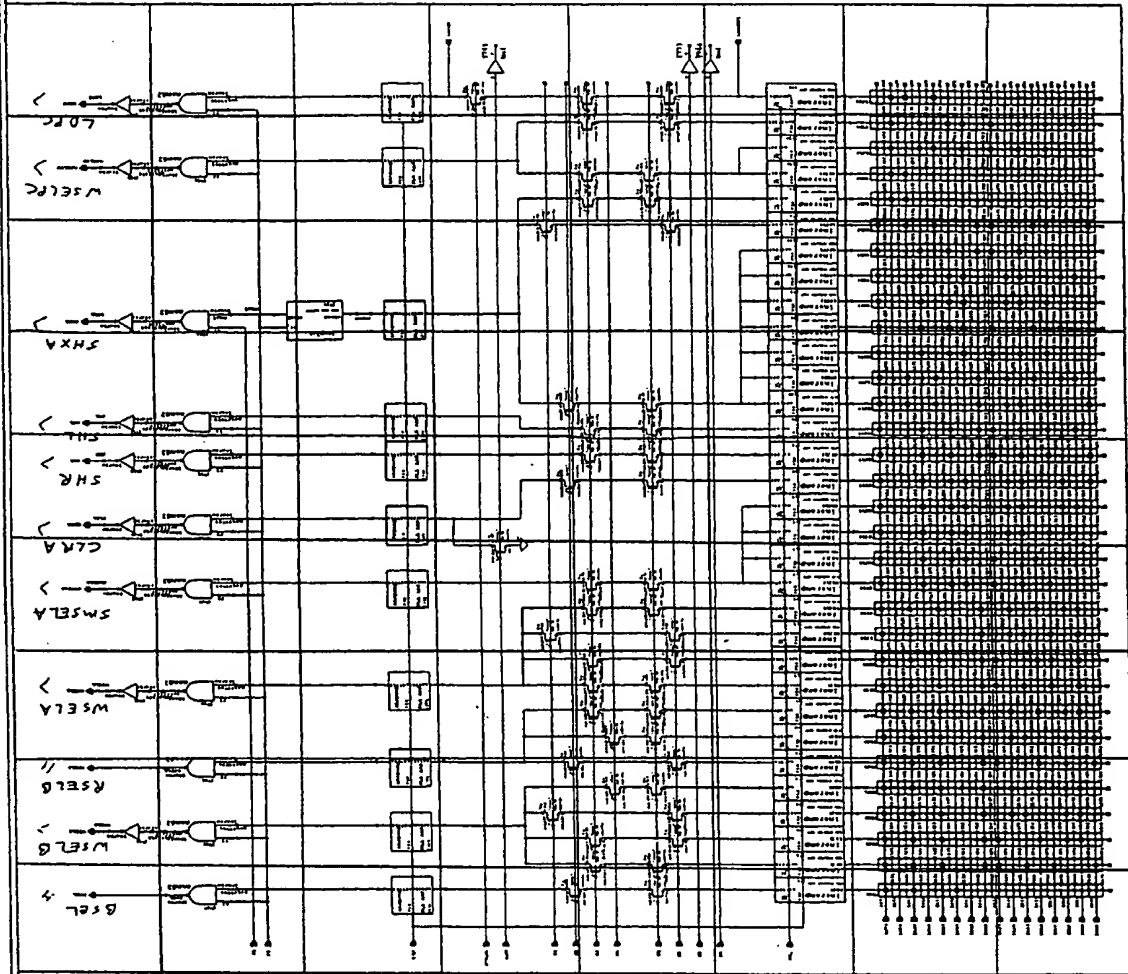
FIG. 7.06

7.0601 7.0601 7.0601

|          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0601AA | 7.0601AB | 7.0601AC | 7.0601AD | 7.0601AE | 7.0601AF | 7.0601AG | 7.0601AH | 7.0601AI |
| 7.0601BA | 7.0601BB | 7.0601BC | 7.0601BD | 7.0601BE | 7.0601BF | 7.0601BG | 7.0601BH | 7.0601BI |
| 7.0601CA | 7.0601CB | 7.0601CC | 7.0601CD | 7.0601CE | 7.0601CF | 7.0601CG | 7.0601CH | 7.0601CI |
| 7.0601DA | 7.0601DB | 7.0601DC | 7.0601DD | 7.0601DE | 7.0601DF | 7.0601DG | 7.0601DH | 7.0601DI |
| 7.0601EA | 7.0601EB | 7.0601EC | 7.0601ED | 7.0601EE | 7.0601EF | 7.0601EG | 7.0601EH | 7.0601EI |
| 7.0601FA | 7.0601FB | 7.0601FC | 7.0601FD | 7.0601FE | 7.0601FF | 7.0601FG | 7.0601FH | 7.0601FI |
| 7.0601GA | 7.0601GB | 7.0601GC | 7.0601GD | 7.0601GE | 7.0601GF | 7.0601GG | 7.0601GH | 7.0601GI |
| 7.0601HA | 7.0601HB | 7.0601HC | 7.0601HD | 7.0601HE | 7.0601HF | 7.0601HG | 7.0601HH | 7.0601HI |

7.0601

FIG. 7.0601



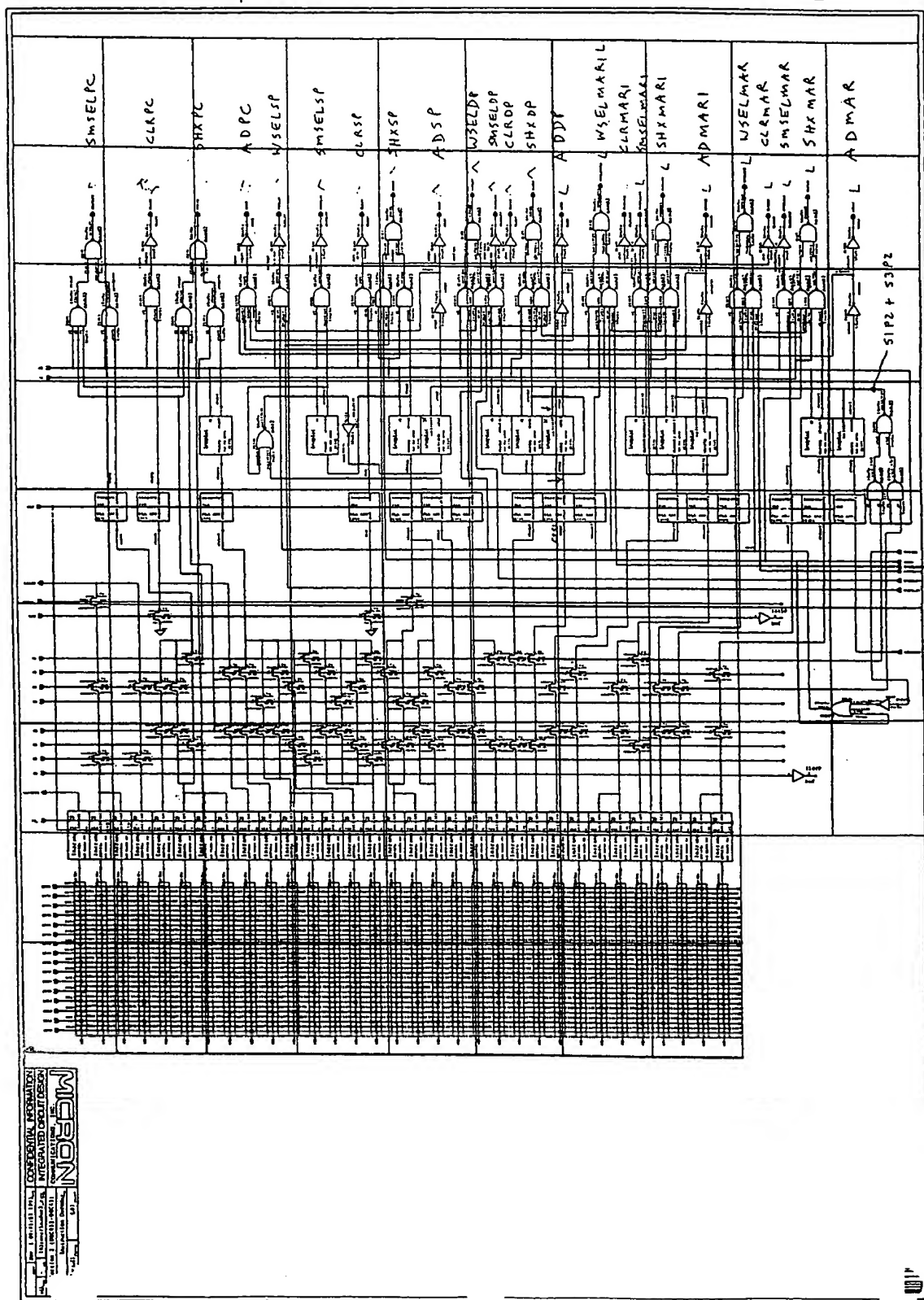
|                           |                        |        |
|---------------------------|------------------------|--------|
| <b>MICRON</b>             | Lab                    | Serial |
| COMMUNICATIONS, INC.      | 2500 West 10th Avenue  |        |
| INTEGRATED CIRCUIT DESIGN | Seattle, WA 98148-7421 |        |
| CONFIDENTIAL INFORMATION  | 100                    | 100    |
|                           | Page                   | 100    |

7.0602 7.0602 7.0602

|          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0602AA | 7.0602AB | 7.0602AC | 7.0602AD | 7.0602AE | 7.0602AF | 7.0602AG | 7.0602AH |
| 7.0602BA | 7.0602BB | 7.0602BC | 7.0602BD | 7.0602BE | 7.0602BF | 7.0602BG | 7.0602BH |
| 7.0602CA | 7.0602CB | 7.0602CC | 7.0602CD | 7.0602CE | 7.0602CF | 7.0602CG | 7.0602CH |
| 7.0602DA | 7.0602DB | 7.0602DC | 7.0602DD | 7.0602DE | 7.0602DF | 7.0602DG | 7.0602DH |
| 7.0602EA | 7.0602EB | 7.0602EC | 7.0602ED | 7.0602EE | 7.0602EF | 7.0602EG | 7.0602EH |
| 7.0602FA | 7.0602FB | 7.0602FC | 7.0602FD | 7.0602FE | 7.0602FF | 7.0602FG | 7.0602FH |
| 7.0602GA | 7.0602GB | 7.0602GC | 7.0602GD | 7.0602GE | 7.0602GF | 7.0602GG | 7.0602GH |
| 7.0602HA | 7.0602HB | 7.0602HC | 7.0602HD | 7.0602HE | 7.0602HF | 7.0602HG | 7.0602HH |
|          |          | 7.0602IC | 7.0602ID | 7.0602IE | 7.0602IF | 7.0602IG | 7.0602IH |
|          |          | 7.0602JC | 7.0602JD | 7.0602JE | 7.0602JF | 7.0602JG | 7.0602JH |

7.0602

|                           |                         |        |
|---------------------------|-------------------------|--------|
| CONFIDENTIAL INFORMATION  | DATE 1 (MM/YY) 11/11    |        |
| INTEGRATED CIRCUIT DESIGN | 1b) name / number / ID  |        |
| COMMUNICATIONS, INC.      | Version 3 (MM/YY) 06/11 |        |
| <b>Micron</b>             | Instruction Details     |        |
|                           | 1a) ID                  | 1b) ID |



- ADPC is in  
one of these is ADP  
ADSP, ADPC, ADMA  
ADMAN

FIG. 7.0602



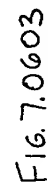
7.0603AA 7.0603AB

|          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0603AA | 7.0603AB | 7.0603AC | 7.0603AD | 7.0603AE | 7.0603AF | 7.0603AH | 7.0603AI | 7.0603AJ |
| 7.0603BA | 7.0603BB | 7.0603BC | 7.0603BD | 7.0603BE | 7.0603BF | 7.0603BG | 7.0603BI | 7.0603BJ |
| 7.0603CA | 7.0603CB | 7.0603CC | 7.0603CD | 7.0603CE | 7.0603CF | 7.0603CG | 7.0603CI | 7.0603CJ |
| 7.0603DA | 7.0603DB | 7.0603DC | 7.0603DD | 7.0603DE | 7.0603DF | 7.0603DG | 7.0603DI | 7.0603DJ |
| 7.0603EA | 7.0603EB | 7.0603EC | 7.0603ED | 7.0603EE | 7.0603EF | 7.0603EG | 7.0603EI | 7.0603EJ |
| 7.0603FA | 7.0603FB | 7.0603FC | 7.0603FD | 7.0603FE | 7.0603FF | 7.0603FG | 7.0603FI | 7.0603FJ |
| 7.0603GA | 7.0603GB | 7.0603GC | 7.0603GD | 7.0603GE | 7.0603GF | 7.0603GG | 7.0603GI | 7.0603GJ |
| 7.0603HA | 7.0603HB | 7.0603HC | 7.0603HD | 7.0603HE | 7.0603HF | 7.0603HG | 7.0603HI | 7.0603HJ |
| 7.0603IA | 7.0603IB | 7.0603IC | 7.0603ID | 7.0603IE | 7.0603IF | 7.0603IG | 7.0603II | 7.0603IJ |
|          |          | 7.0603JC | 7.0603JD | 7.0603JE | 7.0603JF | 7.0603JG | 7.0603JI |          |
|          |          |          |          |          |          |          |          | 7.0603BK |

7.0603 7.0603

[illegible]

CBC clear carry

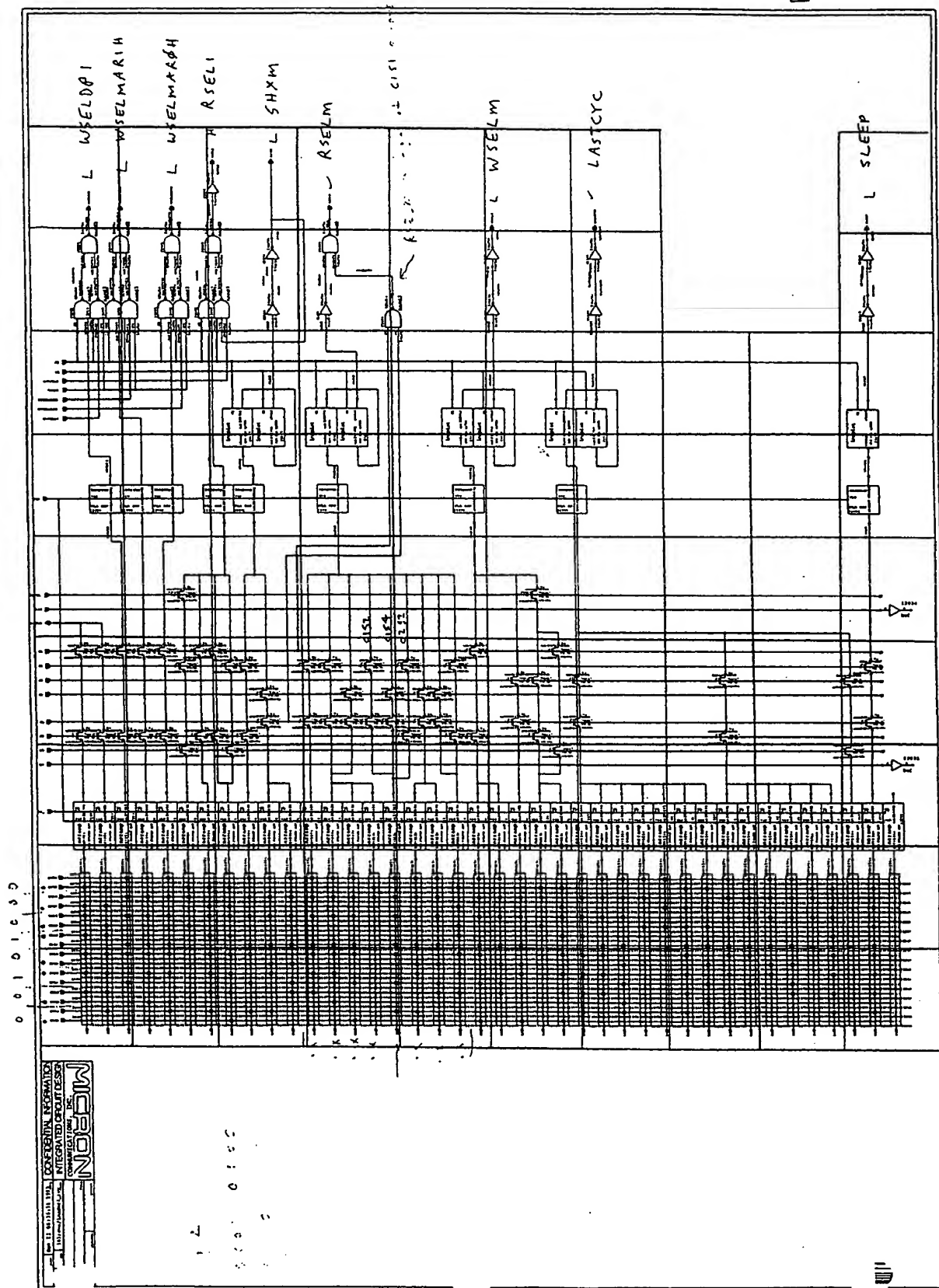


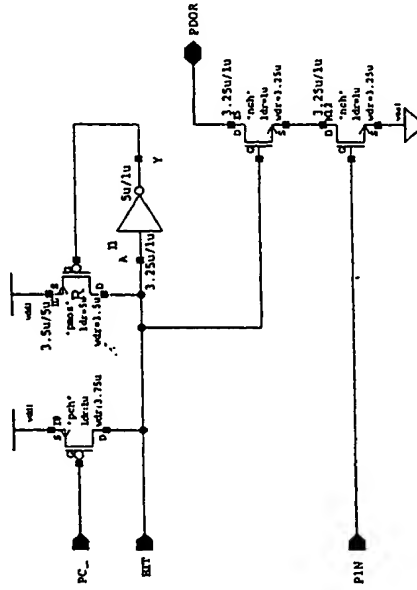
7.0604AA 7.0604AB 7.0604AC 7.0604AD 7.0604AE 7.0604AF 7.0604AG 7.0604AH 7.0604AI

|          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 7.0604AA | 7.0604AB | 7.0604AC | 7.0604AD | 7.0604AE | 7.0604AF | 7.0604AG | 7.0604AH | 7.0604AI |
| 7.0604BA | 7.0604BB | 7.0604BC | 7.0604BD | 7.0604BE | 7.0604BF | 7.0604BG | 7.0604BH | 7.0604BI |
| 7.0604CA | 7.0604CB | 7.0604CC | 7.0604CD | 7.0604CE | 7.0604CF | 7.0604CG | 7.0604CH | 7.0604CI |
| 7.0604DA | 7.0604DB | 7.0604DC | 7.0604DD | 7.0604DE | 7.0604DF | 7.0604DG | 7.0604DH | 7.0604DI |
| 7.0604EA | 7.0604EB | 7.0604EC | 7.0604ED | 7.0604EE | 7.0604EF | 7.0604EG | 7.0604EH | 7.0604EI |
| 7.0604FA | 7.0604FB | 7.0604FC | 7.0604FD | 7.0604FE | 7.0604FF | 7.0604FG | 7.0604FH | 7.0604FI |
| 7.0604GA | 7.0604GB | 7.0604GC | 7.0604GD | 7.0604GE | 7.0604GF | 7.0604GG | 7.0604GH | 7.0604GI |
| 7.0604HA | 7.0604HB | 7.0604HC | 7.0604HD | 7.0604HE | 7.0604HF | 7.0604HG |          |          |
| 7.0604IA | 7.0604IB | 7.0604IC | 7.0604ID | 7.0604IE | 7.0604IF | 7.0604IG |          |          |
| 7.0604JA | 7.0604JB | 7.0604JC | 7.0604JD | 7.0604JE | 7.0604JF | 7.0604JG | 7.0604JH | 7.0604JI |

7.0604JA 7.0604JB 7.0604JC 7.0604JD 7.0604JE 7.0604JF 7.0604JG 7.0604JH 7.0604JI

Fig. 7.06





**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

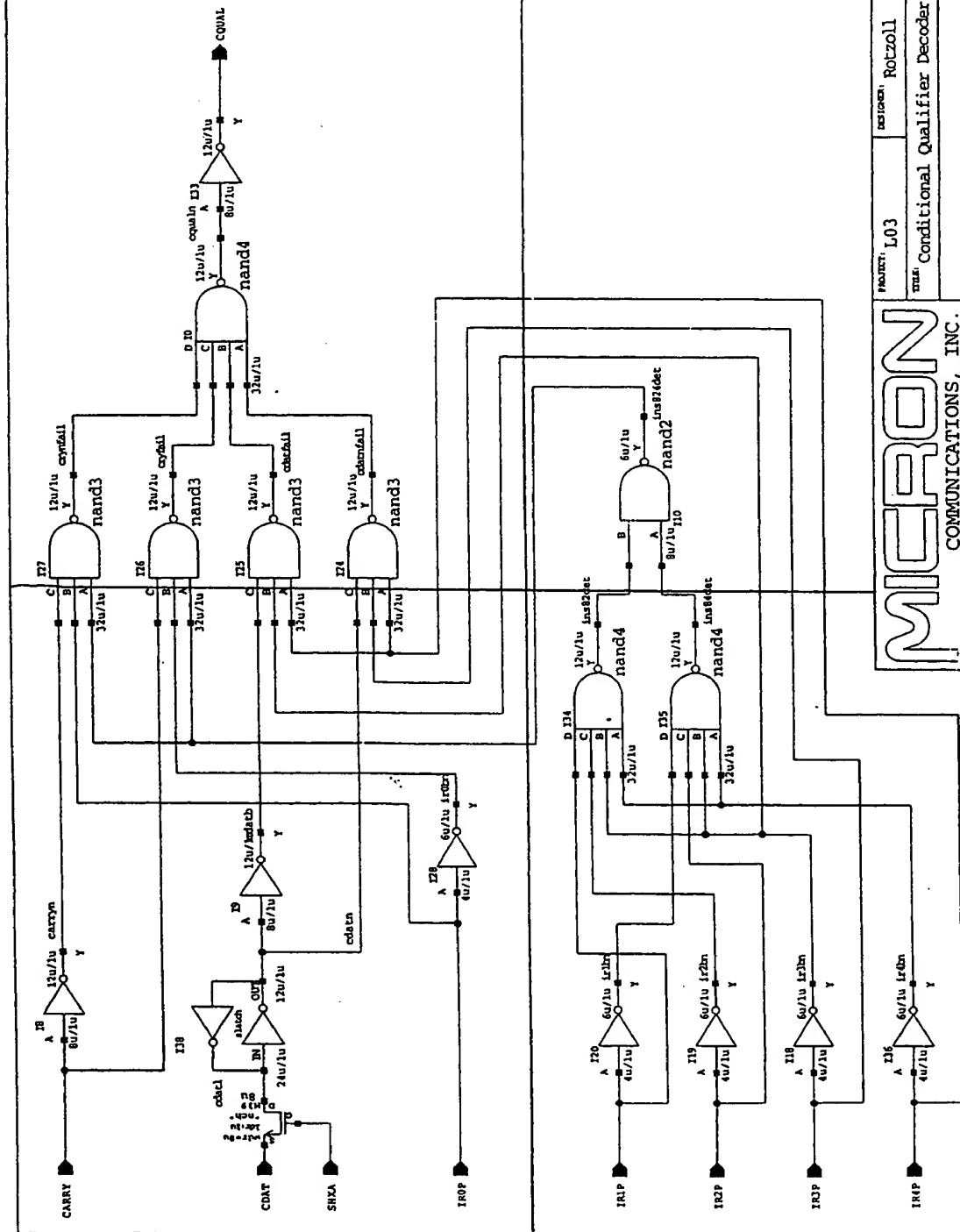
|                                    |                   |
|------------------------------------|-------------------|
| PROJECT: L03                       | DESIGNER: Rotzoll |
| TITLE: Instruction Decoder ROM Amp |                   |
| MODE: 103revs/insramp              | REV: -            |
| DATE: Oct 6 12:20:55 1993          | SHEET: A          |











**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|                                       |                   |
|---------------------------------------|-------------------|
| PROJECT: L03                          | DESIGNER: Rotzoll |
| STATUS: Conditional Qualifier Decoder |                   |
| MADE: 103reva/cqualdec                | REV: - A          |
| DATE: Nov 17 20:09:12 1993            | DESIGN: A         |

7.08AA

7.08BA

7.08CA

7.08

7.08AA 7.08BA 7.08CA

22

|                           |          |                        |         |
|---------------------------|----------|------------------------|---------|
| MICROON                   |          | COMMUNICATIONS, INC.   |         |
| INTEGRATED CIRCUIT DESIGN |          |                        |         |
| CONFIDENTIAL INFORMATION  |          |                        |         |
| FORM NO.                  | LO-3     | REVISED                | RET-311 |
| TYPED                     |          | Databus Latch/Preclear |         |
| DATE                      | 10/1/80  | BY                     | 10/1/80 |
| TIME                      | 14:51:49 | DATE                   | 1993    |

TABLED E5022860

|        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|
| 7.09AA | 7.09AB | 7.09AC | 7.09AD | 7.09AE | 7.09AF |
| 7.09BA | 7.09BB | 7.09BC | 7.09BD | 7.09BE | 7.09BF |

EX 11.09

10020" E3022000

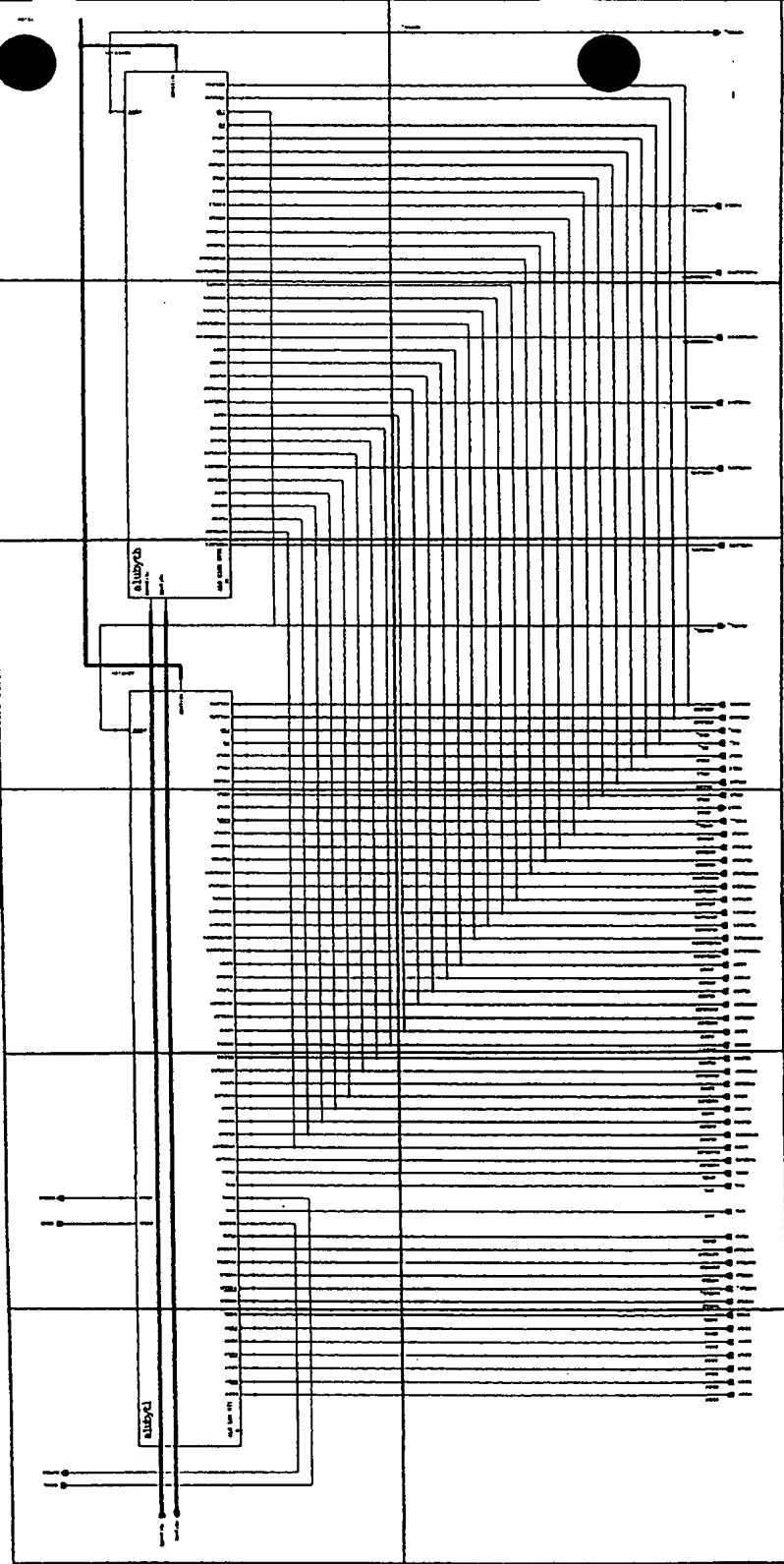


Fig. 7.09

|                          |          |
|--------------------------|----------|
| MICRON                   |          |
| MANUFACTURED BY          | DATE     |
| DESIGNED BY              | REVISION |
| EXPERIMENTAL INFORMATION |          |

"FOCUS" SECTION

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 7.0901AA | 7.0901AB | 7.0901AC | 7.0901AD | 7.0901AE |
| 7.0901BA | 7.0901BB | 7.0901BC | 7.0901BD | 7.0901BE |
| 7.0901CA | 7.0901CB | 7.0901CC | 7.0901CD | 7.0901CE |

IL 11 11 11 11 11

Figure 7.0901

Fig. 7.0901

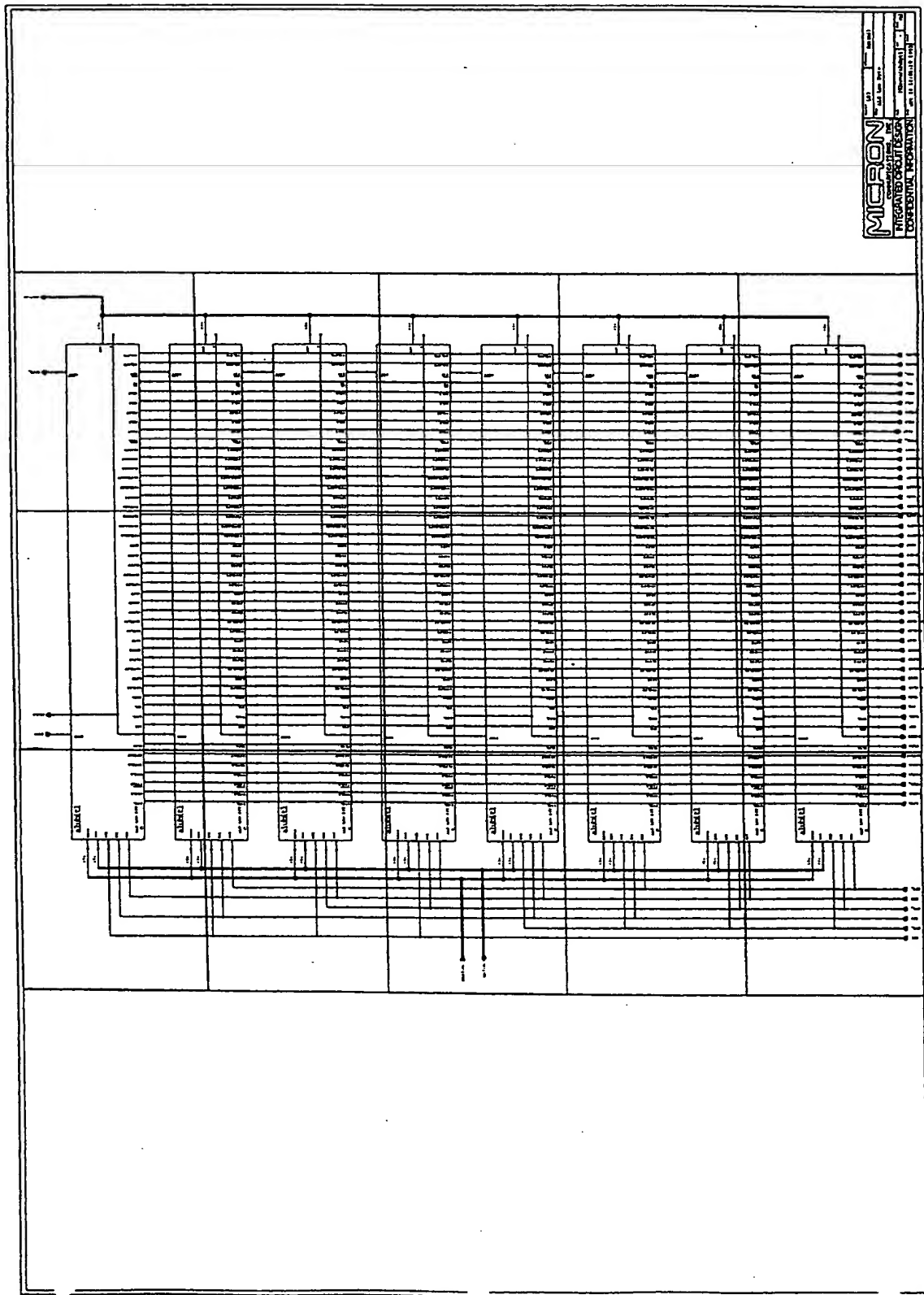


Fig. 7.0901

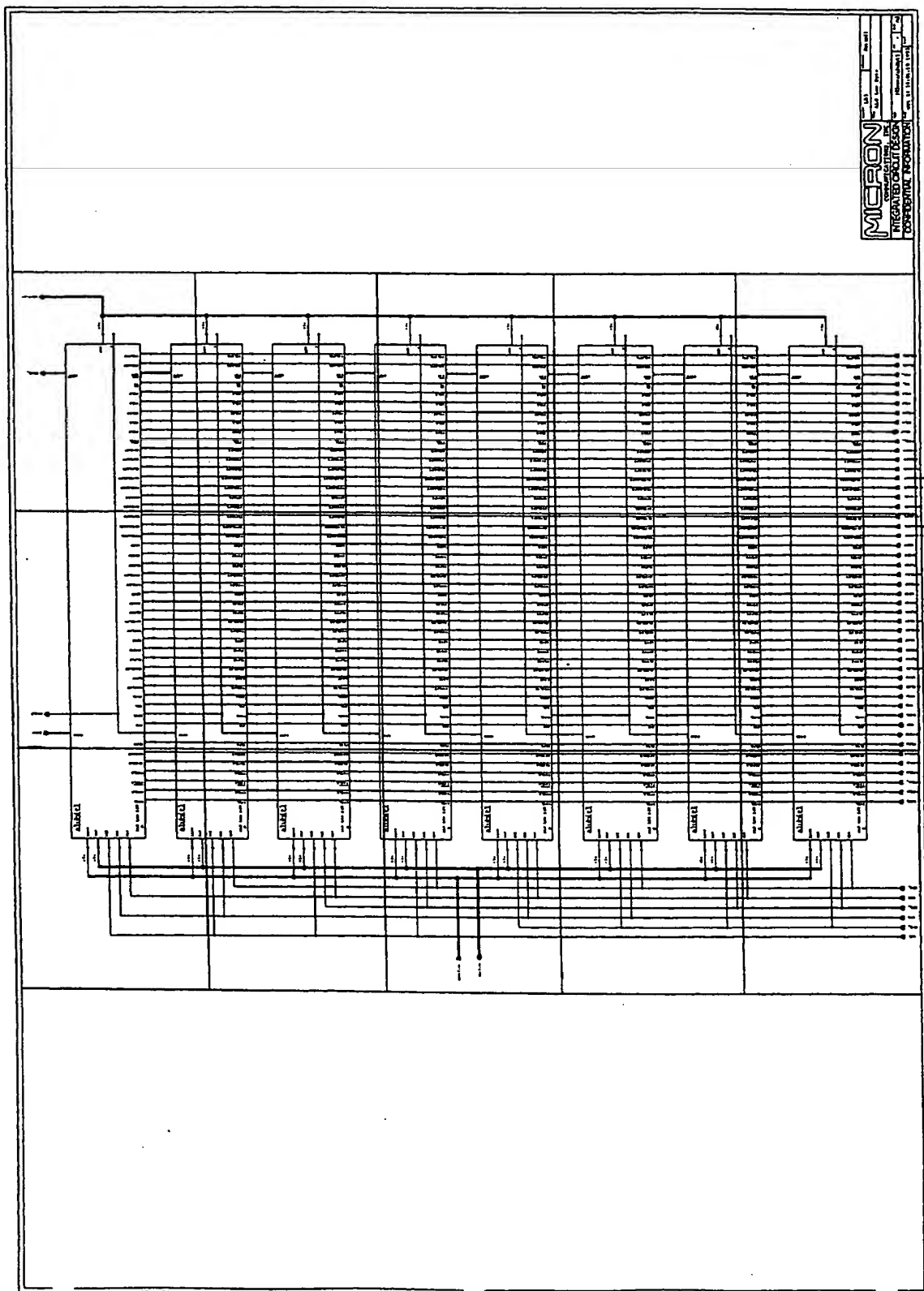




TABLE 2

|            |            |            |            |
|------------|------------|------------|------------|
| 7.090101AA | 7.090101AB | 7.090101AC | 7.090101AD |
|------------|------------|------------|------------|

7.090101

FIG. 7.090101

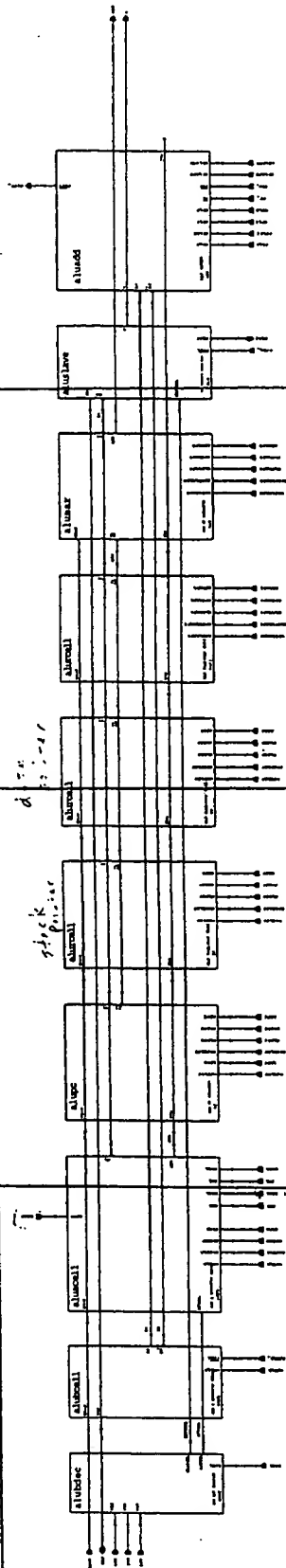
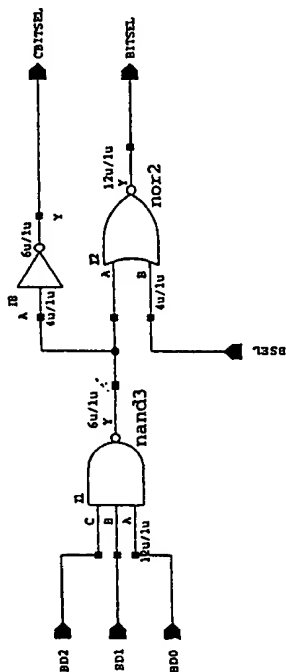


FIG. 7.090101

|               |          |
|---------------|----------|
| MICRON        |          |
| COMPONENT NO. | 7.090101 |
| DATE          | 10/1/68  |
| BY            | J. J. J. |
| FOR           | 10/1/68  |

FIG. 7.09010101

FIG. 7.09010101



|                             |                      |                   |   |
|-----------------------------|----------------------|-------------------|---|
| PROJECT: L03                |                      | DESIGNER: Rotzoll |   |
| TITLE: ALU Bit Decoder Cell |                      |                   |   |
| NAME:                       | 103reva/alubdec      | REV:              | A |
| DATE:                       | Sep 29 16:07:43 1993 | SHEET:            |   |

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION



TABLE "C" 10/20/60

|              |              |
|--------------|--------------|
| 7.09010103AA | 7.09010103AB |
|--------------|--------------|

10/20/60 10/20/60

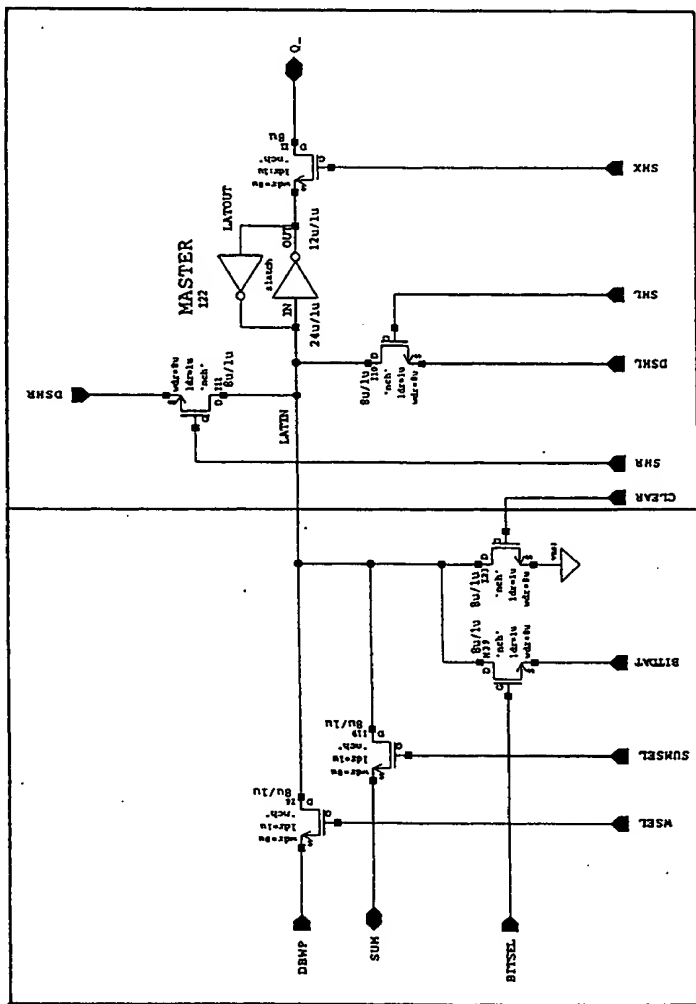


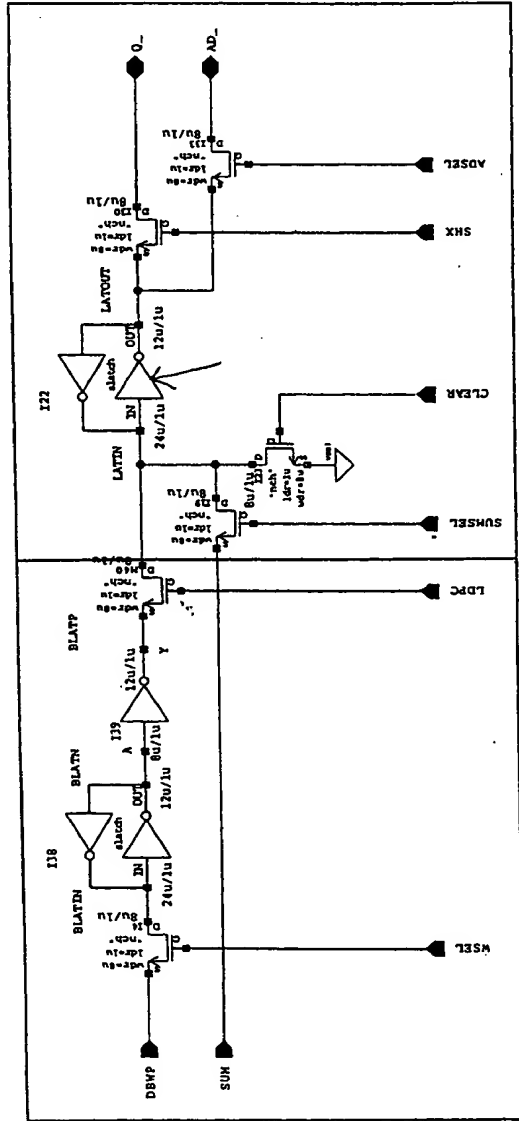
Fig. 7.09010103

|                           |  |                            |  |                   |  |
|---------------------------|--|----------------------------|--|-------------------|--|
| MICRON                    |  | PROJECT: L03               |  | DESIGNER: Rotzoll |  |
|                           |  | TITLE: ALU A Register Cell |  |                   |  |
| COMMUNICATIONS, INC.      |  |                            |  |                   |  |
| INTEGRATED CIRCUIT DESIGN |  | MAJOR: 103reva/aluacell    |  | REV: -            |  |
| CONFIDENTIAL INFORMATION  |  | DATE: Oct 1 15:41:37 1993  |  | SHEET: A          |  |

7.09010104AA

|              |              |
|--------------|--------------|
| 7.09010104AA | 7.09010104AB |
|--------------|--------------|

7.09010104



|                           |  |                     |  |                          |  |                   |  |
|---------------------------|--|---------------------|--|--------------------------|--|-------------------|--|
| MICRON                    |  |                     |  | PROJECT: L03             |  | DESIGNER: Rotzoll |  |
| COMMUNICATIONS, INC.      |  |                     |  | TITLE: ALU Register Cell |  |                   |  |
| INTEGRATED CIRCUIT DESIGN |  |                     |  |                          |  |                   |  |
| CONFIDENTIAL INFORMATION  |  |                     |  |                          |  |                   |  |
| NAME:                     |  | 103revA/alupc       |  | REV: -                   |  | EVAL: A           |  |
| DATE:                     |  | Oct 1 15:45:48 1993 |  | PROJECT:                 |  |                   |  |

FIG. 7.09010104



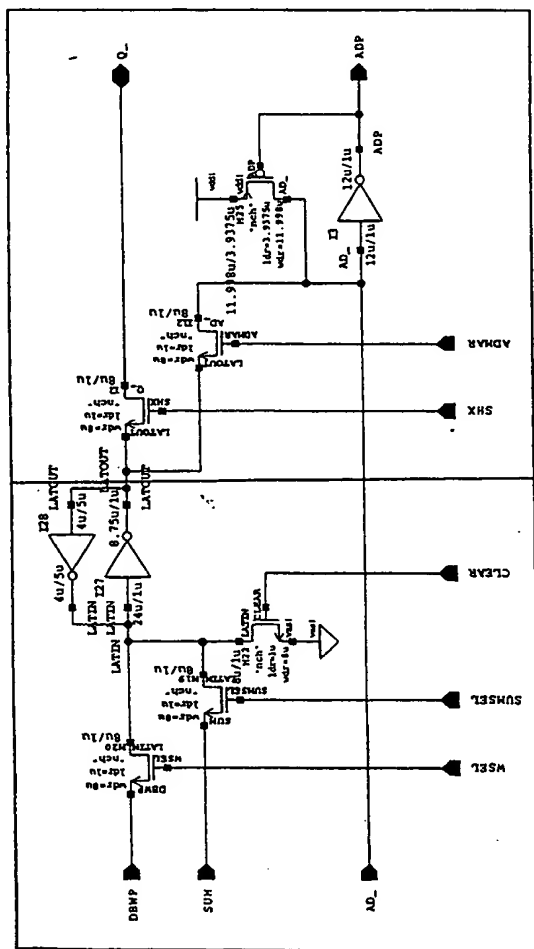


7.09010106AA

|              |              |
|--------------|--------------|
| 7.09010106AA | 7.09010106AB |
|--------------|--------------|

7.09010106AB

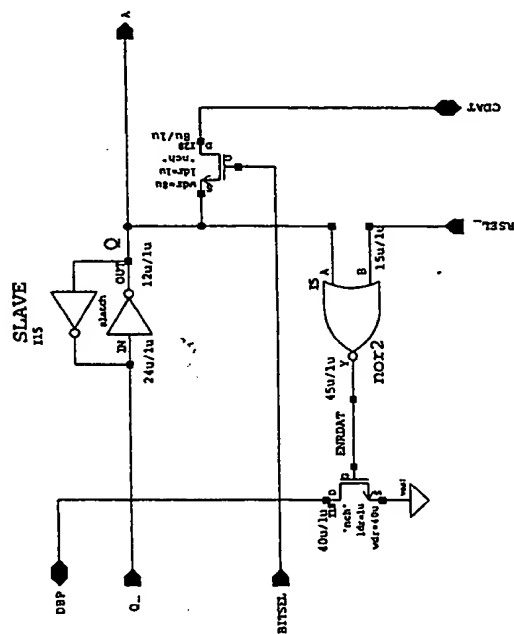
FIG. 7.09010106



|  |  |                         |  |
|--|--|-------------------------|--|
| <b>PROJECT</b> L03                       |  | <b>DESIGNER</b> JOTOOLE |  |
| <b>TITLE</b> ALU Memory Address Register |  |                         |  |
| <b>NAME</b> 103reva/alumar               |  |                         |  |
| <b>REV</b> 'B8                           |  | <b>SIZE</b> A           |  |
| <b>DATE</b> Jan 4 10:27:28 1996          |  | <b>ISHEET:</b>          |  |

**B8: added pch feedback device**

FIG. 7.09010107



**NICRON**  
COMMUNICATIONS, INC.

|                |                      |         |         |
|----------------|----------------------|---------|---------|
| FRONT:         | L03                  | DESIGN: | Rotzoll |
| TITLE          |                      |         |         |
| ALU Slave Cell |                      |         |         |
| NAME:          | 103reva/aluslave     | REV:    | 0       |
| DATE:          | Sep 29 16:09:53 1993 | SIZE:   | A       |
| SHEET:         |                      |         |         |

7.09010108AA

|              |              |              |
|--------------|--------------|--------------|
| 7.09010108AA | 7.09010108AB | 7.09010108AC |
| 7.09010108BA | 7.09010108BB | 7.09010108BC |

7.09010108BB

FIG. 7.09010108

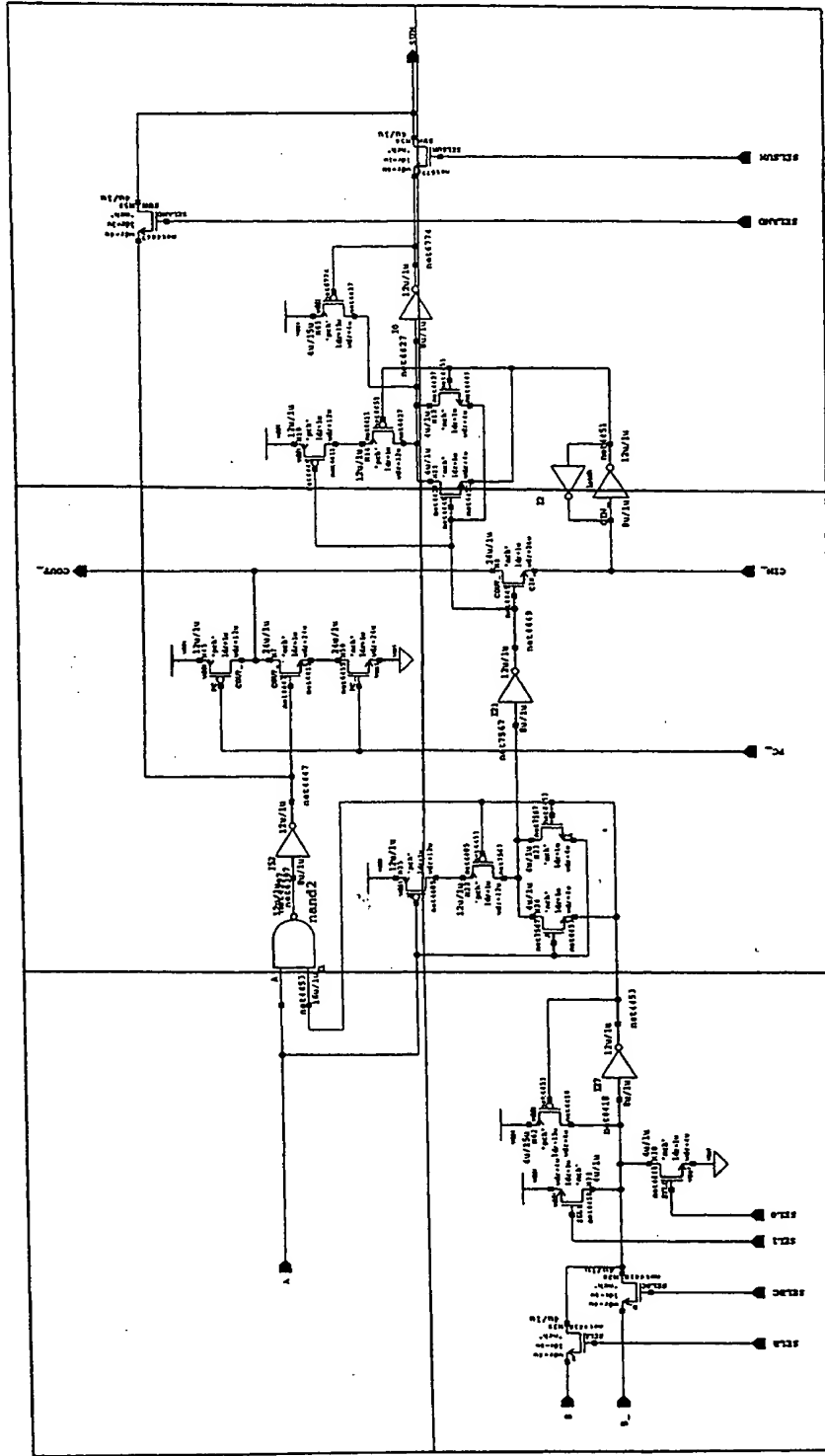


Fig. 7.09010108

85: move feedback device from I21 to I27

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|         |                |      |                      |
|---------|----------------|------|----------------------|
| PROJECT | L03            | DATE | J07000LE             |
| TYPE    | ALU Adder      | REV  | B5                   |
| DESIGN  | 103revs/aluadd | DATE | Sep 16 15:48:21 1995 |
| SIZE    | nl             | BY   | nl                   |

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 7.0902AA | 7.0902AB | 7.0902AC | 7.0902AD | 7.0902BD |
| 7.0902BA | 7.0902BB | 7.0902BC |          |          |





7.090201AA 7.090201AB 7.090201AC

|            |            |            |
|------------|------------|------------|
| 7.090201AA | 7.090201AB | 7.090201AC |
|------------|------------|------------|

7.090201

60660 60660 60660

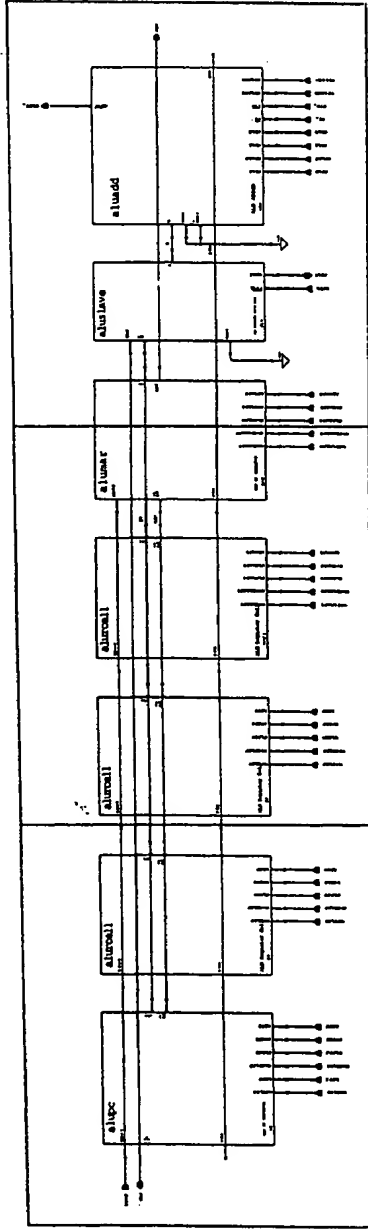
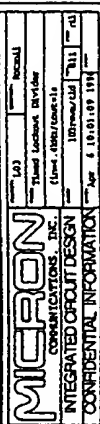
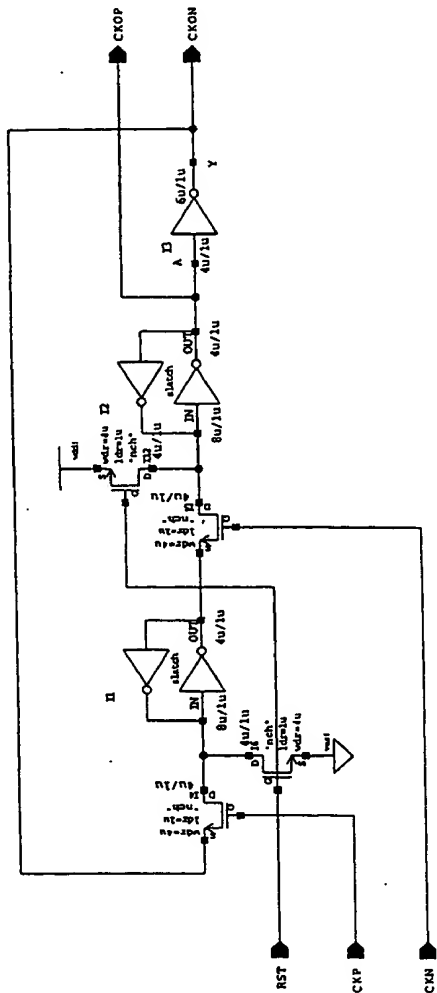


Fig. 7.090201

|        |        |        |
|--------|--------|--------|
| 7.10AA | 7.10AB | 7.10AC |
| 7.10BA | 7.10BB | 7.10BC |
| 7.10CA | 7.10CB | 7.10CC |

И. И. И.





12/29/92

|                           |  |                                  |                   |
|---------------------------|--|----------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                     | DESIGNER: Rotzoll |
| COMMUNICATIONS, INC.      |  | NAME: Timed Lockout Divider Cell | REV: A            |
| INTEGRATED CIRCUIT DESIGN |  | DATE: Sep 22 15:26:56 1994       | SHEET: 1          |
| CONFIDENTIAL INFORMATION  |  |                                  |                   |

Fig. 7.1001

FOUO" E902360

|        |        |
|--------|--------|
| 7.11AA | 7.11AB |
|--------|--------|

Ex 7.11

Rev. 2.3026360

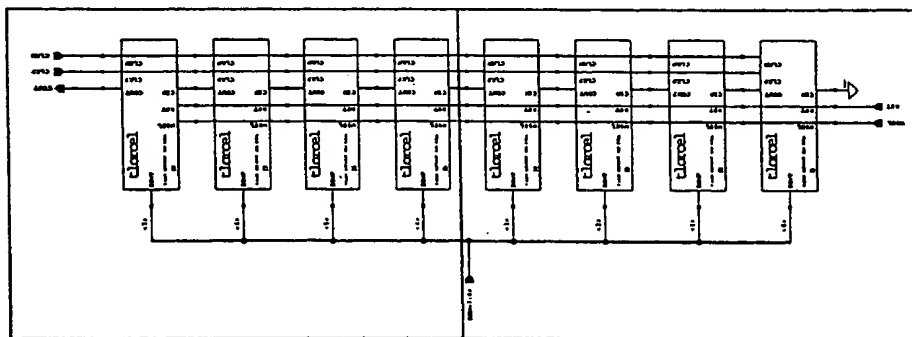


FIG. 7.11

|                           |  |                         |     |     |
|---------------------------|--|-------------------------|-----|-----|
| MICRON                    |  | 101                     | 101 | 101 |
| COMMUNICATIONS, INC.      |  | Titled Lockout Register |     |     |
| INTEGRATED CIRCUIT DESIGN |  | 101 (rev. 1/1989)       |     |     |
| CONFIDENTIAL INFORMATION  |  | Doc. 1.12.05.137.101    |     |     |

7.1101AA 7.1101AB 7.1101AC

|          |          |          |
|----------|----------|----------|
| 7.1101AA | 7.1101AB | 7.1101AC |
|----------|----------|----------|

II II II II II



TOP SECRET

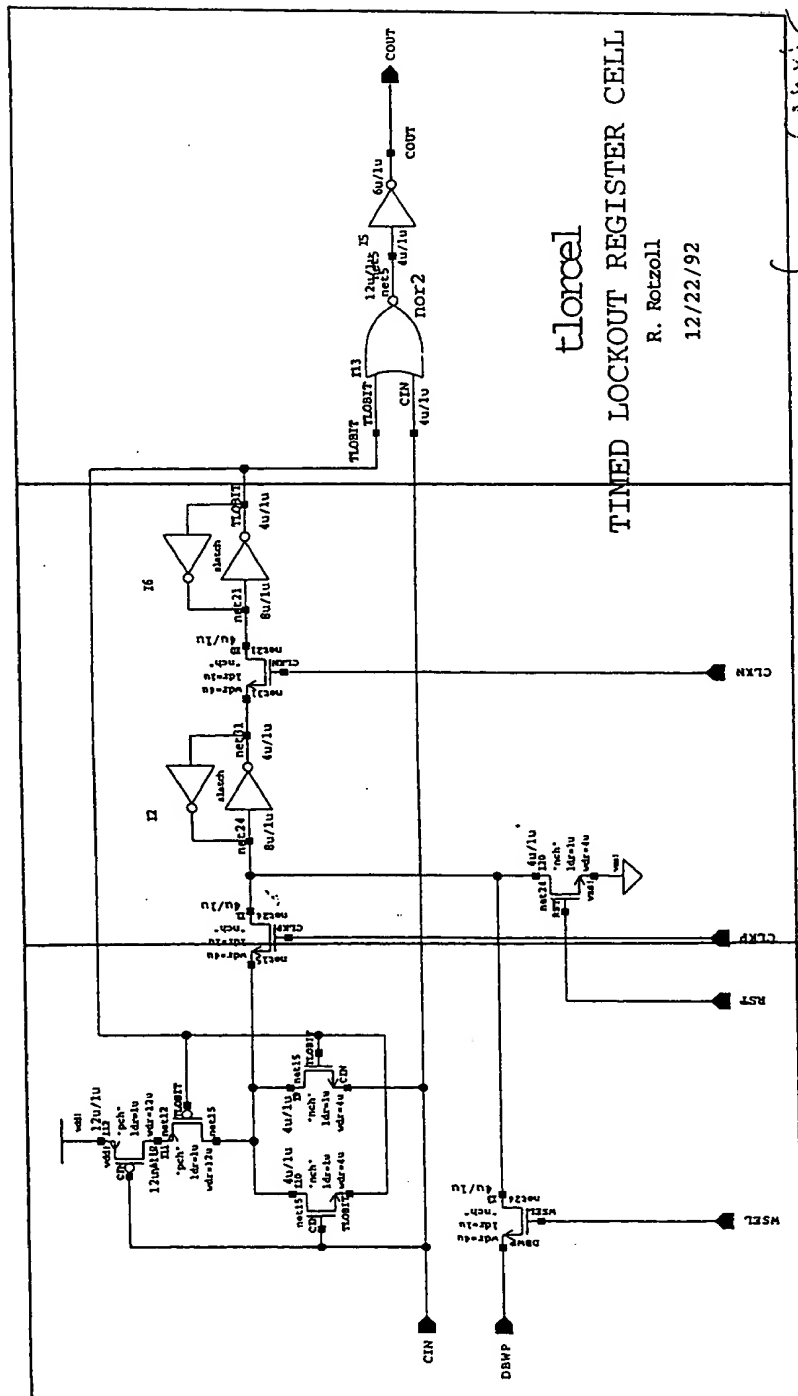


FIG. 7.1101



## 7.12AC

$$\frac{\pi \pi}{\pi. \pi \pi}$$

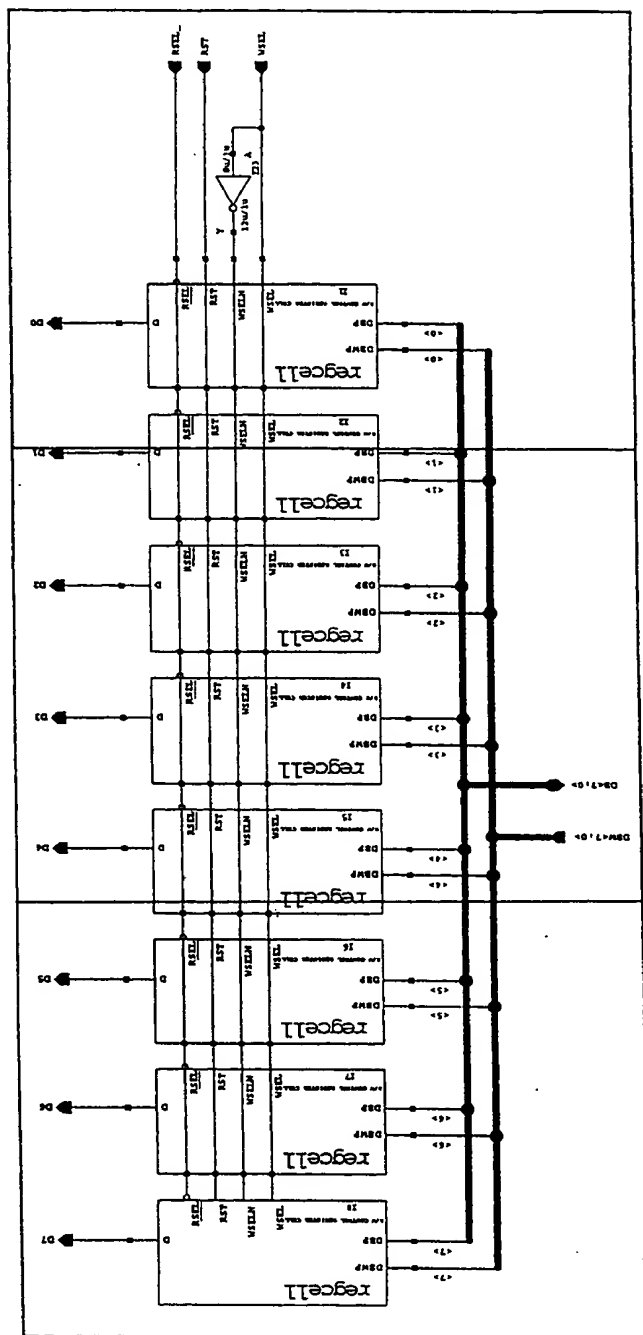
[illegible]

Fig. 7.12

|                           |               |                             |           |          |
|---------------------------|---------------|-----------------------------|-----------|----------|
| <b>MICRON</b>             |               | PROJECT: L03                | REVISION: | RETCOLL  |
| COMMUNICATIONS, INC.      |               | TITLE: R/W Control Register |           |          |
| INTEGRATED CIRCUIT DESIGN |               |                             |           |          |
| PROJECT:                  | 103Ireva/oreg | REV:                        | -         | 1.00 mil |
| CONFIDENTIAL INFORMATION  |               | DATE: Nov 12 09:44:40 1993  |           |          |



7.13AA

*7.13BA*

И. И. У. И. И. И.

[illegible]





R. Rotzoll

12/8/92

FIG. 7.1301

|  |        |  |
|--|--------|--|
|  | 7.14AB |  |
|  | 7.14AA |  |

И. П. И.





|          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|
| 7.1401AA | 7.1401AB | 7.1401AC | 7.1401AD | 7.1401AE | 7.1401AF |
| 7.1401BA | 7.1401BB | 7.1401BC | 7.1401BD | 7.1401BE | 7.1401BF |
| 7.1401CA | 7.1401CB | 7.1401CC | 7.1401CD | 7.1401CE | 7.1401CF |
| 7.1401DA | 7.1401DB | 7.1401DC | 7.1401DD | 7.1401DE | 7.1401DF |
| 7.1401EA | 7.1401EB | 7.1401EC | 7.1401ED | 7.1401EE | 7.1401EF |
| 7.1401FA | 7.1401FB | 7.1401FC | 7.1401FD | 7.1401FE | 7.1401FF |
| 7.1401GA | 7.1401GB | 7.1401GC | 7.1401GD | 7.1401GE | 7.1401GF |

FIG. 7.1401

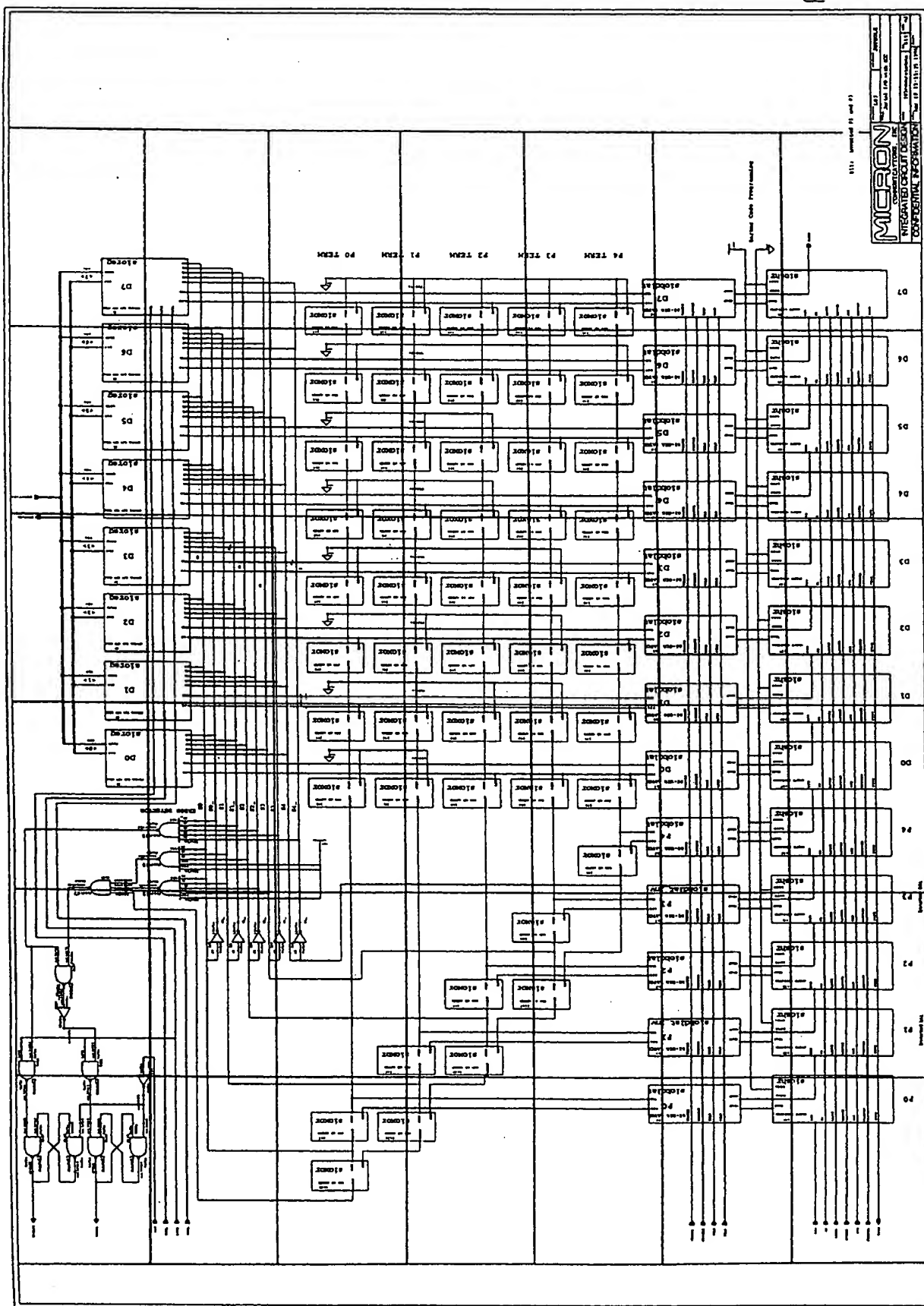
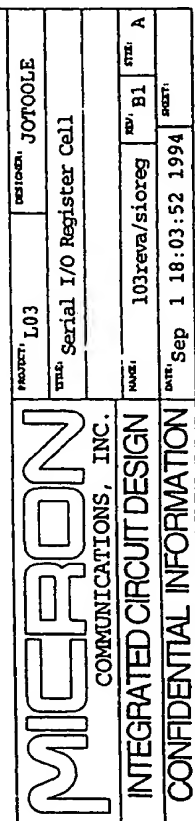




Fig. 7.140101



**victron**  
COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

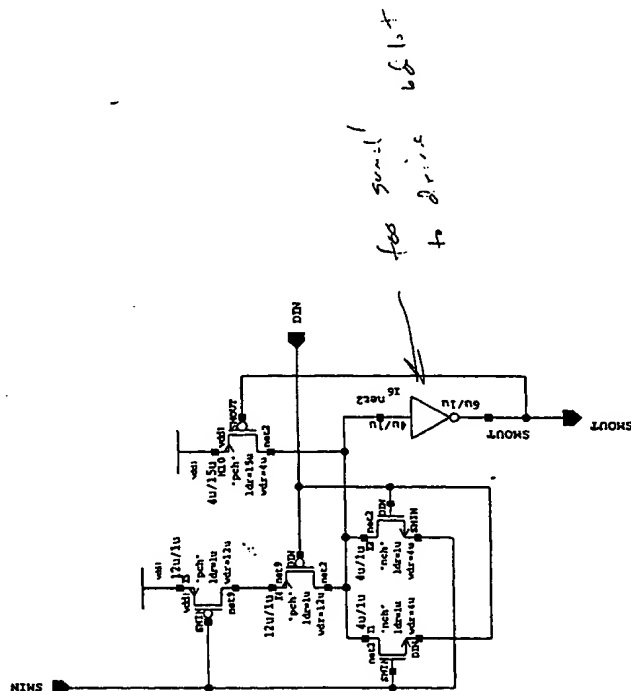
[illegible]

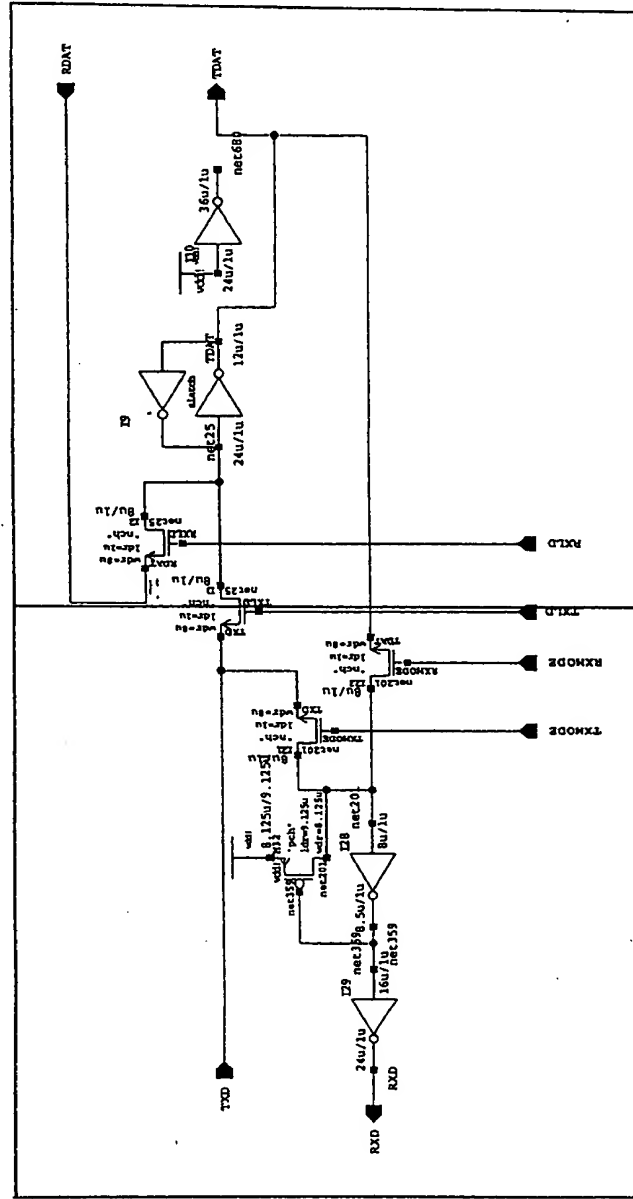
FIG. 7.140102

|                           |  |                           |                   |
|---------------------------|--|---------------------------|-------------------|
| MICRON                    |  | PROJECT: L03              | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: SIO XOR            |                   |
|                           |  |                           |                   |
|                           |  | NAME: 103reva/sioxor      | REV: B1           |
|                           |  | SIZE: A                   |                   |
|                           |  | DATE: Sep 1 18:07:22 1994 |                   |
|                           |  | SHEET:                    |                   |
| INTEGRATED CIRCUIT DESIGN |  |                           |                   |
| CONFIDENTIAL INFORMATION  |  |                           |                   |

7.140103AA

|            |            |
|------------|------------|
| 7.140103AA | 7.140103AB |
|------------|------------|

7.140103AB



|                           |  |                                |                   |
|---------------------------|--|--------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                   | REVISION: J0700LE |
| COMMUNICATIONS, INC.      |  | TITLE: STO Bidirectional Latch |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/sioblat_inv      | REV: B11          |
| CONFIDENTIAL INFORMATION  |  | DATE: Apr 10 15:13:59 1996     | DESIGNER: A       |

B11: inverted bit

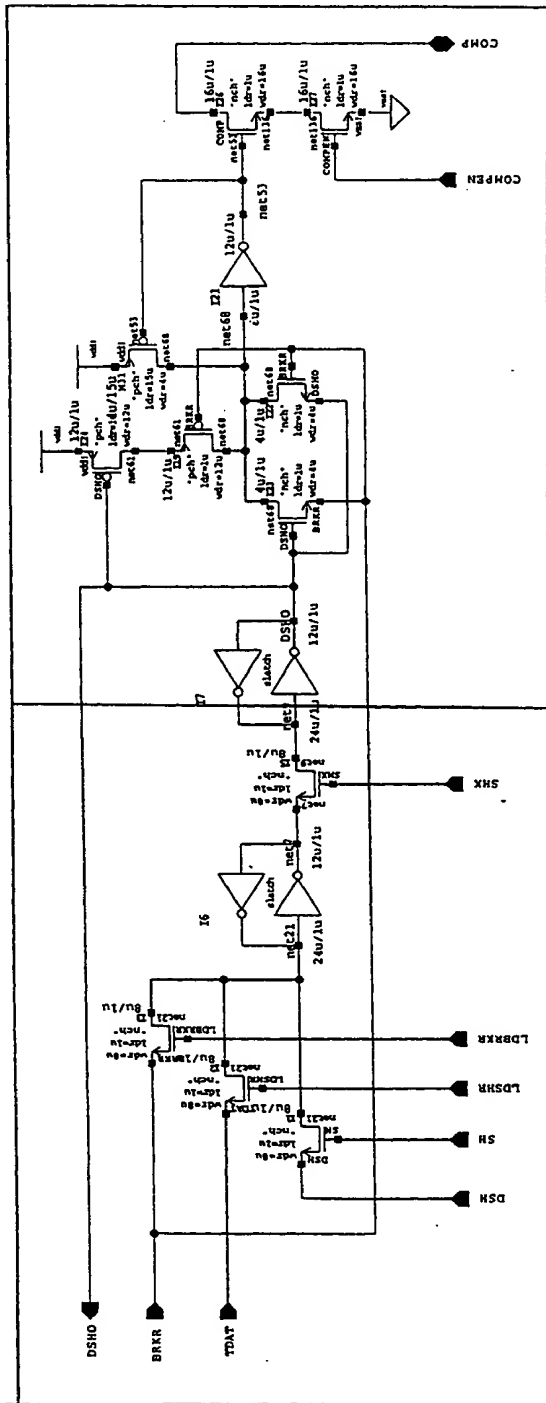


7.140104AA 7.140104AB

|            |            |
|------------|------------|
| 7.140104AA | 7.140104AB |
|------------|------------|

7.140104AA

7140104



7140104

Fig. 7.140104

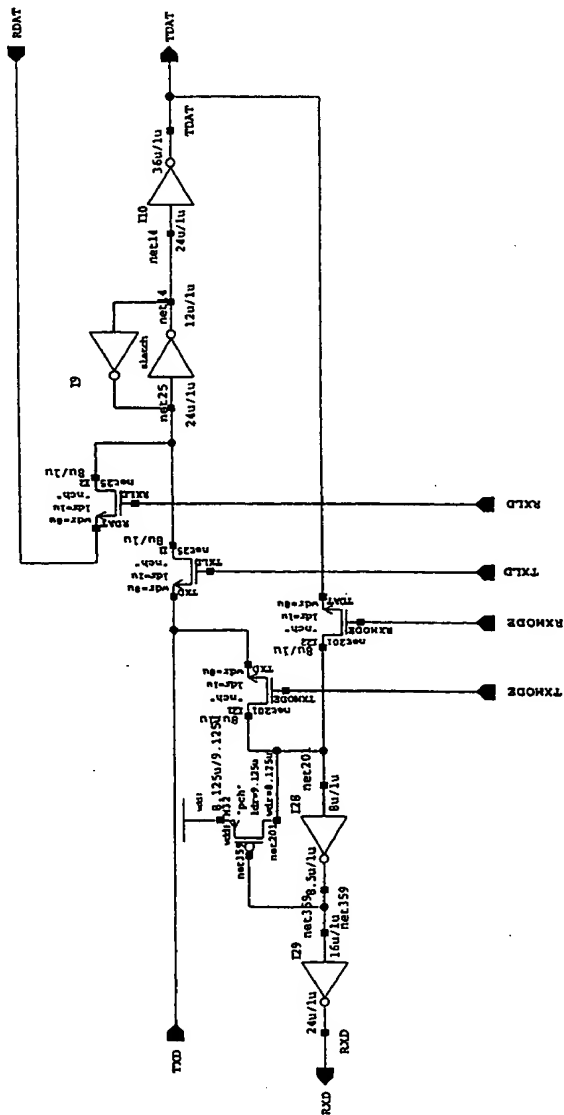
|                           |  |                           |  |                   |  |
|---------------------------|--|---------------------------|--|-------------------|--|
| MICRON                    |  | PROJECT: L03              |  | DESIGNER: JOTOOLE |  |
| COMMUNICATIONS, INC.      |  | TITLE: SIO Shift Register |  |                   |  |
| INTEGRATED CIRCUIT DESIGN |  |                           |  |                   |  |
| CONFIDENTIAL INFORMATION  |  |                           |  |                   |  |
| NAME: 103reva/sioshr      |  | REV: B1                   |  | SHEET: A          |  |
| DATE: Sep 2 08:06:26 1994 |  | SHEET:                    |  |                   |  |

FORM 6000

|            |            |
|------------|------------|
| 7.140105AA | 7.140105AB |
|------------|------------|

EX 7.140105

Fig. 7.140105



**B8: added feedback device**

|                           |  |                                |  |                   |          |
|---------------------------|--|--------------------------------|--|-------------------|----------|
| MICRON                    |  | PROJECT: L03                   |  | DESIGNER: JOTOOLE |          |
| COMMUNICATIONS, INC.      |  | TITLE: SIO Bidirectional Latch |  |                   |          |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/siobdlat         |  | REV: B8           | SHEET: A |
| CONFIDENTIAL INFORMATION  |  | DATE: Jan 8 11:04:57 1996      |  | SHEET:            |          |

|          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|
| 7.1402AC | 7.1402AD | 7.1402AE | 7.1402AF | 7.1402AG | 7.1402AH | 7.1402AI |
| 7.1402BC | 7.1402BD | 7.1402BE | 7.1402BF | 7.1402BG | 7.1402BH | 7.1402BI |
| 7.1402CC | 7.1402CD | 7.1402CE | 7.1402CF | 7.1402CG | 7.1402CH | 7.1402CI |
| 7.1402DC | 7.1402DD | 7.1402DE | 7.1402DF | 7.1402DG | 7.1402DH | 7.1402DI |
| 7.1402EC | 7.1402ED | 7.1402EE | 7.1402EF | 7.1402EG | 7.1402EH | 7.1402EI |

И. И. Ильин

70660" 2902060

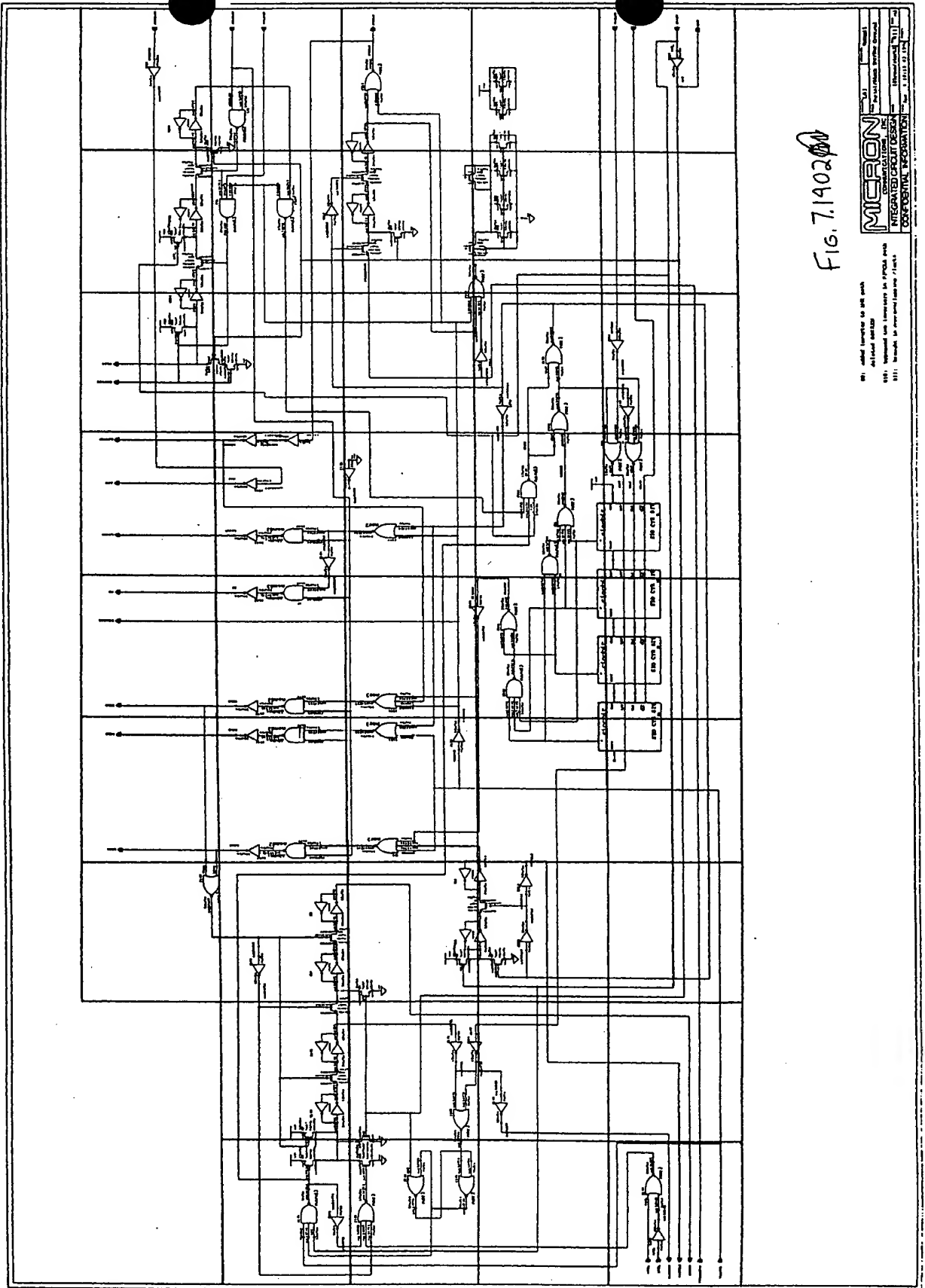


FIG. 7.1402

80. added jumper to 48 pin  
81. added 48 pin  
82. removed pin 10 from 48 pin  
83. removed pin 10 from 48 pin

|                           |  |
|---------------------------|--|
| MICRON                    |  |
| INTEGRATED CIRCUIT DESIGN |  |
| DESIGNATION: 70660        |  |
| DATE: 10/10/70            |  |
| DRAWN BY: J. L. J.        |  |
| CHECKED BY: J. L. J.      |  |
| APPROVED BY: J. L. J.     |  |

TABLE 2-50

|            |            |
|------------|------------|
| 7.140201AA | 7.140201AB |
|------------|------------|

END 7.140201

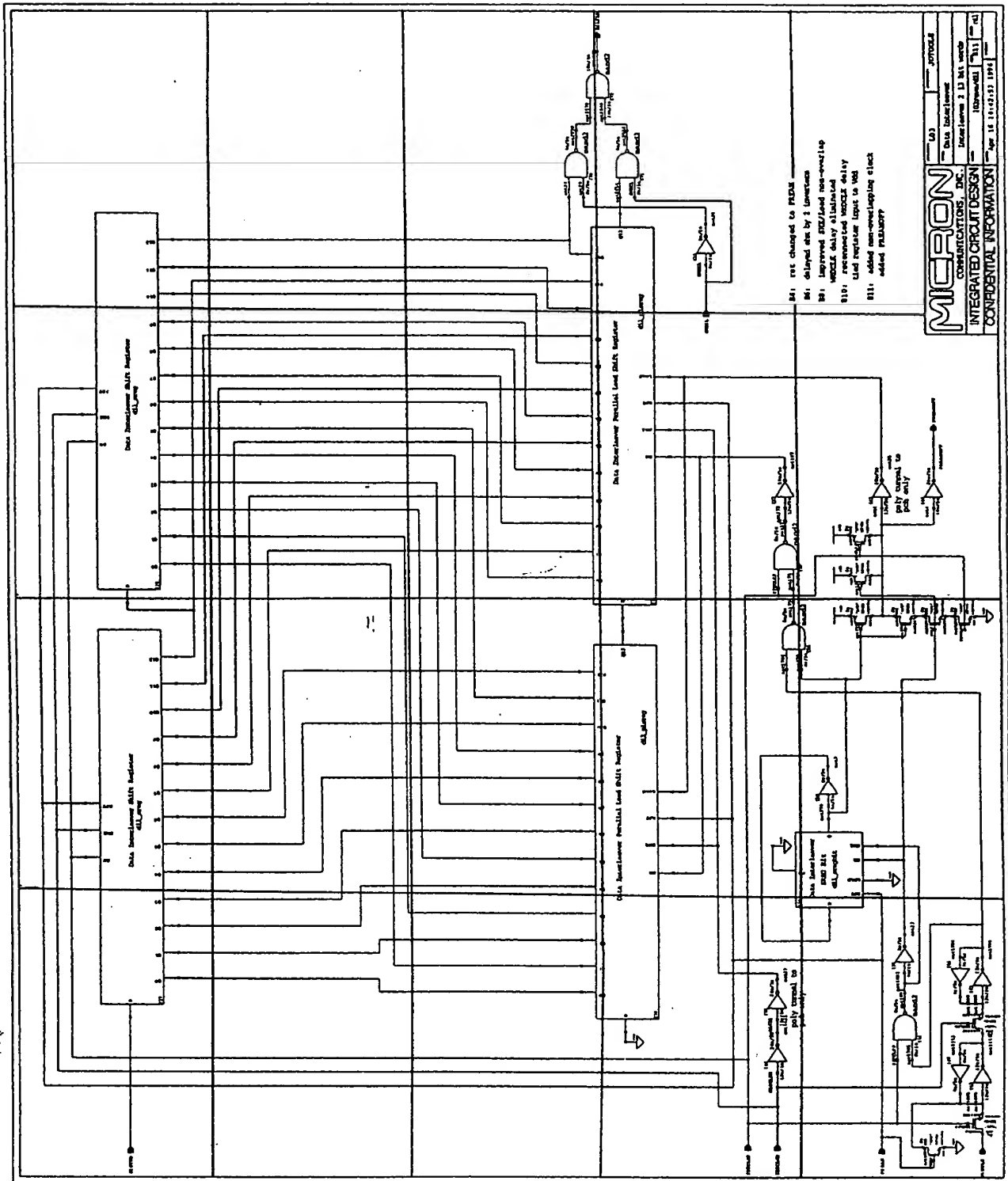




|        |        |        |        |
|--------|--------|--------|--------|
| 7.15AA | 7.15AB | 7.15AC | 7.15AD |
| 7.15BA | 7.15BB | 7.15BC | 7.15BD |
| 7.15CA | 7.15CB | 7.15CC | 7.15CD |
| 7.15DA | 7.15DB | 7.15DC | 7.15DD |
| 7.15EA | 7.15EB | 7.15EC |        |

|      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |        |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--------|
| 1993 | 1994 | 1995 | 1996 | 1997 | 1998 | 1999 | 2000 | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 | 2014 | 2015 | 2016 | 2017 | 2018 | 2019 | 2020 | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 | 2032 | 2033 | 2034 | 2035 | 2036 | 2037 | 2038 | 2039 | 2040 | 2041 | 2042 | 2043 | 2044 | 2045 | 2046 | 2047 | 2048 | 2049 | 2050 | 2051 | 2052 | 2053 | 2054 | 2055 | 2056 | 2057 | 2058 | 2059 | 2060 | 2061 | 2062 | 2063 | 2064 | 2065 | 2066 | 2067 | 2068 | 2069 | 2070 | 2071 | 2072 | 2073 | 2074 | 2075 | 2076 | 2077 | 2078 | 2079 | 2080 | 2081 | 2082 | 2083 | 2084 | 2085 | 2086 | 2087 | 2088 | 2089 | 2090 | 2091 | 2092 | 2093 | 2094 | 2095 | 2096 | 2097 | 2098 | 2099 | 2100 | 2101 | 2102 | 2103 | 2104 | 2105 | 2106 | 2107 | 2108 | 2109 | 2110 | 2111 | 2112 | 2113 | 2114 | 2115 | 2116 | 2117 | 2118 | 2119 | 2120 | 2121 | 2122 | 2123 | 2124 | 2125 | 2126 | 2127 | 2128 | 2129 | 2130 | 2131 | 2132 | 2133 | 2134 | 2135 | 2136 | 2137 | 2138 | 2139 | 2140 | 2141 | 2142 | 2143 | 2144 | 2145 | 2146 | 2147 | 2148 | 2149 | 2150 | 2151 | 2152 | 2153 | 2154 | 2155 | 2156 | 2157 | 2158 | 2159 | 2160 | 2161 | 2162 | 2163 | 2164 | 2165 | 2166 | 2167 | 2168 | 2169 | 2170 | 2171 | 2172 | 2173 | 2174 | 2175 | 2176 | 2177 | 2178 | 2179 | 2180 | 2181 | 2182 | 2183 | 2184 | 2185 | 2186 | 2187 | 2188 | 2189 | 2190 | 2191 | 2192 | 2193 | 2194 | 2195 | 2196 | 2197 | 2198 | 2199 | 2200 | 2201 | 2202 | 2203 | 2204 | 2205 | 2206 | 2207 | 2208 | 2209 | 2210 | 2211 | 2212 | 2213 | 2214 | 2215 | 2216 | 2217 | 2218 | 2219 | 2220 | 2221 | 2222 | 2223 | 2224 | 2225 | 2226 | 2227 | 2228 | 2229 | 2230 | 2231 | 2232 | 2233 | 2234 | 2235 | 2236 | 2237 | 2238 | 2239 | 2240 | 2241 | 2242 | 2243 | 2244 | 2245 | 2246 | 2247 | 2248 | 2249 | 2250 | 2251 | 2252 | 2253 | 2254 | 2255 | 2256 | 2257 | 2258 | 2259 | 2260 | 2261 | 2262 | 2263 | 2264 | 2265 | 2266 | 2267 | 2268 | 2269 | 2270 | 2271 | 2272 | 2273 | 2274 | 2275 | 2276 | 2277 | 2278 | 2279 | 2280 | 2281 | 2282 | 2283 | 2284 | 2285 | 2286 | 2287 | 2288 | 2289 | 2290 | 2291 | 2292 | 2293 | 2294 | 2295 | 2296 | 2297 | 2298 | 2299 | 2300 | 2301 | 2302 | 2303 | 2304 | 2305 | 2306 | 2307 | 2308 | 2309 | 2310 | 2311 | 2312 | 2313 | 2314 | 2315 | 2316 | 2317 | 2318 | 2319 | 2320 | 2321 | 2322 | 2323 | 2324 | 2325 | 2326 | 2327 | 2328 | 2329 | 2330 | 2331 | 2332 | 2333 | 2334 | 2335 | 2336 | 2337 | 2338 | 2339 | 2340 | 2341 | 2342 | 2343 | 2344 | 2345 | 2346 | 2347 | 2348 | 2349 | 2350 | 2351 | 2352 | 2353 | 2354 | 2355 | 2356 | 2357 | 2358 | 2359 | 2360 | 2361 | 2362 | 2363 | 2364 | 2365 | 2366 | 2367 | 2368 | 2369 | 2370 | 2371 | 2372 | 2373 | 2374 | 2375 | 2376 | 2377 | 2378 | 2379 | 2380 | 2381 | 2382 | 2383 | 2384 | 2385 | 2386 | 2387 | 2388 | 2389 | 2390 | 2391 | 2392 | 2393 | 2394 | 2395 | 2396 | 2397 | 2398 | 2399 | 2400 | 2401</ |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--------|

Fig. 7.15



**7.1501BA**

7.1501CA

IT 7.1501

CONFIDENTIAL

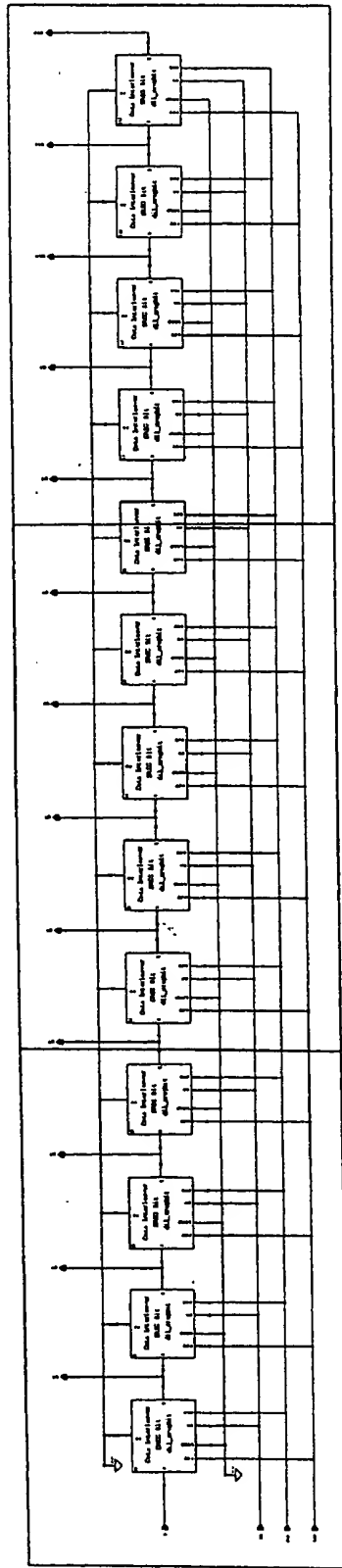


Fig. 7.1501

7.1502AA

7.1502BA

7.1502CA

Fig 7.1502

2025-02-20 10:20:20

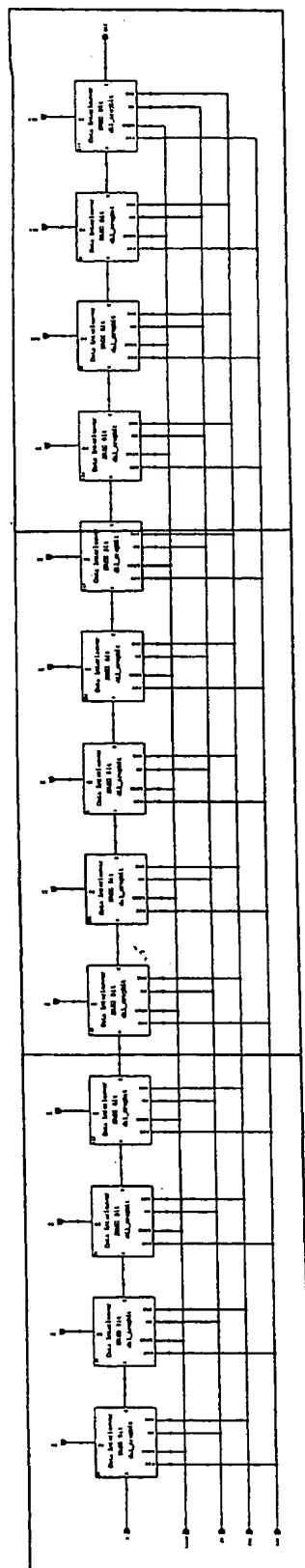
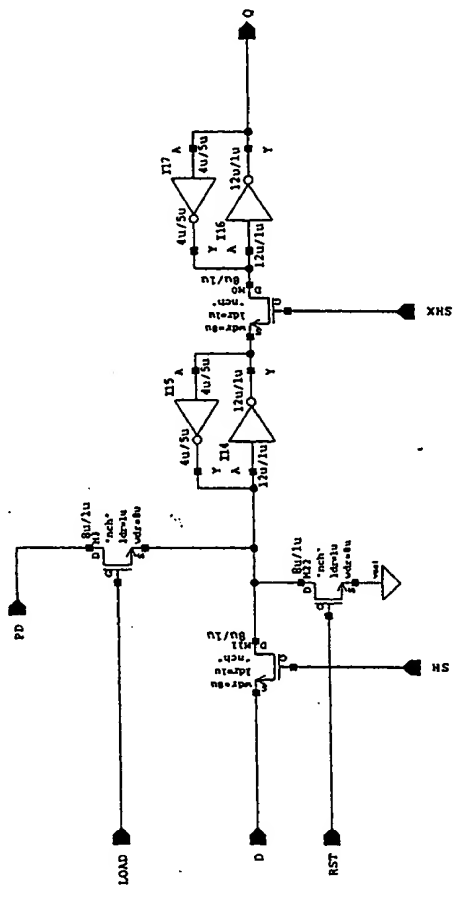


Fig. 7.1502

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
NONPARENTAL REGISTRATION

FIG. 7, 150201

FIG. 7, 150201



|                             |  |                               |  |                   |         |
|-----------------------------|--|-------------------------------|--|-------------------|---------|
| MICRON COMMUNICATIONS, INC. |  | PROJECT: L03                  |  | DESIGNER: JOTOOLE |         |
| INTEGRATED CIRCUIT DESIGN   |  | TITLE: Data Interleaver Shift |  |                   |         |
| CONFIDENTIAL INFORMATION    |  | REGISTER BIT                  |  |                   |         |
|                             |  | NAME: 103reva/dil_sregbit     |  | REV: B1           | DATE: A |
|                             |  | DATE: Sep 27 10:25:07 1994    |  | PAGE: 1           |         |

"FOREF" E30E200

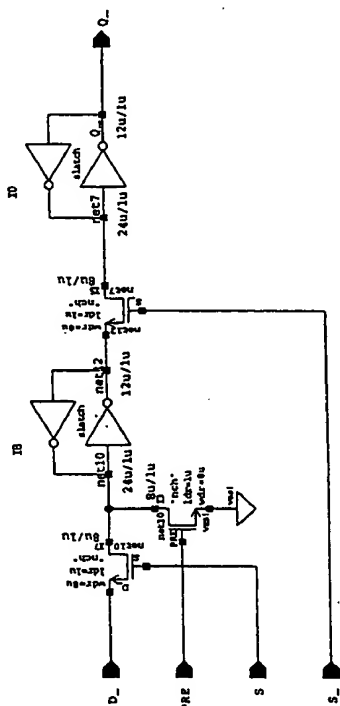
|        |        |        |        |
|--------|--------|--------|--------|
| 7.16AA | 7.16AB | 7.16AC | 7.16AD |
| 7.16BA | 7.16BB | 7.16BC | 7.16BD |
| 7.16CA | 7.16CB | 7.16CC | 7.16CD |

IL 11 11 11.11



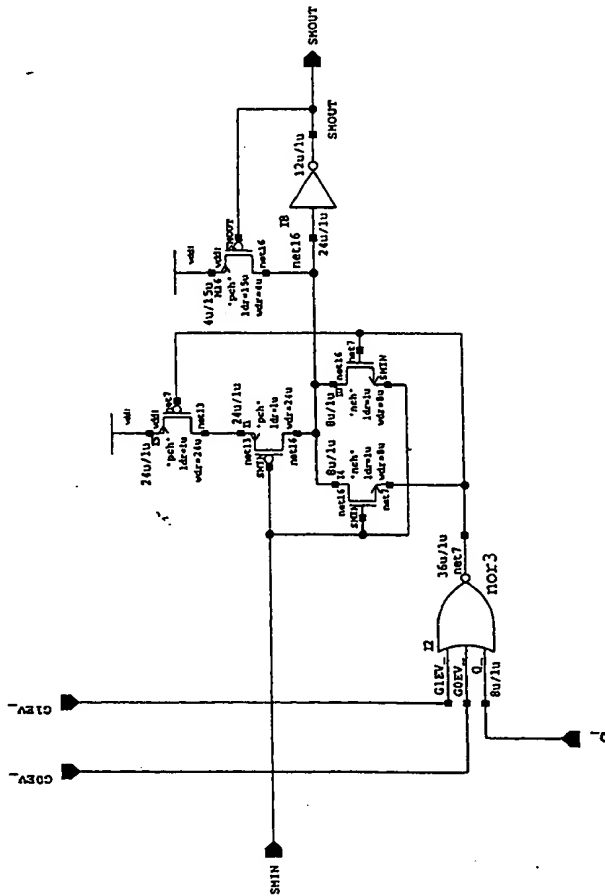


FIG. 7.1601



|                           |  |                             |                   |
|---------------------------|--|-----------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                | DESIGNER: Rotzoll |
| COMMUNICATIONS, INC.      |  | Convolutional Encoder Shift |                   |
| INTEGRATED CIRCUIT DESIGN |  | Register Cell               |                   |
| CONFIDENTIAL INFORMATION  |  | 103reva/convshr             | REV: A            |
|                           |  | DATE: Sep 2 10:34:27 1994   | SHEET: 1          |

Fig. 7.1602

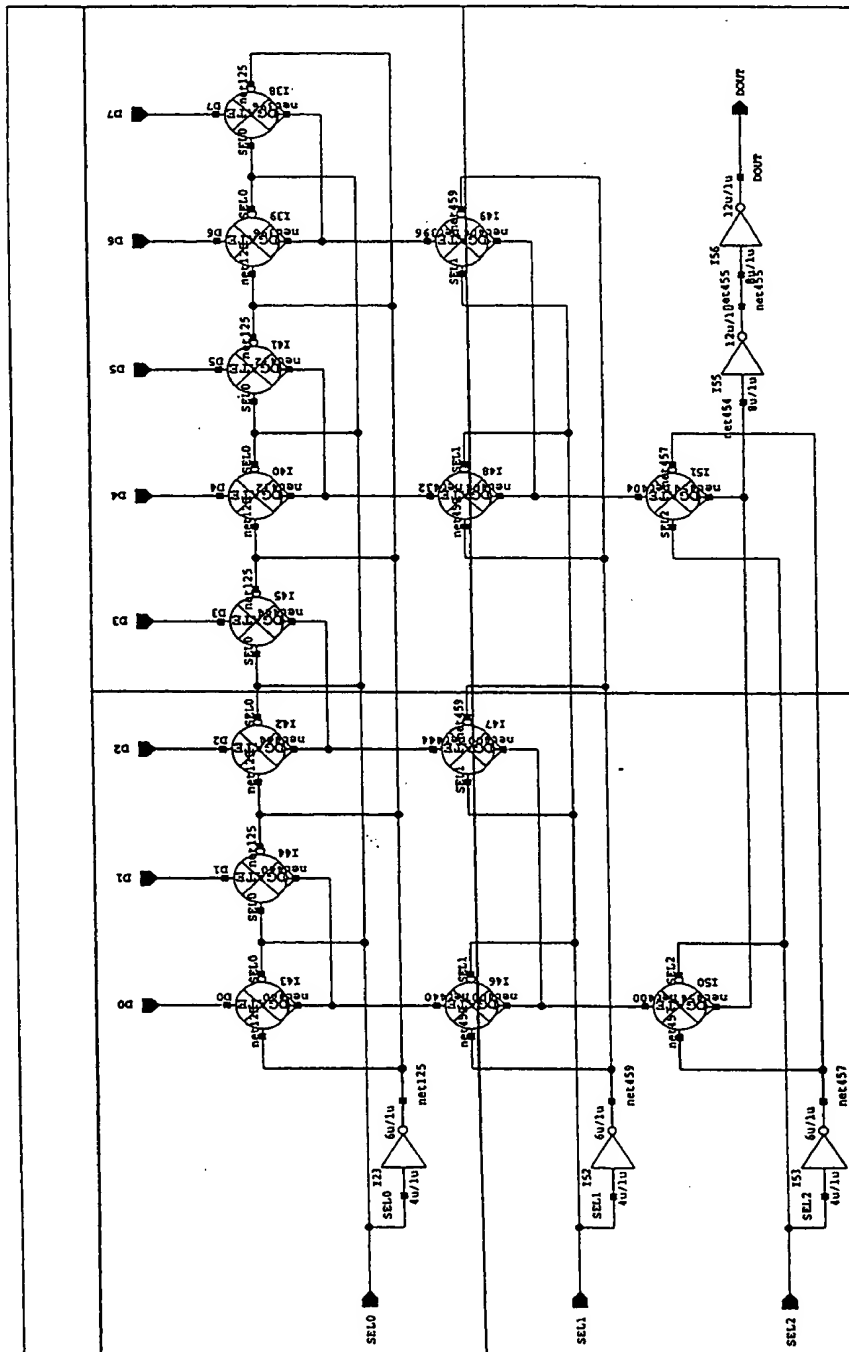


|                           |  |                                     |                   |
|---------------------------|--|-------------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                        | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: Convolutional Encoder Summer |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/convsum               | REV: B1           |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 2 10:32:17 1994           | REV: A            |

|        |        |
|--------|--------|
| 7.17AA | 7.17AB |
| 7.17BA | 7.17BB |

II. II. II. II. II.

Fig. 7.17



|                           |  |                                      |                   |
|---------------------------|--|--------------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                         | DESIGNER: Rotzoll |
| COMMUNICATIONS, INC.      |  | TITLE: Shift Register Input Data Mux |                   |
| INTEGRATED CIRCUIT DESIGN |  | 8 to 1 MUX                           |                   |
| CONFIDENTIAL INFORMATION  |  | NAME: 103reva/shdse1                 | REV: -            |
|                           |  | DATE: Nov 11 06:55:32 1993           | SHEET: A          |

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|        |        |        |
|--------|--------|--------|
| 7.18AA | 7.18AB | 7.18AC |
| 7.18BA | 7.18BB | 7.18BC |
| 7.18CA | 7.18CB | 7.18CC |

IL 07 7.18B

CONFIDENTIAL

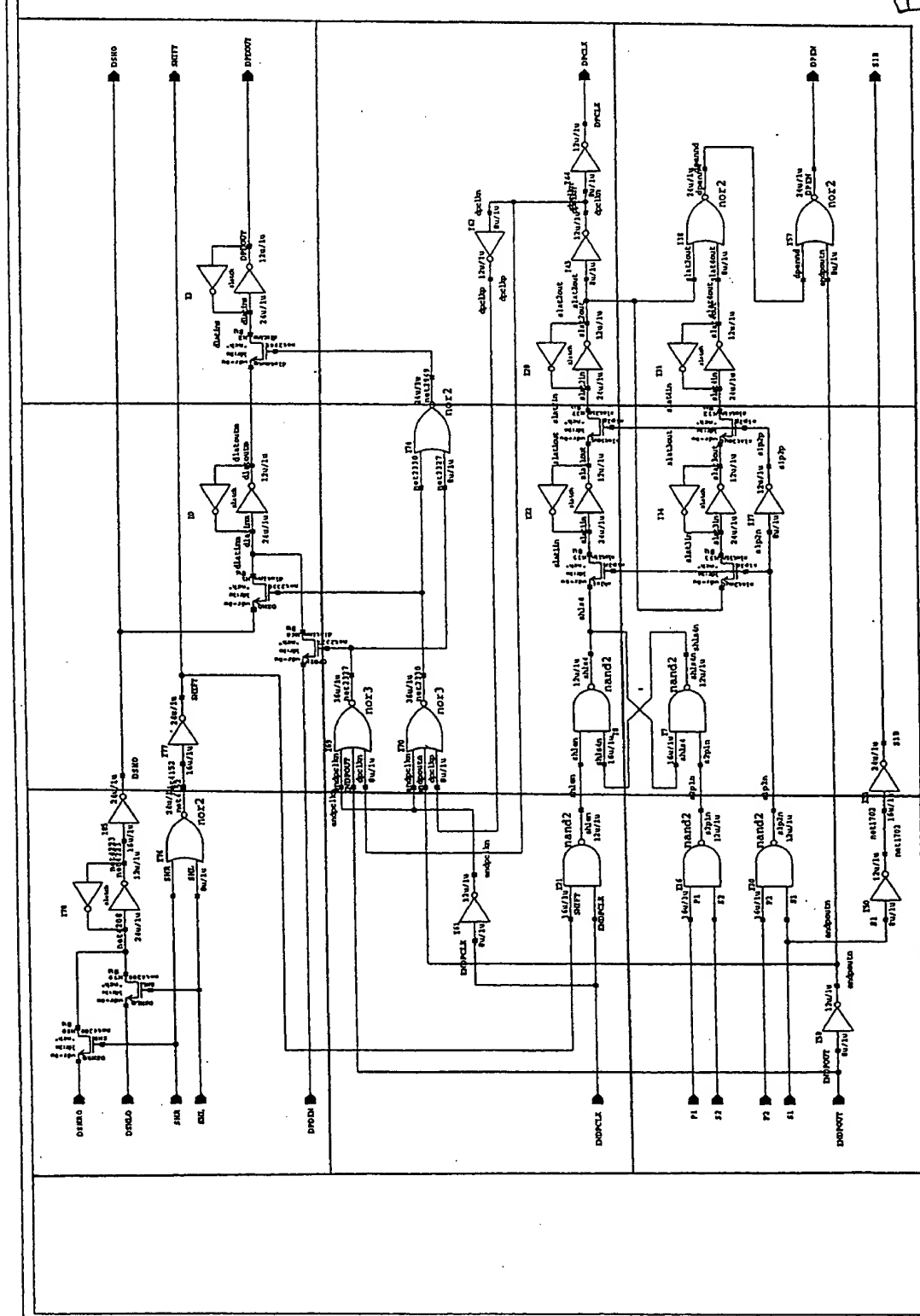


Fig. 7.18

|                           |  |                                |                      |
|---------------------------|--|--------------------------------|----------------------|
| MICRON                    |  | PROJECT 1.03                   | REVISION 100         |
| COMMUNICATIONS, INC.      |  | Digital Port Output Controller |                      |
| INTEGRATED CIRCUIT DESIGN |  | DATE                           | 10/12/80             |
| CONFIDENTIAL INFORMATION  |  | REV                            | 1                    |
|                           |  | DATE                           | Nov 12 10:05:40 1993 |
|                           |  | REV                            | 1                    |
|                           |  | DATE                           | Nov 12 10:05:40 1993 |

8AA

8AB

8BA

8BB

8CA

8CB

IE II □ BB

FOUO E30E2860



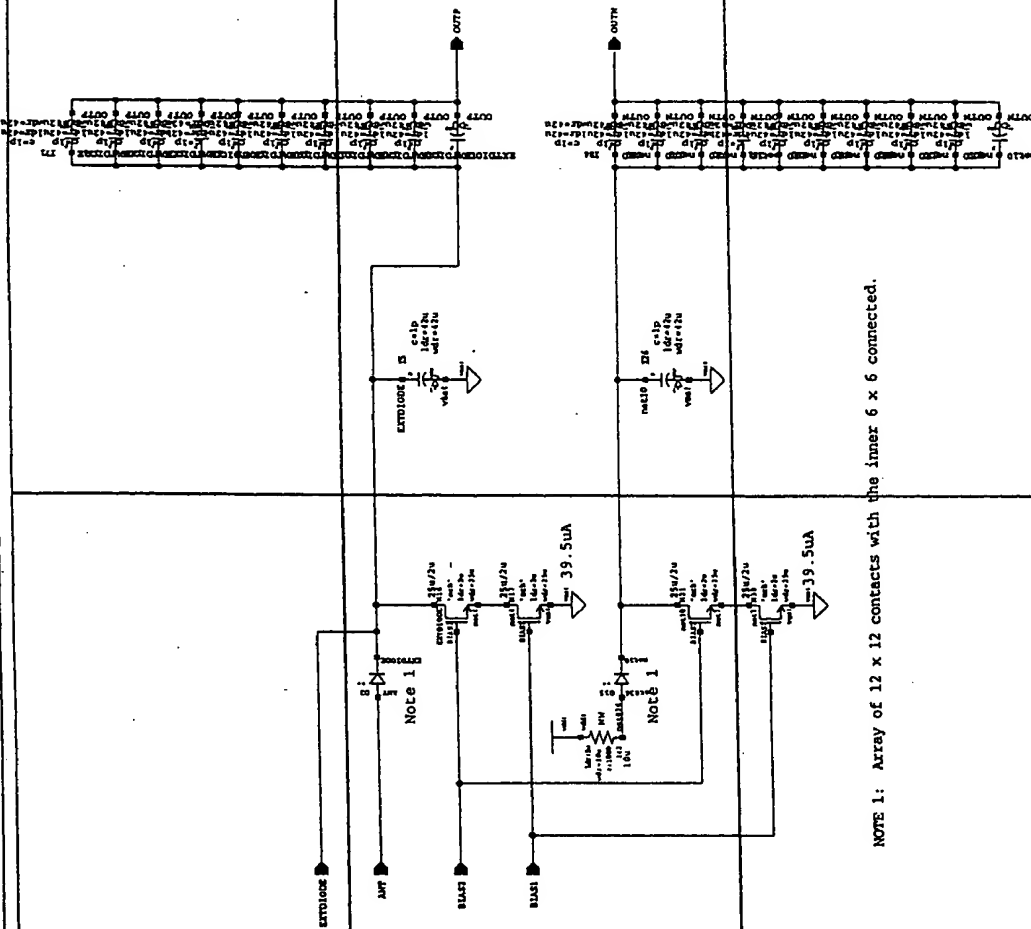


|        |        |        |        |        |
|--------|--------|--------|--------|--------|
| 8.01AA | 8.01AB | 8.01AC | 8.01AD | 8.01AE |
| 8.01BA | 8.01BB | 8.01BC | 8.01BD | 8.01BE |
| 8.01CA | 8.01CB | 8.01CC | 8.01CD | 8.01CE |
| 8.01DA | 8.01DB | 8.01DC | 8.01DD | 8.01DE |





SECRET



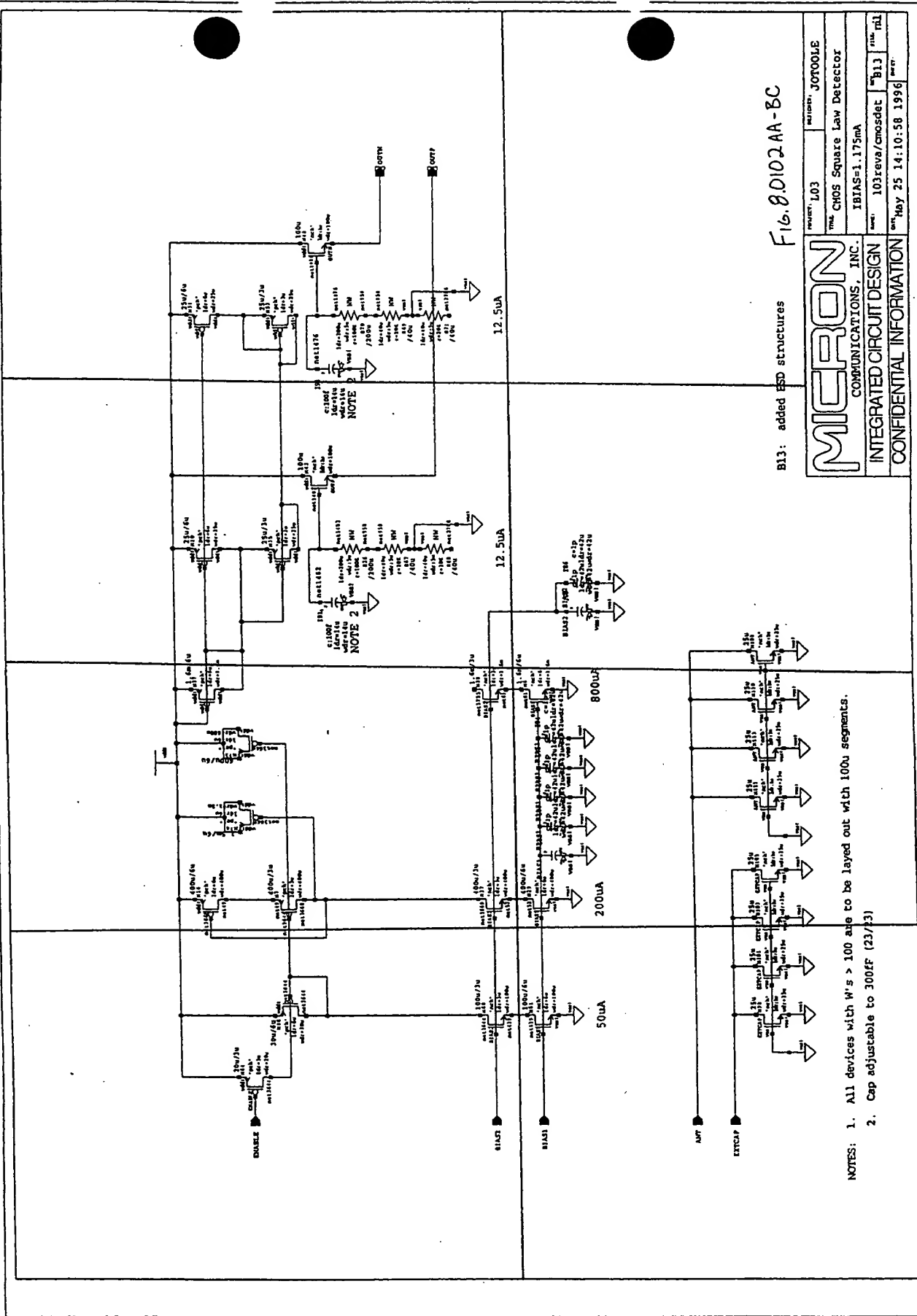
NOTE 1: Array of 12 x 12 contacts with the inner 6 x 6 connected.

FIG. 8.0101AA-C8

|   |                                |                   |
|---|--------------------------------|-------------------|
| MICRON<br>COMMUNICATIONS, INC.<br>INTEGRATED CIRCUIT DESIGN<br>CONFIDENTIAL INFORMATION | PROJECT: L03                   | REVISION: J0700LE |
|   | Title: Schottky Diode Detector |                   |
|   | IBIAS=79uA                     |                   |
|   | 103renu/diodelet               | B13               |
| May 24 13:54:28 1996  |                                |                   |

- B2: connected EXTIOIDE line
- B6: schottky array changed to 6x6
- rf cap reduced to 1pF
- B8: increased Cc to 10pF; decreased Crf to 1pF
- B13: added 1k resistor in series with dummy diode for ESD

|          |          |          |          |
|----------|----------|----------|----------|
| 8.0102AA | 8.0102AB | 8.0102AC | 8.0102AD |
| 8.0102BA | 8.0102BB | 8.0102BC |          |



B13: added BSD structures

|                           |                                |                            |
|---------------------------|--------------------------------|----------------------------|
| MICRON                    | PROJECT: L03                   | REVISION: J0700LE          |
|                           | NAME: CMOS Square Law Detector |                            |
|                           | IBIAS=1.175mA                  |                            |
|                           | DATE: 103.revs/cmosdet         | FILE: B13                  |
| INTEGRATED CIRCUIT DESIGN |                                | DATE: May 25 14:10:58 1996 |
| CONFIDENTIAL INFORMATION  |                                |                            |

"000000" 000000

|          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|
| 8.0103AA | 8.0103AB | 8.0103AC | 8.0103AD | 8.0103AE | 8.0103AF |
| 8.0103BA | 8.0103BB | 8.0103BC | 8.0103BD | 8.0103BE | 8.0103BF |
| 8.0103CA | 8.0103CB | 8.0103CC | 8.0103CD | 8.0103CE | 8.0103CF |

000000 000000



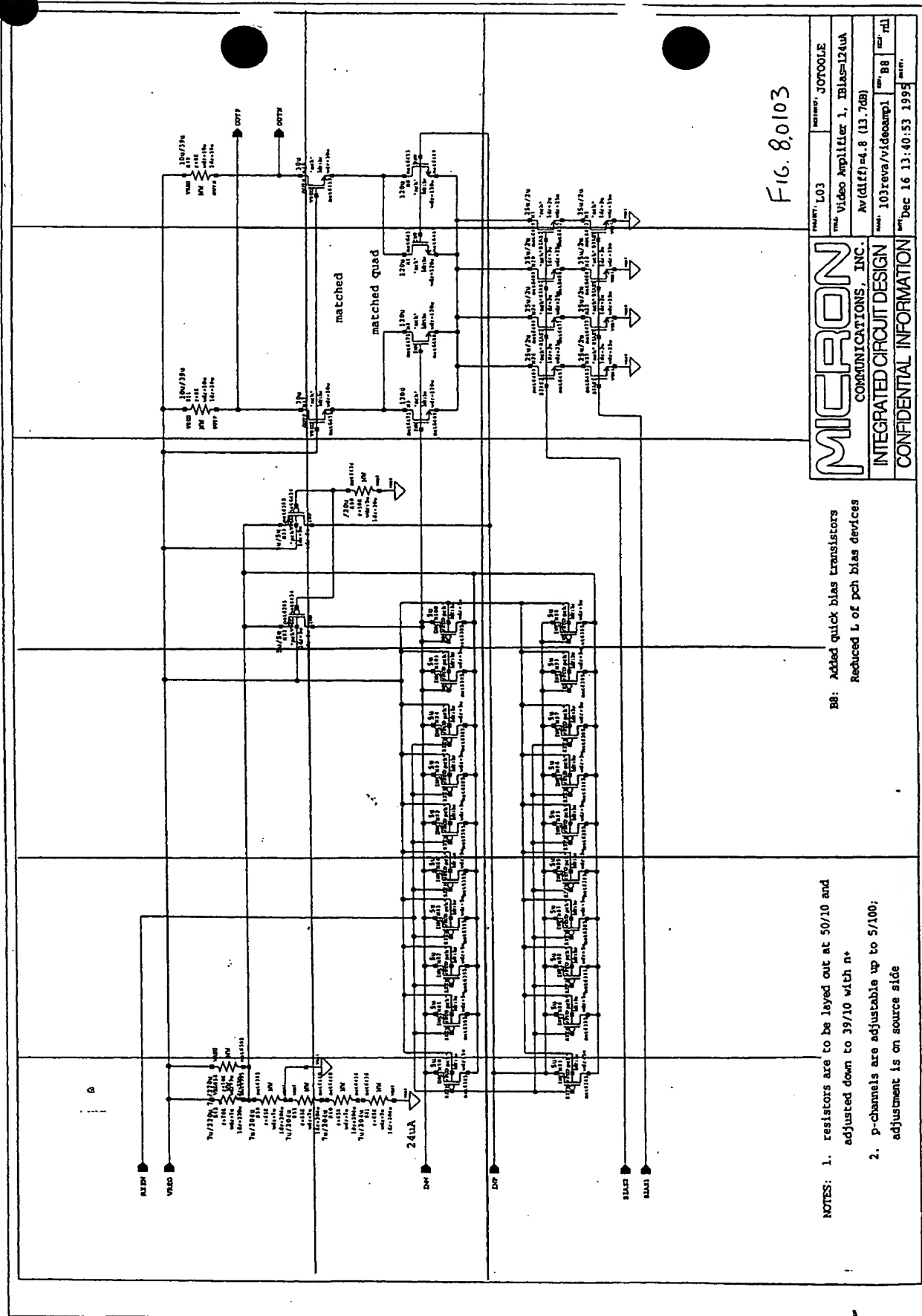


FIG. 8.0103

|                               |         |
|-------------------------------|---------|
| MICRON                        |         |
| COMMUNICATIONS, INC.          |         |
| INTEGRATED CIRCUIT DESIGN     |         |
| CONFIDENTIAL INFORMATION      |         |
| PART NO. L03                  | REV. B8 |
| Video Amplifier 1, Bias=124uA |         |
| Av(dB)=4.8 (13.7dB)           |         |
| 103reva/videoamp1             |         |
| Dec 16 13:40:53 1995          |         |

- NOTES:
1. resistors are to be layed out at 50/10 and adjusted down to 39/10 with n+
  2. p-channels are adjustable up to 5/100; adjustment is on source side
- B8: Added quick bias transistors  
Reduced L of pch bias devices

MI40-030

|          |          |          |
|----------|----------|----------|
| 8.0104AA | 8.0104AB | 8.0104AC |
| 8.0104BA | 8.0104BB | 8.0104BC |

MI40-030

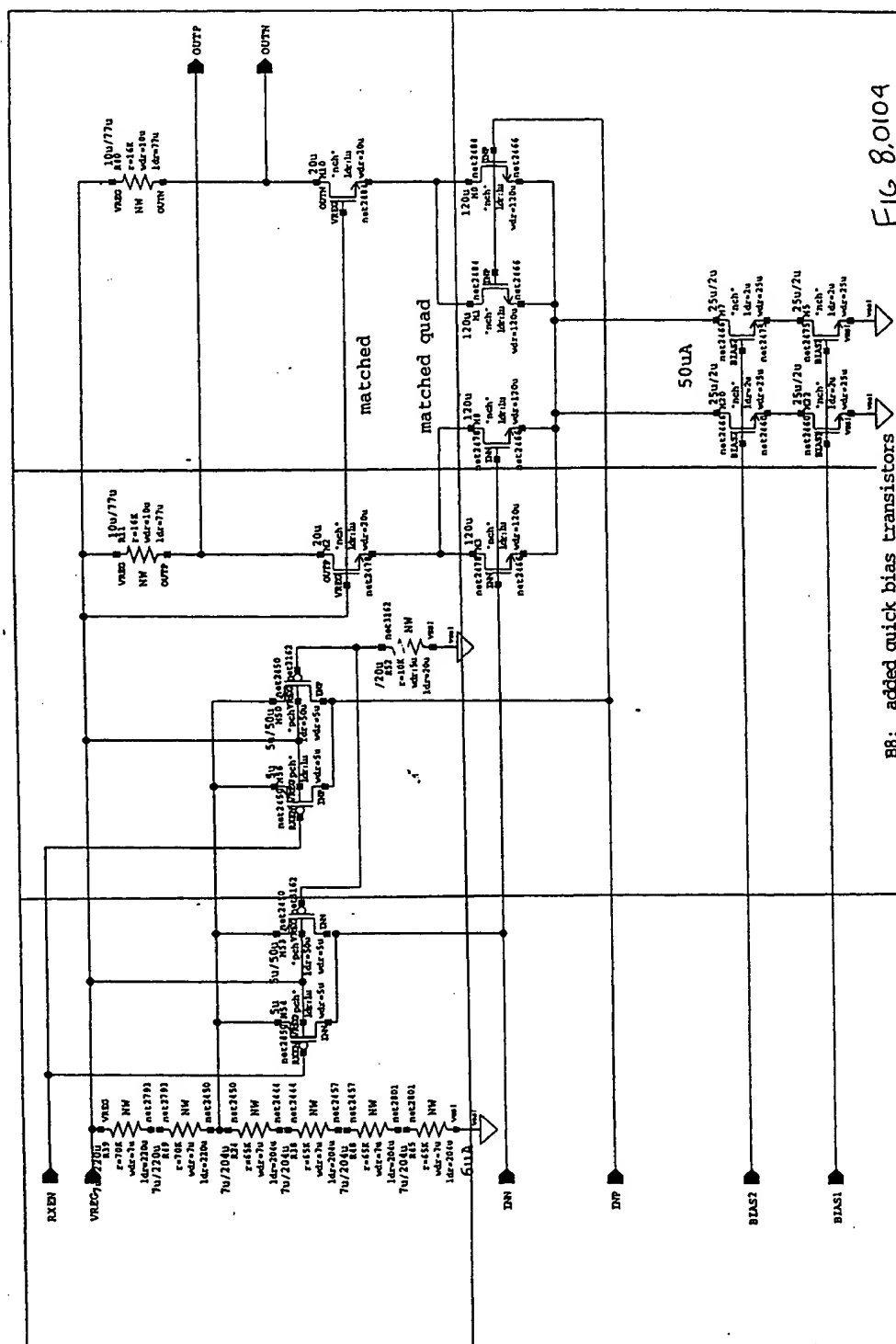


FIG 8.0104

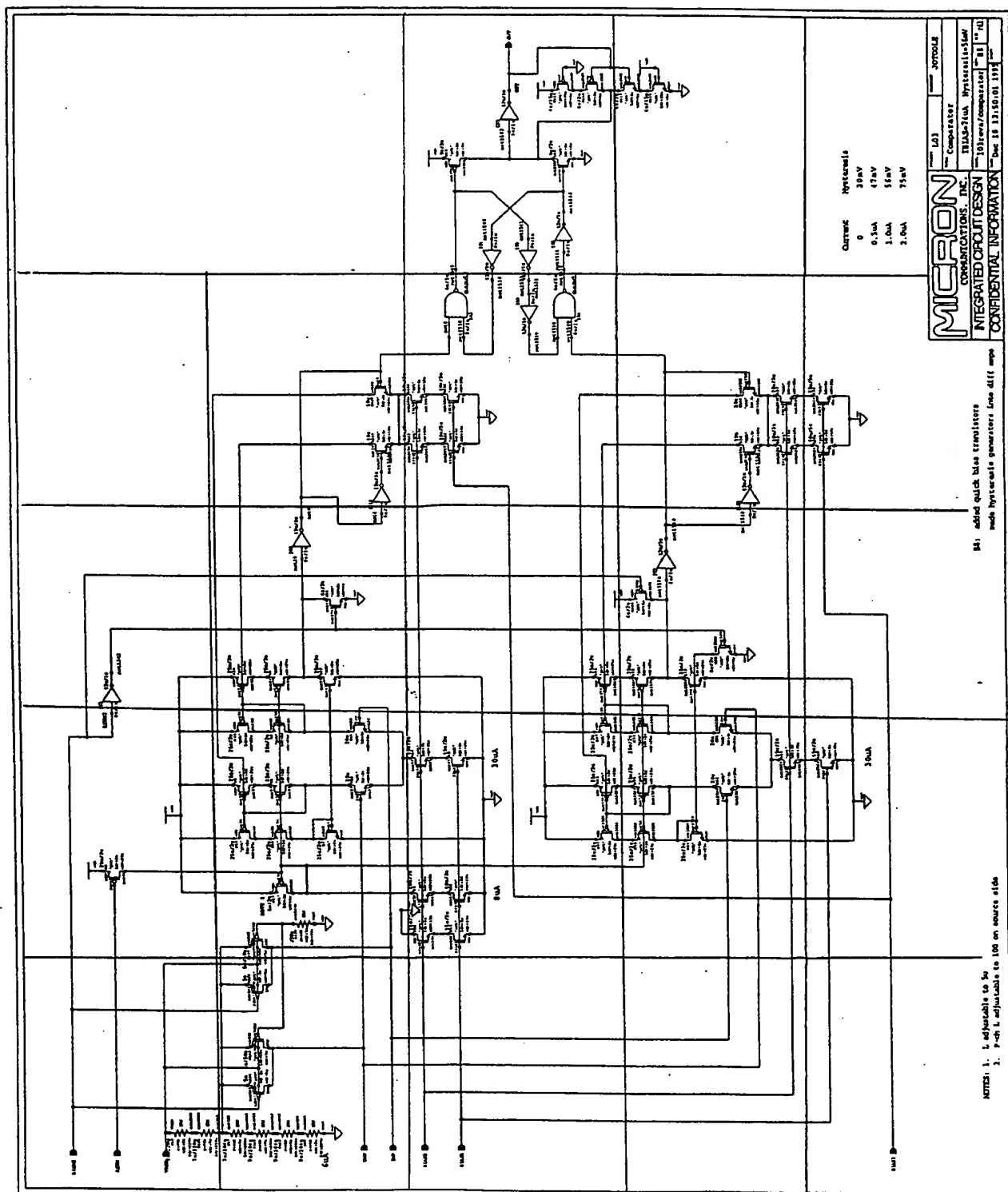
888: added quick bias transistors

- NOTES:
1. resistors are to be layed out at 100/10 and adjusted down to 77/10 with n+
  2. p-channels are adjustable up to 5/100; adjustment is on the source side.

|                                      |                   |                   |    |
|--------------------------------------|-------------------|-------------------|----|
| PROJECT: L03                         |                   | DESIGNER: JOTOOLE |    |
| TITLE: Video Amplifier 2, IBias=56uA |                   |                   |    |
| Av(diff)=5.6 (15dB)                  |                   |                   |    |
| MODE:                                | 103reva/Videoamp2 | REV:              | B8 |
| DATE: Dec 16 13:42:25 1995           |                   | PAGE: 1           |    |

BB.00105  
II II

FIG. 8.0105

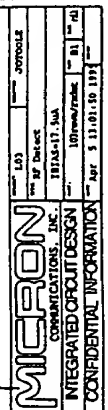


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|          |          |          |          |
|----------|----------|----------|----------|
| 8.0106AA | 8.0106AB | 8.0106AC | 8.0106AD |
| 8.0106BA | 8.0106BB | 8.0106BC | 8.0106BD |
| 8.0106CA | 8.0106CB | 8.0106CC | 8.0106CD |

EX-111115

FIG. 8.0106



7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

|          |          |          |          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.0107AA | 8.0107AB | 8.0107AC | 8.0107AD | 8.0107AE | 8.0107AF | 8.0107AG | 8.0107AH | 8.0107AI | 8.0107AJ | 8.0107AK | 8.0107AL | 8.0107AM |          |
| 8.0107BA | 8.0107BB | 8.0107BC | 8.0107BD | 8.0107BE | 8.0107BF | 8.0107BG | 8.0107BH | 8.0107BI | 8.0107BJ | 8.0701BK | 8.0107BL | 8.0107BM | 8.0107BN |
| 8.0107CA | 8.0107CB | 8.0107CC | 8.0107CD | 8.0107CE | 8.0107CF | 8.0107CG | 8.0107CH | 8.0107CI | 8.0107CJ | 8.0107CK | 8.0107CL | 8.0107CM | 8.0107CN |
| 8.0107DA | 8.0107DB | 8.0107DC | 8.0107DD | 8.0107DE | 8.0107DF | 8.0107DG | 8.0107DH | 8.0107DI | 8.0107DJ | 8.0107DK | 8.0107DL | 8.0107DM | 8.0107DN |
| 8.0107EA | 8.0107EB | 8.0107EC | 8.0107ED | 8.0107EE | 8.0107EF | 8.0107EG | 8.0107EH | 8.0107EI | 8.0107EJ | 8.0107EK | 8.0107EL | 8.0107EM | 8.0107EN |
| 8.0107FA | 8.0107FB | 8.0107FC | 8.0107FD | 8.0107FE | 8.0107FF | 8.0107FG | 8.0107FH | 8.0107FI | 8.0107FJ | 8.0107FK | 8.0107FL | 8.0107FM | 8.0107FN |
| 8.0107GA | 8.0107GB | 8.0107GC | 8.0107GD | 8.0107GE | 8.0107GF | 8.0107GG | 8.0107GH | 8.0107GI | 8.0107GJ | 8.0107GK | 8.0107GL | 8.0107GM | 8.0107GN |

7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0





FOUO 8302260

|          |          |          |
|----------|----------|----------|
| 8.0108AA | 8.0108AB | 8.0108AC |
|----------|----------|----------|

EX-11 8302260

FIGURE 8-108

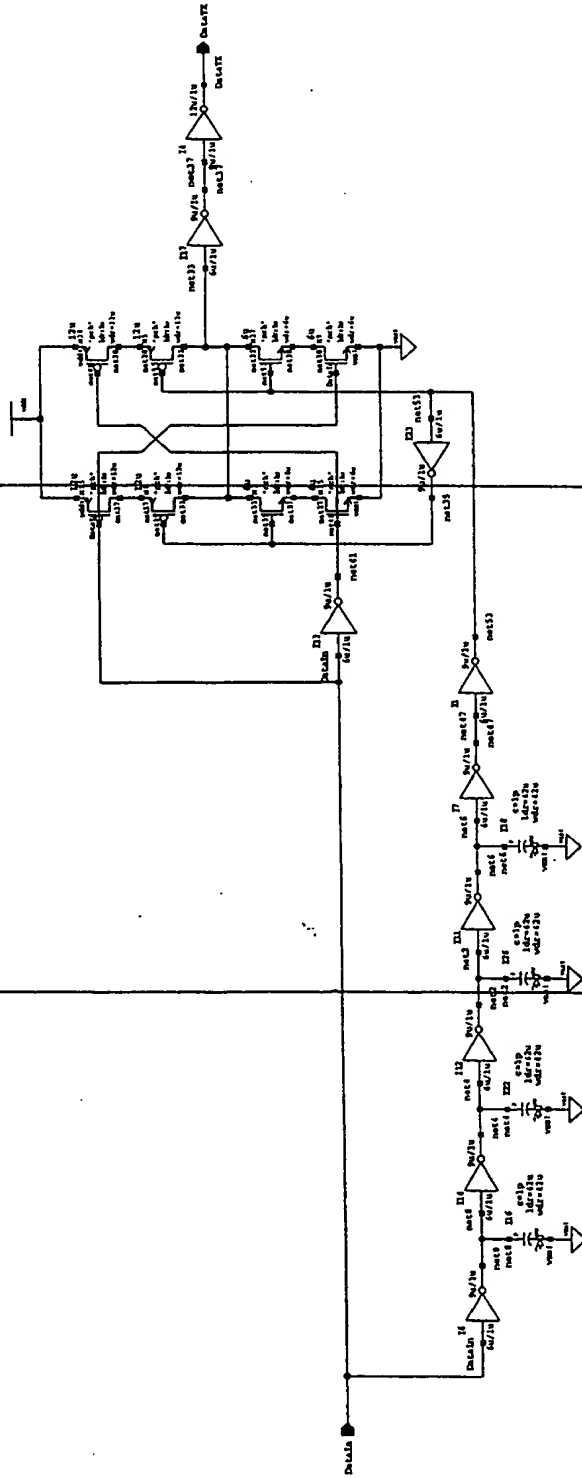


FIG. 8.0108

**MICRON**  
COMMUNICATIONS, INC.

PART 103 JOT00LE

Data Transition Detector

Output Pulse Width = 40ns (non

103reva/dacack B1 77m ml

CONFIDENTIAL INFORMATION 177 Dec 14 17:11:15 1994

|        |        |        |
|--------|--------|--------|
| 8.02AA | 8.02AB | 8.02AC |
| 8.02BA | 8.02BB | 8.02BC |



TABLE 8.0201

|          |          |
|----------|----------|
| 8.0201AA | 8.0201AB |
|----------|----------|

8.0201AA 8.0201AB

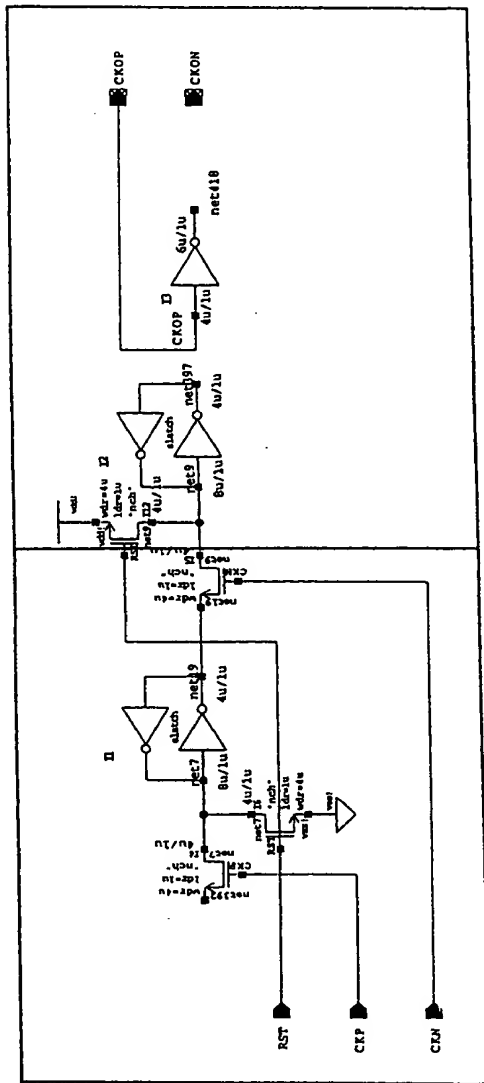


Fig. 8.0201

|                           |  |                                    |                  |
|---------------------------|--|------------------------------------|------------------|
| MICRON                    |  | PRINT: L03                         | SECTION: J0700LE |
| COMMUNICATIONS, INC.      |  | TITLE: Titled Lockout Divider Cell |                  |
| INTEGRATED CIRCUIT DESIGN |  | PART: T03reva/tldcel_bypass        |                  |
| CONFIDENTIAL INFORMATION  |  | REV: B10                           |                  |
|                           |  | DATE: Mar 26 13:54:47 1996         |                  |

B10: new cell to bypass 1st counter stage

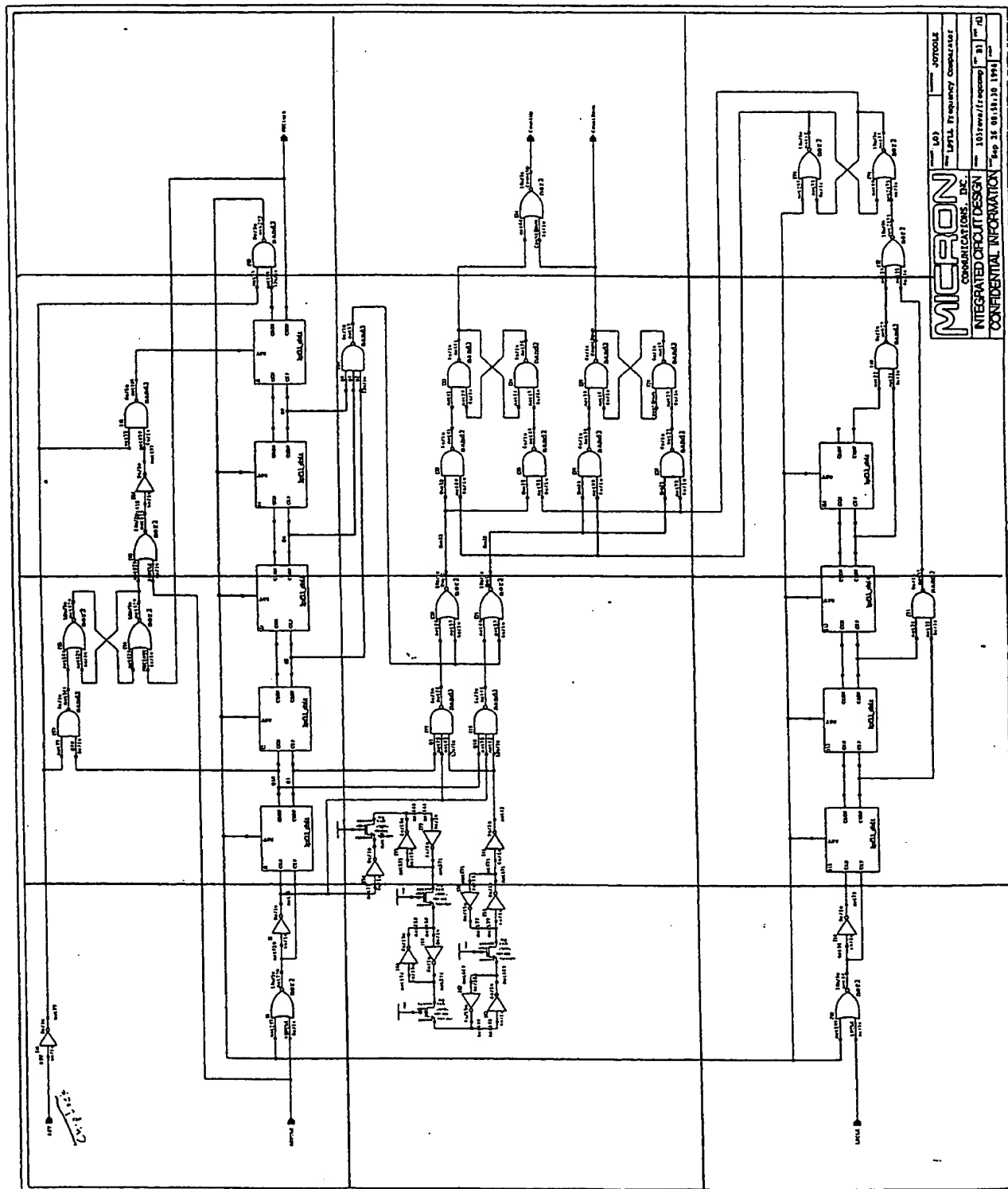
TABLE 20000000

|          |          |          |          |
|----------|----------|----------|----------|
| 8.0202AA | 8.0202AB | 8.0202AC | 8.0202AD |
| 8.0202BA | 8.0202BB | 8.0202BC | 8.0202BD |
| 8.0202CA | 8.0202CB | 8.0202CC | 8.0202CD |

TABLE 20000000



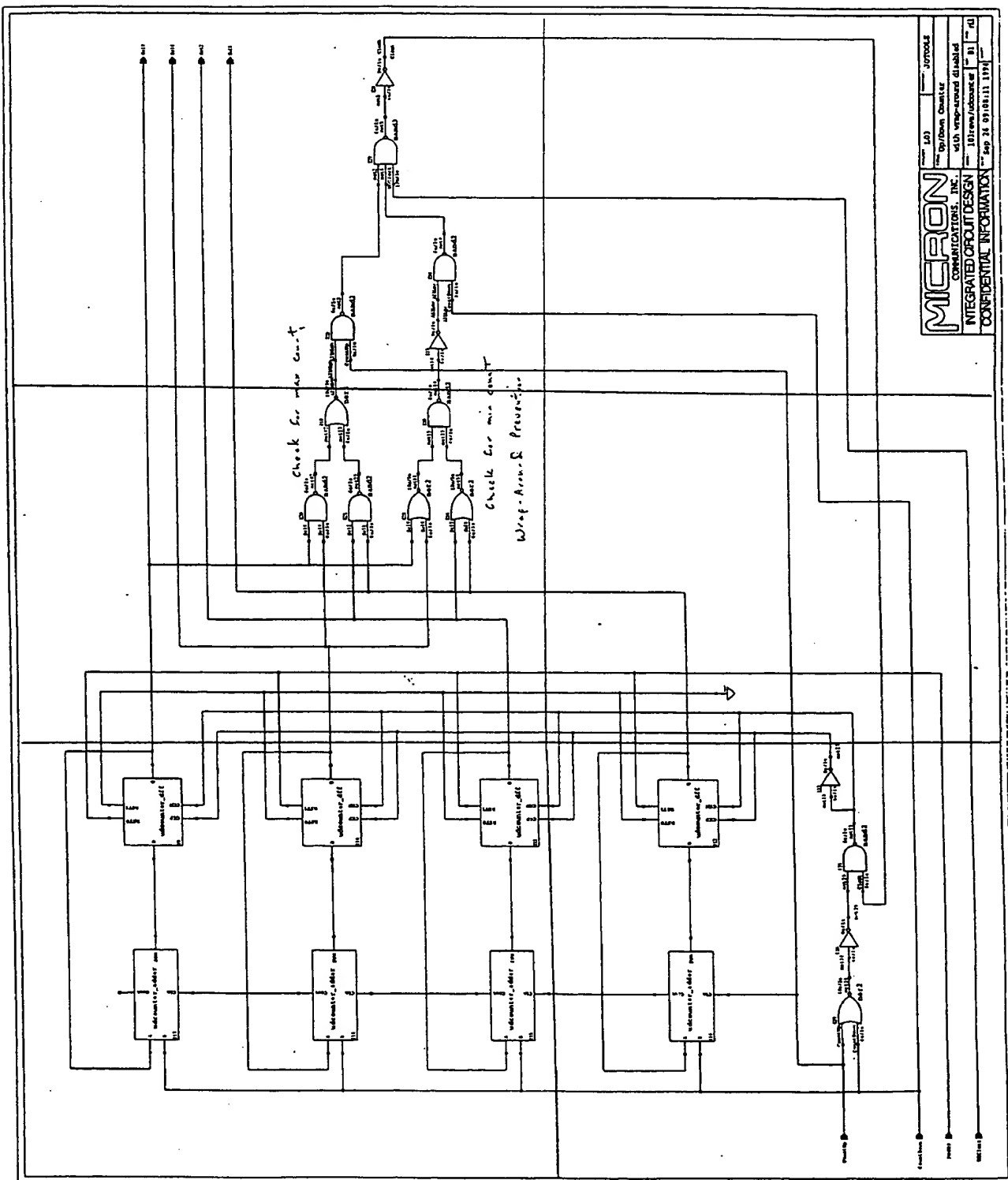
Fig. 8.0202



|          |          |          |
|----------|----------|----------|
| 8.0203AA | 8.0203AB | 8.0203AC |
| 8.0203BA | 8.0203BB | 8.0203BC |

IL 09 88.002003

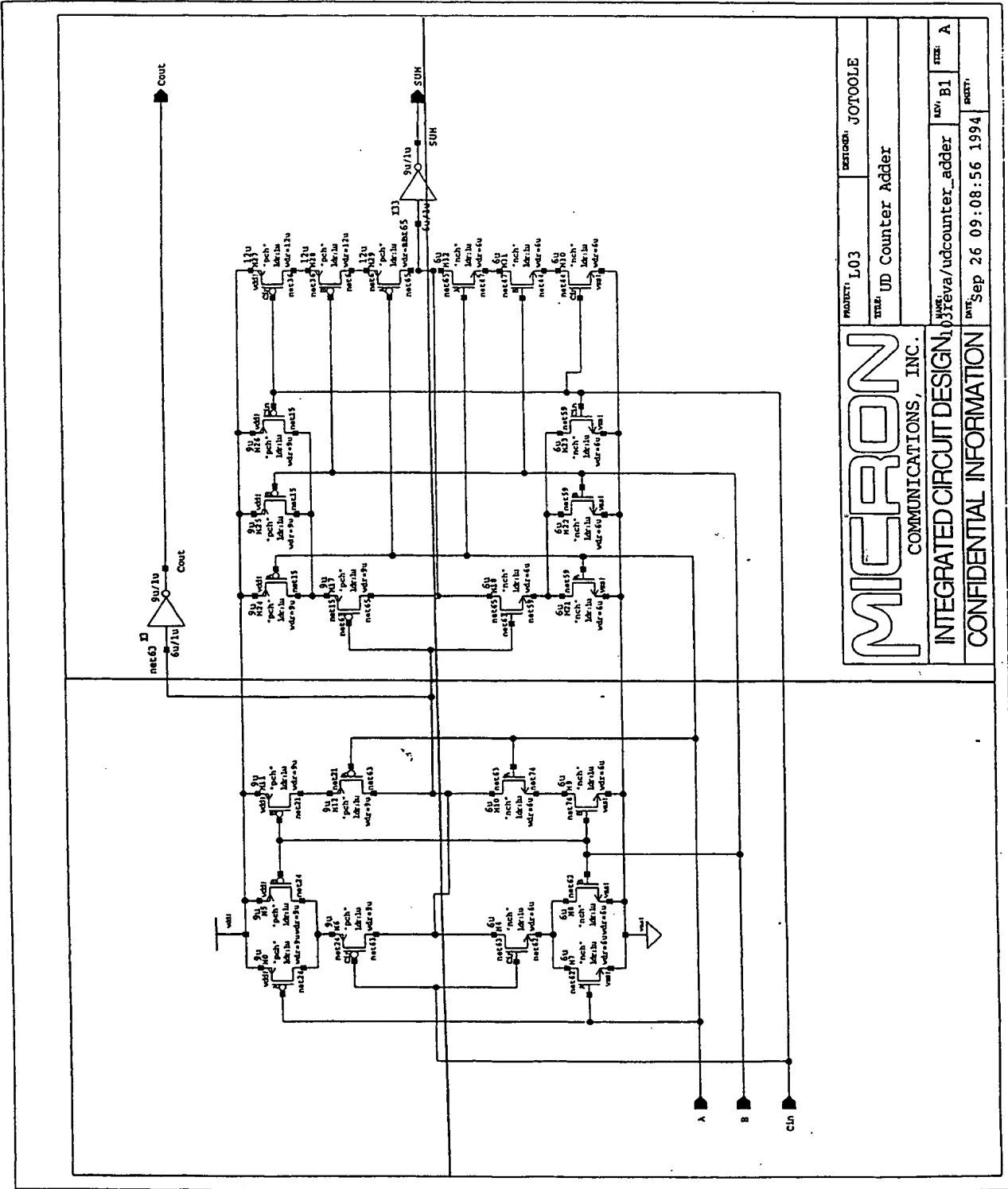
Fig. 8.0203



Page 30 of 30

|            |            |
|------------|------------|
| 8.020301AA | 8.020301AB |
| 8.020301BA | 8.020301BB |

8.020301BB



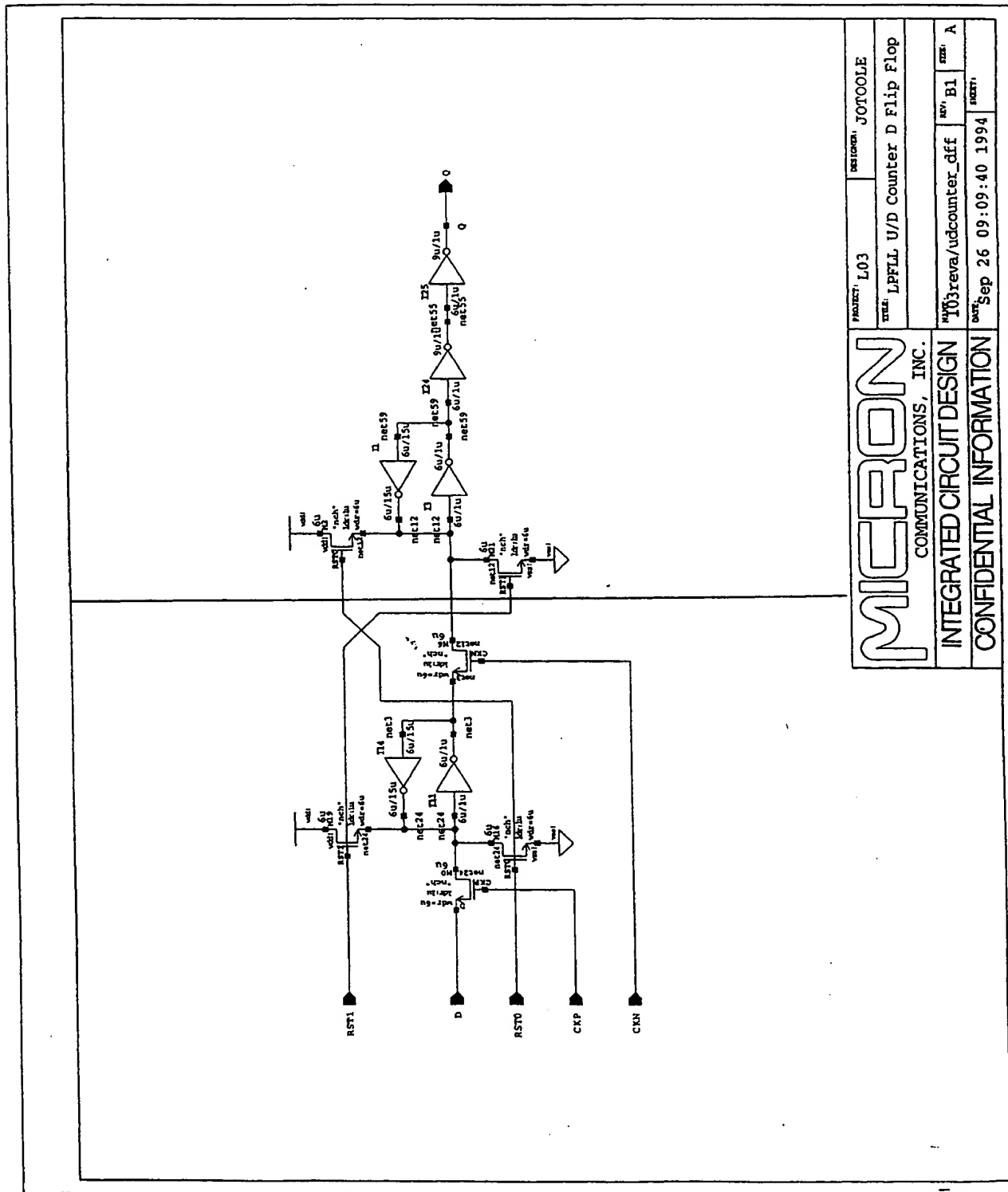
|                           |  |                                  |                    |
|---------------------------|--|----------------------------------|--------------------|
| MICRON                    |  | PROJECT: L03                     | EXTENSION: J0700LE |
| COMMUNICATIONS, INC.      |  | TITLE: UD Counter Adder          |                    |
| INTEGRATED CIRCUIT DESIGN |  | DESIGNER: jfreva/udcounter_adder | REV: B1            |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 26 09:08:56 1994       | FIG: A             |

Fig. 8.020501

8.020302AA

2016.02.08 6.11.1

Figure 8.020302



|                           |  |                                     |                   |
|---------------------------|--|-------------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                        | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: LPFL U/D Counter D Flip Flop |                   |
| INTEGRATED CIRCUIT DESIGN |  | REV: B1                             | REV: A            |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 26 09:09:40 1994          | DATE:             |

Fig. 8.020302

|          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.0204AA | 8.0204AB | 8.0204AC | 8.0204AD | 8.0204AE | 8.0204AF | 8.0204AG | 8.0204AH | 8.0204AI | 8.0204AJ |
| 8.0204BA | 8.0204BB | 8.0204BC | 8.0204BD | 8.0204BE | 8.0204BF | 8.0204BG | 8.0204BH | 8.0204BI | 8.0204BJ |
| 8.0204CA | 8.0204CB | 8.0204CC | 8.0204CD | 8.0204CE | 8.0204CF | 8.0204CG | 8.0204CH | 8.0204CI |          |
| 8.0204DA | 8.0204DB | 8.0204DC | 8.0204DD | 8.0204DE | 8.0204DF | 8.0204DG | 8.0204DH | 8.0204DI |          |
| 8.0204EA | 8.0204EB | 8.0204EC | 8.0204ED | 8.0204EE | 8.0204EF | 8.0204EG | 8.0204EH | 8.0204EI | 8.0204EJ |

Итого 8.0204



The diagram illustrates a complex integrated circuit (IC) design, likely a microprocessor or memory controller, featuring a dense network of logic gates, multiplexers, and data paths. The circuit is organized into a grid with horizontal and vertical lines representing signal buses. Various components are labeled with alphanumeric codes, and the overall layout is dense and intricate.

**Legend:**

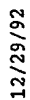
- 1: 1-bit data bus
- 2: 2-bit data bus
- 4: 4-bit data bus
- 8: 8-bit data bus
- 16: 16-bit data bus
- 32: 32-bit data bus
- 64: 64-bit data bus
- 128: 128-bit data bus
- 256: 256-bit data bus
- 512: 512-bit data bus
- 1024: 1024-bit data bus
- 2048: 2048-bit data bus
- 4096: 4096-bit data bus
- 8192: 8192-bit data bus
- 16384: 16384-bit data bus
- 32768: 32768-bit data bus
- 65536: 65536-bit data bus
- 131072: 131072-bit data bus
- 262144: 262144-bit data bus
- 524288: 524288-bit data bus
- 1048576: 1048576-bit data bus
- 2097152: 2097152-bit data bus
- 4194304: 4194304-bit data bus
- 8388608: 8388608-bit data bus
- 16777216: 16777216-bit data bus
- 33554432: 33554432-bit data bus
- 67108864: 67108864-bit data bus
- 134217728: 134217728-bit data bus
- 268435456: 268435456-bit data bus
- 536870912: 536870912-bit data bus
- 1073741824: 1073741824-bit data bus
- 2147483648: 2147483648-bit data bus
- 4294967296: 4294967296-bit data bus
- 8589934592: 8589934592-bit data bus
- 17179869184: 17179869184-bit data bus
- 34359738368: 34359738368-bit data bus
- 68719476736: 68719476736-bit data bus
- 137438953472: 137438953472-bit data bus
- 274877906944: 274877906944-bit data bus
- 549755813888: 549755813888-bit data bus
- 1099511627776: 1099511627776-bit data bus
- 2199023255552: 2199023255552-bit data bus
- 4398046511104: 4398046511104-bit data bus
- 8796093022208: 8796093022208-bit data bus
- 17592186044416: 17592186044416-bit data bus
- 35184372088832: 35184372088832-bit data bus
- 70368744177664: 70368744177664-bit data bus
- 140737488355328: 140737488355328-bit data bus
- 281474976710656: 281474976710656-bit data bus
- 562949953421312: 562949953421312-bit data bus
- 1125899906842624: 1125899906842624-bit data bus
- 2251799813685248: 2251799813685248-bit data bus
- 4503599627370496: 4503599627370496-bit data bus
- 9007199254740992: 9007199254740992-bit data bus
- 18014398509481984: 18014398509481984-bit data bus
- 36028797018963968: 36028797018963968-bit data bus
- 72057594037927936: 72057594037927936-bit data bus
- 144115188075855872: 144115188075855872-bit data bus
- 288230376151711744: 288230376151711744-bit data bus
- 576460752303423488: 576460752303423488-bit data bus
- 1152921504606846976: 1152921504606846976-bit data bus
- 2305843009213693952: 2305843009213693952-bit data bus
- 4611686018427387904: 4611686018427387904-bit data bus
- 9223372036854775808: 9223372036854775808-bit data bus
- 18446744073709551616: 18446744073709551616-bit data bus
- 36893488147419103232: 36893488147419103232-bit data bus
- 73786976294838206464: 73786976294838206464-bit data bus
- 147573952589676412928: 147573952589676412928-bit data bus
- 295147905179352825856: 295147905179352825856-bit data bus
- 590295810358705651712: 590295810358705651712-bit data bus
- 1180591620717411303424: 1180591620717411303424-bit data bus
- 2361183241434822606848: 2361183241434822606848-bit data bus
- 4722366482869645213696: 4722366482869645213696-bit data bus
- 9444732965739290427392: 9444732965739290427392-bit data bus
- 18889465931478580854784: 18889465931478580854784-bit data bus
- 37778931862957161709568: 37778931862957161709568-bit data bus
- 75557863725914323419136: 75557863725914323419136-bit data bus
- 151115727451828646838272: 151115727451828646838272-bit data bus
- 302231454903657293676544: 302231454903657293676544-bit data bus
- 604462909807314587353088: 604462909807314587353088-bit data bus
- 1208925819614629174706176: 1208925819614629174706176-bit data bus
- 2417851639229258349412352: 2417851639229258349412352-bit data bus
- 4835703278458516698824704: 4835703278458516698824704-bit data bus
- 9671406556917033397649408: 9671406556917033397649408-bit data bus
- 19342813113834066795298816: 19342813113834066795298816-bit data bus
- 38685626227668133590597632: 38685626227668133590597632-bit data bus
- 77371252455336267181195264: 77371252455336267181195264-bit data bus
- 154742504910672534362390528: 154742504910672534362390528-bit data bus
- 309485009821345068724781056: 309485009821345068724781056-bit data bus
- 618970019642690137449562112: 618970019642690137449562112-bit data bus
- 1237940039285380274899124224: 1237940039285380274899124224-bit data bus
- 2475880078570760549798248448: 2475880078570760549798248448-bit data bus
- 4951760157141521099596496896: 4951760157141521099596496896-bit data bus
- 9903520314283042199192993792: 9903520314283042199192993792-bit data bus
- 19807040628566084398385987584: 19807040628566084398385987584-bit data bus
- 39614081257132168796771975168: 39614081257132168796771975168-bit data bus
- 79228162514264337593543950336: 7922816251426

[illegible]

...and ... ..

CONFIDENTIAL INFORMATION - 100-5-10  
t.g. 80204A-EJ

~~F16.0205~~  
F16.0205

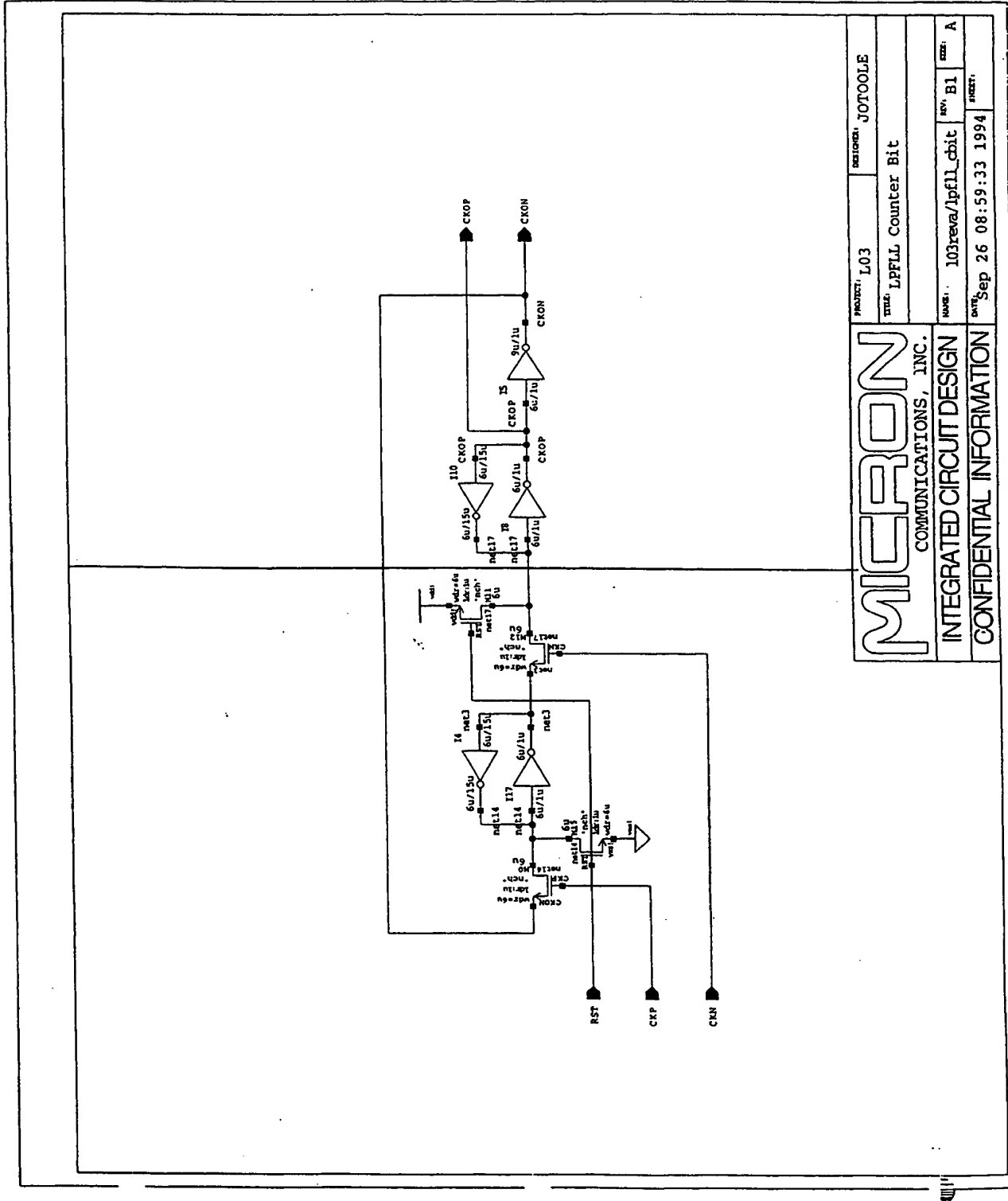


|                             |  |                                   |                   |
|-----------------------------|--|-----------------------------------|-------------------|
| MICRON COMMUNICATIONS, INC. |  | PROJECT: L03                      | DESIGNER: Rotzoll |
| INTEGRATED CIRCUIT DESIGN   |  | TITLE: Tuned Lockout Divider Cell |                   |
| CONFIDENTIAL INFORMATION    |  | MADE: 103rev(a)/bladel            | REV: A            |
|                             |  | DATE: Sep 22 15:26:56 1994        | SHEET: 1          |

8.03AB

### 8.03AA

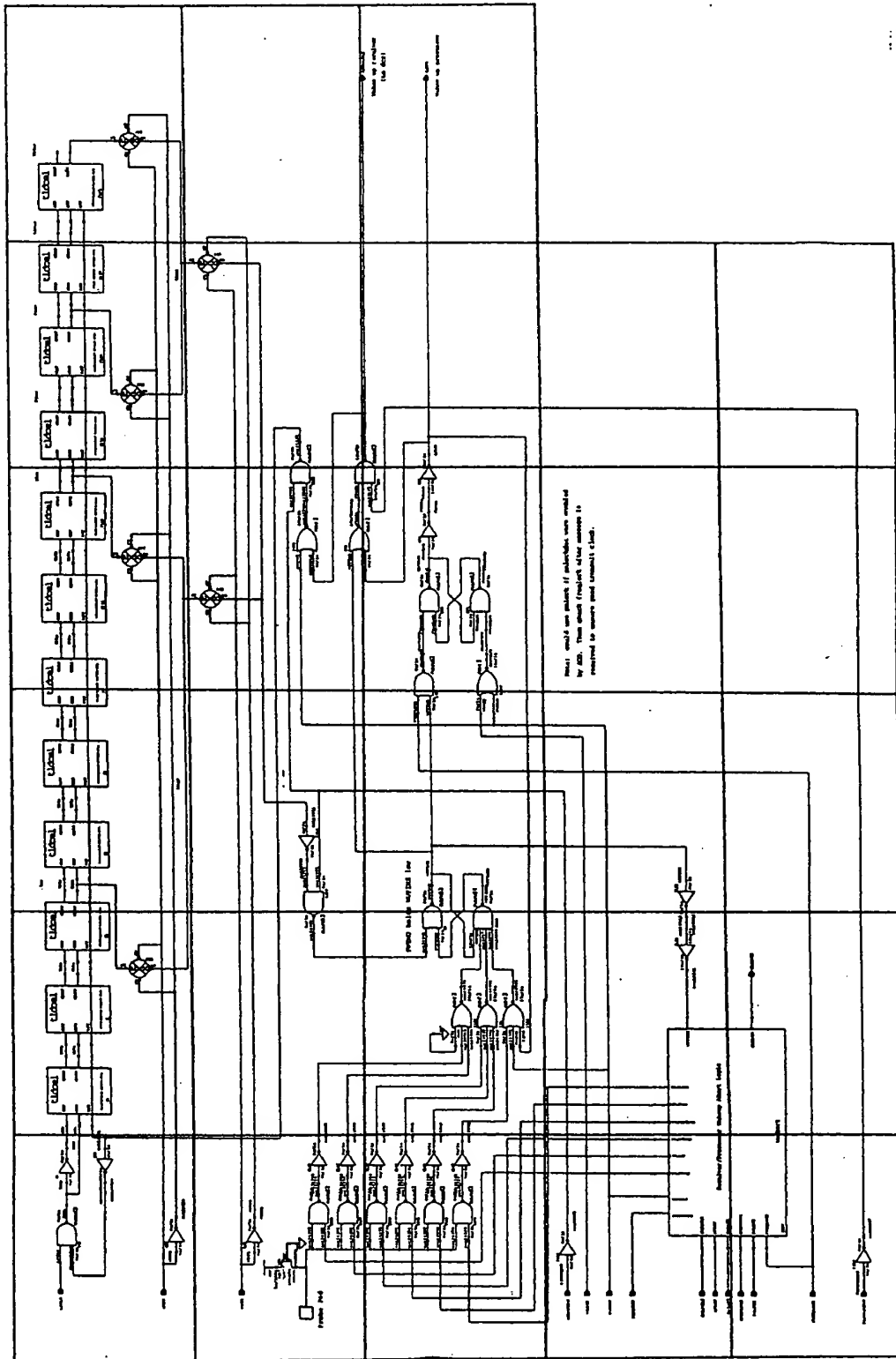
[illegible]



|                           |  |                            |                   |
|---------------------------|--|----------------------------|-------------------|
| MICRON                    |  | PROJECT: L03               | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | NAME: LPFLL Counter Bit    |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103revA/lpfll_chit   | REV: B1           |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 26 08:59:33 1994 | SIZE: A           |

|        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|
| 8.04AA | 8.04AB | 8.04AC | 8.04AD | 8.04AE | 8.04AF |
| 8.04BA | 8.04BB | 8.04BC | 8.04BD | 8.04BE | 8.04BF |
| 8.04CA | 8.04CB | 8.04CC | 8.04CD | 8.04CE | 8.04CF |
| 8.04DA | 8.04DB | 8.04DC | 8.04DD | 8.04DE |        |
| 8.04EA | 8.04EB | 8.04EC | 8.04ED | 8.04EE |        |

LEAD BB-0000

[illegible]

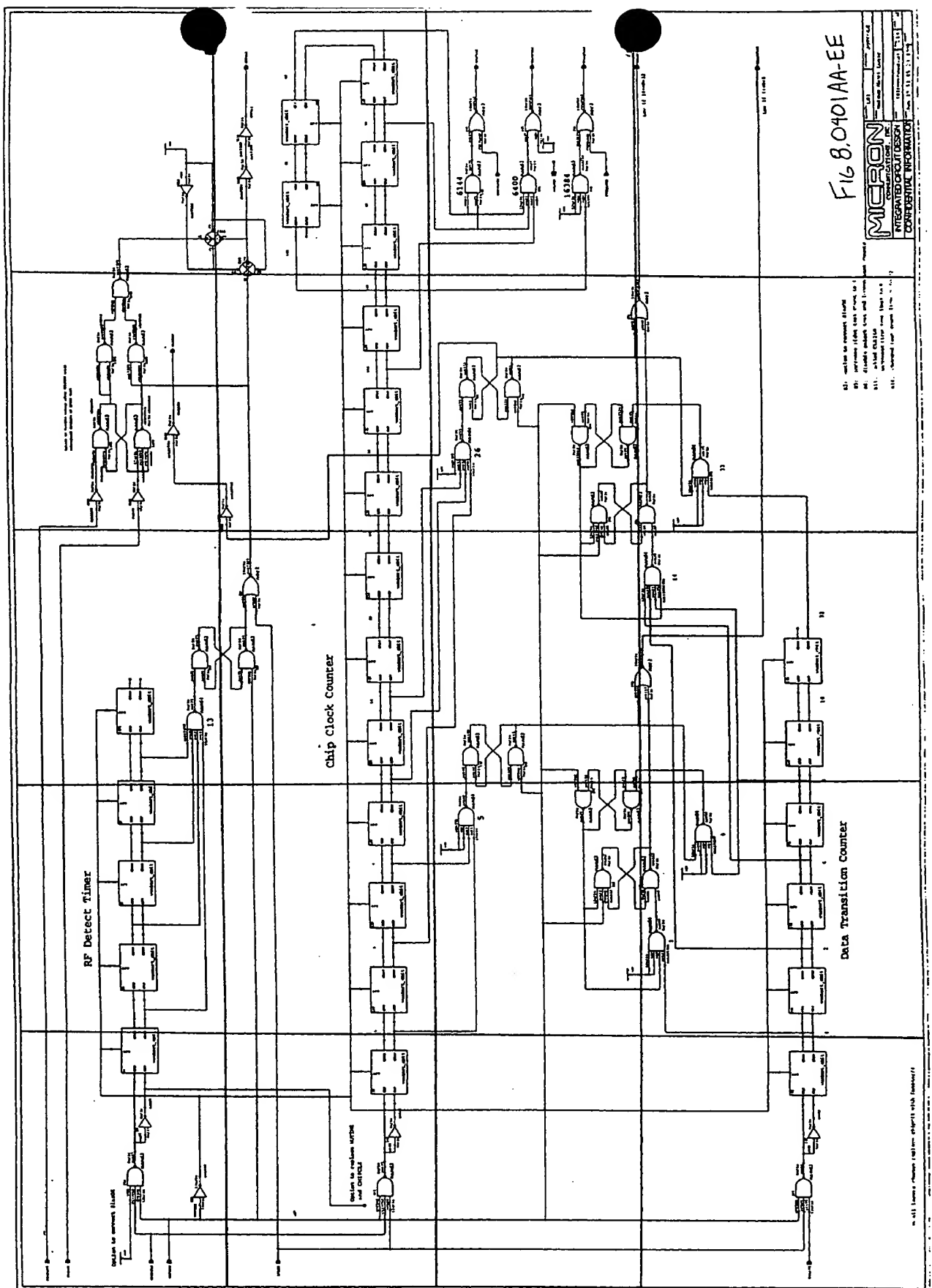
F16.8.04

[illegible]

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 8.0401AA | 8.0401AB | 8.0401AC | 8.0401AD | 8.0401AE |
| 8.0401BA | 8.0401BB | 8.0401BC | 8.0401BD | 8.0401BE |
| 8.0401CA | 8.0401CB | 8.0401CC | 8.0401CD | 8.0401CE |
| 8.0401DA | 8.0401DB | 8.0401DC | 8.0401DD | 8.0401DE |

EX-11

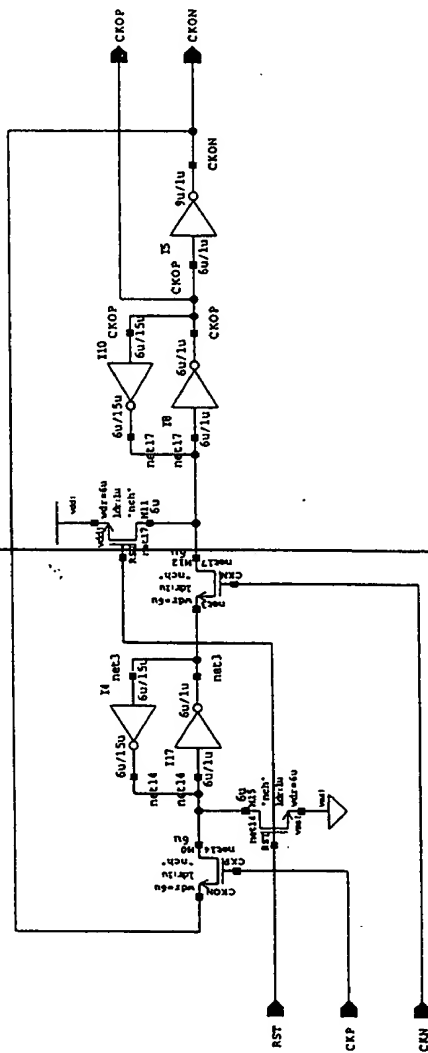
2000 2001 2002 2003 2004 2005 2006 2007 2008 2009 2010 2011 2012 2013 2014 2015 2016 2017 2018 2019 2020 2021 2022 2023 2024 2025 2026 2027 2028 2029 2030 2031 2032 2033 2034 2035 2036 2037 2038 2039 2040 2041 2042 2043 2044 2045 2046 2047 2048 2049 2050 2051 2052 2053 2054 2055 2056 2057 2058 2059 2060 2061 2062 2063 2064 2065 2066 2067 2068 2069 2070 2071 2072 2073 2074 2075 2076 2077 2078 2079 2080 2081 2082 2083 2084 2085 2086 2087 2088 2089 2090 2091 2092 2093 2094 2095 2096 2097 2098 2099 2100 2101 2102 2103 2104 2105 2106 2107 2108 2109 2110 2111 2112 2113 2114 2115 2116 2117 2118 2119 2120 2121 2122 2123 2124 2125 2126 2127 2128 2129 2130 2131 2132 2133 2134 2135 2136 2137 2138 2139 2140 2141 2142 2143 2144 2145 2146 2147 2148 2149 2150 2151 2152 2153 2154 2155 2156 2157 2158 2159 2160 2161 2162 2163 2164 2165 2166 2167 2168 2169 2170 2171 2172 2173 2174 2175 2176 2177 2178 2179 2180 2181 2182 2183 2184 2185 2186 2187 2188 2189 2190 2191 2192 2193 2194 2195 2196 2197 2198 2199 2200 2201 2202 2203 2204 2205 2206 2207 2208 2209 2210 2211 2212 2213 2214 2215 2216 2217 2218 2219 2220 2221 2222 2223 2224 2225 2226 2227 2228 2229 2230 2231 2232 2233 2234 2235 2236 2237 2238 2239 2240 2241 2242 2243 2244 2245 2246 2247 2248 2249 2250 2251 2252 2253 2254 2255 2256 2257 2258 2259 2260 2261 2262 2263 2264 2265 2266 2267 2268 2269 2270 2271 2272 2273 2274 2275 2276 2277 2278 2279 2280 2281 2282 2283 2284 2285 2286 2287 2288 2289 2290 2291 2292 2293 2294 2295 2296 2297 2298 2299 2300 2301 2302 2303 2304 2305 2306 2307 2308 2309 2310 2311 2312 2313 2314 2315 2316 2317 2318 2319 2320 2321 2322 2323 2324 2325 2326 2327 2328 2329 2330 2331 2332 2333 2334 2335 2336 2337 2338 2339 2340 2341 2342 2343 2344 2345 2346 2347 2348 2349 2350 2351 2352 2353 2354 2355 2356 2357 2358 2359 2360 2361 2362 2363 2364 2365 2366 2367 2368 2369 2370 2371 2372 2373 2374 2375 2376 2377 2378 2379 2380 2381 2382 2383 2384 2385 2386 2387 2388 2389 2390 2391 2392 2393 2394 2395 2396 2397 2398 2399 2400 2401 2402 2403 2404 2405 2406 2407 2408 2409 2410 2411 2412 2413 2414 2415 2416 2417 2418 2419 2420 2421 2422 2423 2424 2425 2426 2427 2428 2429 2430 2431 2432 2433 2434 2435 2436 2437 2438 2439 2440 2441 2442 2443 2444 2445 2446 2447 2448 2449 2450 2451 2452 2453 2454 2455 2456 2457 2458 2459 2460 2461 2462 2463 2464 2465 2466 2467 2468 2469 2470 2471 2472 2473 2474 2475 2476 2477 2478 2479 2480 2481 2482 2483 2484 2485 2486 2487 2488 2489 2490 2491 2492 2493 2494 2495 2496 2497 2498 2499 2500 2501 2502 2503 2504 2505 2506 2507 2508 2509 2510 2511 2512 2513 2514 2515 2516 2517 2518 2519 2520 2521 2522 2523 2524 2525 2526 2527 2528 2529 2530 2531 2532 2533 2534 2535 2536 2537 2538 2539 2540 2541 2542 2543 2544 2545 2546 2547 2548 2549 2550 2551 2552 2553 2554 2555 2556 2557 2558 2559 2560 2561 2562 2563 2564 2565 2566 2567 2568 2569 2570 2571 2572 2573 2574 2575 2576 2577 2578 2579 2580 2581 2582 2583 2584 2585 2586 2587 2588 2589 2590 2591 2592 2593 2594 2595 2596 2597 2598 2599 2600 2601 2602 2603 2604 2605 2606 2607 2608 2609 2610 2611 2612 2613 2614 2615 2616 2617 2618 2619 2620 2621 2622 2623 2624 2625 2626 2627 2628 2629 2630 2631 2632 2633 2634 2635 2636 2637 2638 2639 2640 2641 2642 2643 2644 2645 2646 2647 2648 2649 2650 2651 2652 2653 2654 2655 2656 2657 2658 2659 2660 2661 2662 2663 2664 2665 2666 2667 2668 2669 2670 2671 2672 2673 2674 2675 2676 2677 2678 2679 2680 2681 2682 2683 2684 2685 2686 2687 2688 2689 2690 2691 2692 2693 2694 2695 2696 2697 2698 2699 2700 2701 2702 2703 2704 2705 2706 2707 2708 2709 2710 2711 2712 2713 2714 2715 2716 2717 2718 2719 2720 2721 2722 2723 2724 2725 2726 2727 2728 2729 2730 2731 2732 2733 2734 2735 2736 2737 2738 2739 2740 2741 2742 2743 2744 2745 2746 2747 2748 2749 2750 2751 2752 2753 2754 2755 2756 2757 2758 2759 2760 2761 2762 2763 2764 2765 2766 2767 2768 2769 2770 2771 2772 2773 2774 2775 2776 2777 2778 2779 2780 2781 2782 2783 2784 2785 2786 2787 2788 2789 2790 2791 2792 2793 2794 2795 2796 2797 2798 2799 2800 2801 2802 2803 2804 2805 2806 2807 2808 2809 2810 2811 2812 2813 2814 2815 2816 2817 2





|            |            |
|------------|------------|
| 8.040101AA | 8.040101AB |
|------------|------------|

[illegible]



|               |  |                      |                                |          |         |
|---------------|--|----------------------|--------------------------------|----------|---------|
| <b>MICRON</b> |  | PROJECT              | L03                            | DESIGNER | JOTOOLE |
|               |  | TITLE                | Wakeup Abort Logic Counter Bit |          |         |
|               |  |                      |                                |          |         |
|               |  | NAME                 | j03revra/walabart_cbit         | REV      | B1      |
|               |  | DATE                 |                                |          |         |
|               |  | Sep 26 08:40:51 1994 |                                |          |         |
|               |  | DESIGN               |                                |          |         |
|               |  | A                    |                                |          |         |

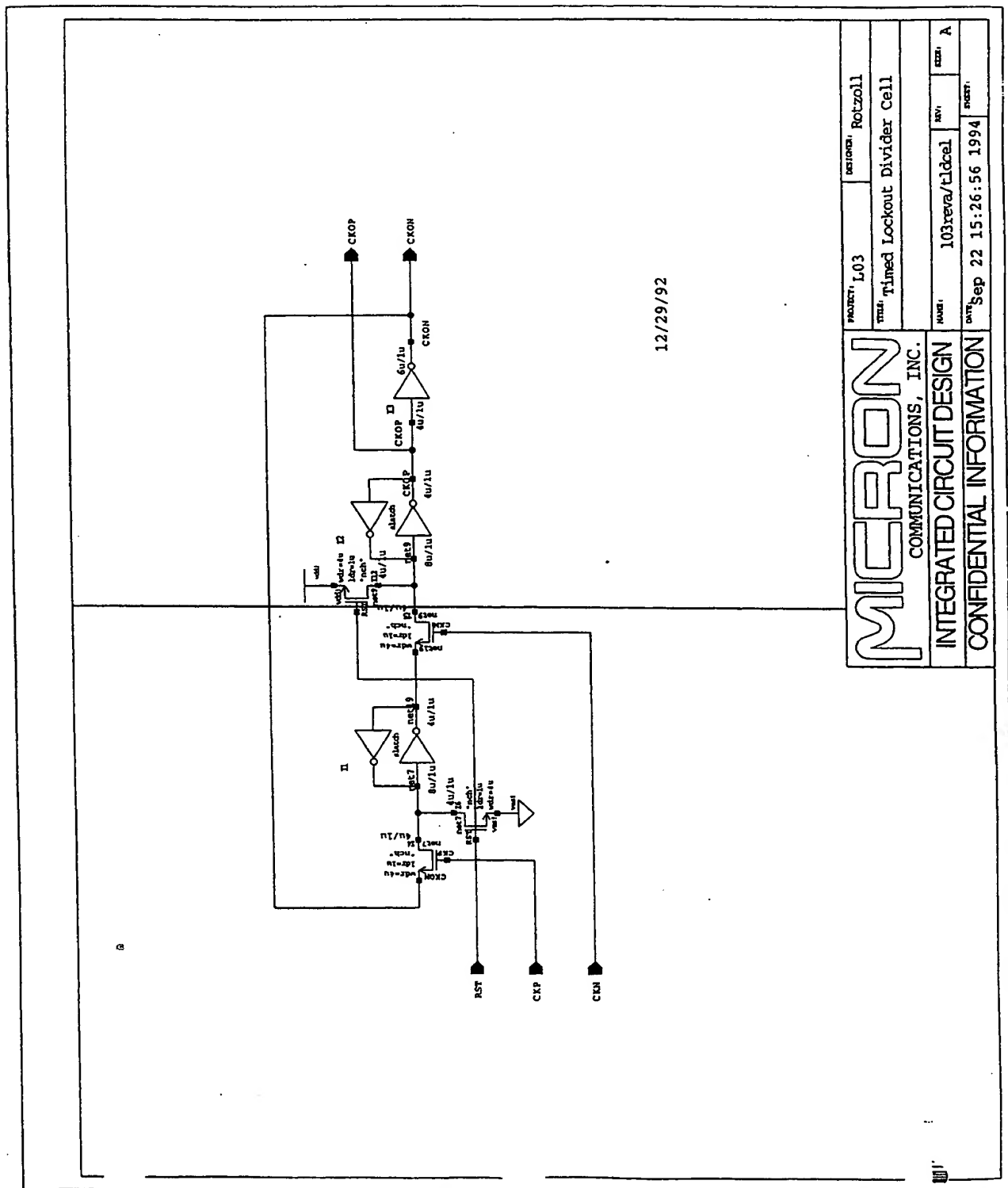
Fig. 8.090101

8.0402AB

8.0402AA

FILED 8.0402

CONFIDENTIAL



|                           |  |          |  |                      |  |                            |  |     |  |     |  |     |  |     |  |
|---------------------------|--|----------|--|----------------------|--|----------------------------|--|-----|--|-----|--|-----|--|-----|--|
| MICRON                    |  | DESIGNER |  | Rotzoll              |  |                            |  |     |  |     |  |     |  |     |  |
| COMMUNICATIONS, INC.      |  | PROJECT  |  | L03                  |  |                            |  |     |  |     |  |     |  |     |  |
| INTEGRATED CIRCUIT DESIGN |  | TITLE    |  |                      |  | Timed Lockout Divider Cell |  |     |  |     |  |     |  |     |  |
| CONFIDENTIAL INFORMATION  |  | DATE     |  | 103:eva/tldcel       |  | REV                        |  | REV |  | REV |  | REV |  | REV |  |
|                           |  | DATE     |  | Sep 22 15:26:56 1994 |  | REV                        |  | REV |  | REV |  | REV |  | REV |  |

FIG. 8.0402

|        |        |        |        |        |
|--------|--------|--------|--------|--------|
| 8.05AA | 8.05AB | 8.05AC | 8.05AD | 8.05AE |
| 8.05BA | 8.05BB | 8.05BC | 8.05BD | 8.05BE |
| 8.05CA | 8.05CB | 8.05CC | 8.05CD | 8.05CE |
| 8.05DA | 8.05DB | 8.05DC | 8.05DD | 8.05DE |

**Fig. 8.05**

**LEGEND**

| Symbol   | Description |
|----------|-------------|
| (Symbol) | AND Gate    |
| (Symbol) | OR Gate     |
| (Symbol) | NOT Gate    |
| (Symbol) | Flip-Flop   |
| (Symbol) | Multiplexer |

**COMPONENTS**

| Part Number | Quantity | Description |
|-------------|----------|-------------|
| 7401        | 1        | AND Gate    |
| 7402        | 1        | OR Gate     |
| 7403        | 1        | NOT Gate    |
| 7404        | 1        | Flip-Flop   |
| 7405        | 1        | Multiplexer |

```

100: altered run type
101: delayed AEP and AEP
102:
103:
104:
105:
106:
107:
108:
109:
110:
111:

```

Fig. 8.05

**MICRON**  
 INTEGRATED CIRCUIT DESIGN  
 CORPORATION  
 3801 Central Expressway  
 Santa Clara, CA 95051  
 (415) 351-5000

NAME  TITLE

COMPANY

ADDRESS

CITY  STATE  ZIP

TELEPHONE

FAX

E-MAIL

DATE

TIME

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 8.0501AA | 8.0501AB | 8.0501AC | 8.0501AD | 8.0501AE |
| 8.0501BA | 8.0501BB | 8.0501BC | 8.0501BD | 8.0501BE |

И.О.С.И.И.

700620 29062000

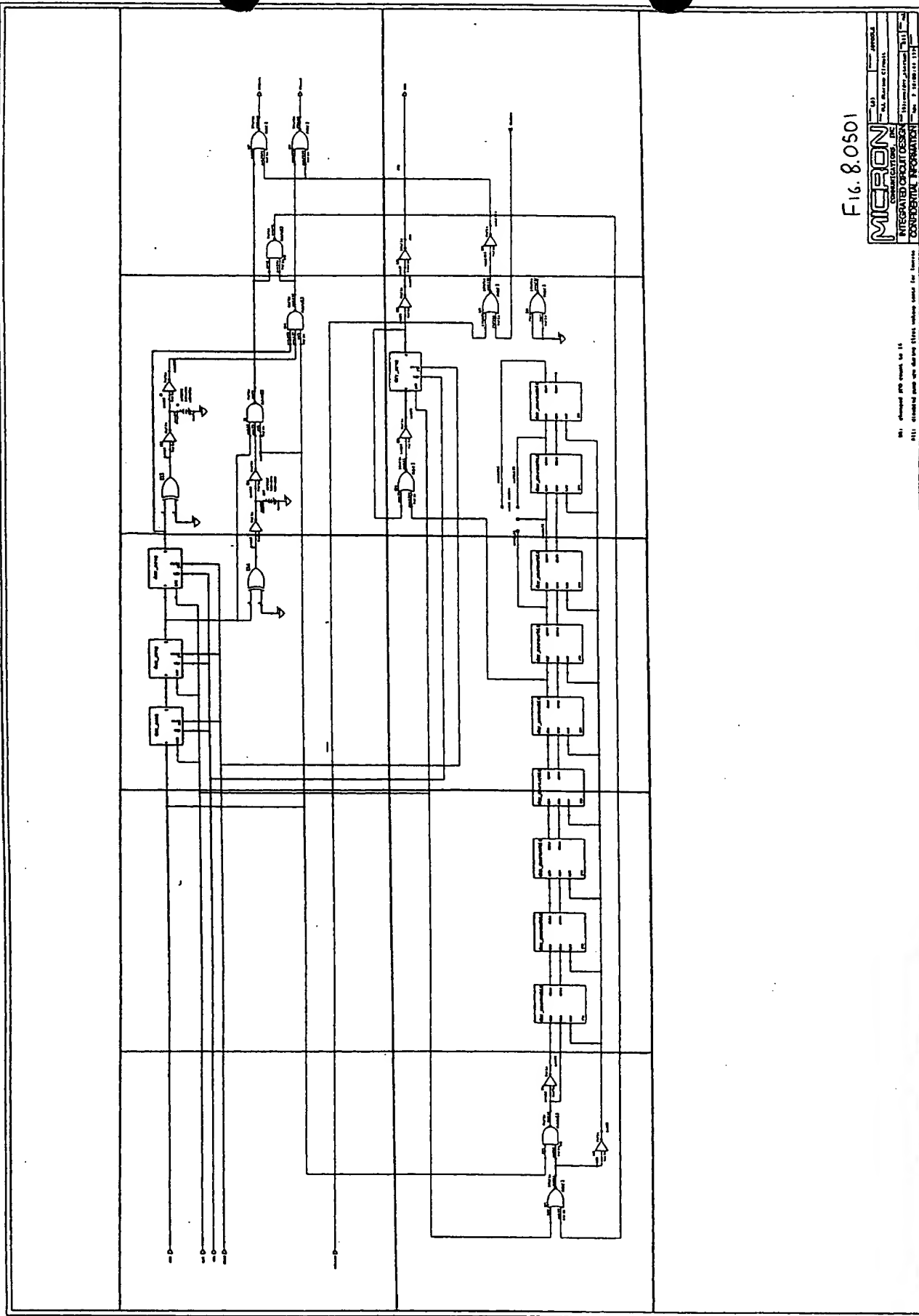


Fig. 8.0501

|                           |             |
|---------------------------|-------------|
| MICRON                    |             |
| INTEGRATED CIRCUIT DESIGN |             |
| CONFIDENTIAL INFORMATION  |             |
| DATE                      | APPROVED    |
| BY                        | BY          |
| DESIGNED BY               | DESIGNED BY |
| CHECKED BY                | CHECKED BY  |
| DATE                      | DATE        |

NOT REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT PERMISSION IN WRITING FROM MICRON TECHNOLOGY, INC.

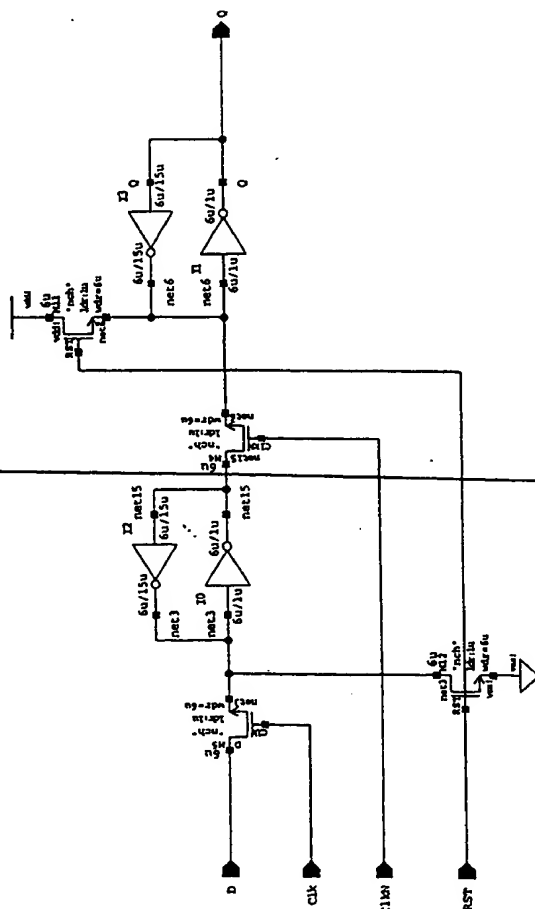


FOUO ESOEBO

|            |            |
|------------|------------|
| 8.050101AA | 8.050101AB |
|------------|------------|

II II ESOEBO II II

FIG. 8.050101



|                            |  |                           |  |                  |  |
|----------------------------|--|---------------------------|--|------------------|--|
| <b>MICRON</b>              |  | PART: LC3                 |  | SECTION: JOTOOLE |  |
|                            |  | TITLE Shift Register Cell |  |                  |  |
|                            |  |                           |  |                  |  |
| NAME: 103reva/dcr_sreg     |  | REV: B1                   |  | A                |  |
| DATE: Aug 31 14:25:03 1994 |  | DISTR:                    |  |                  |  |

**8.050102AA**

2015.8.15

FIG. 8.050102

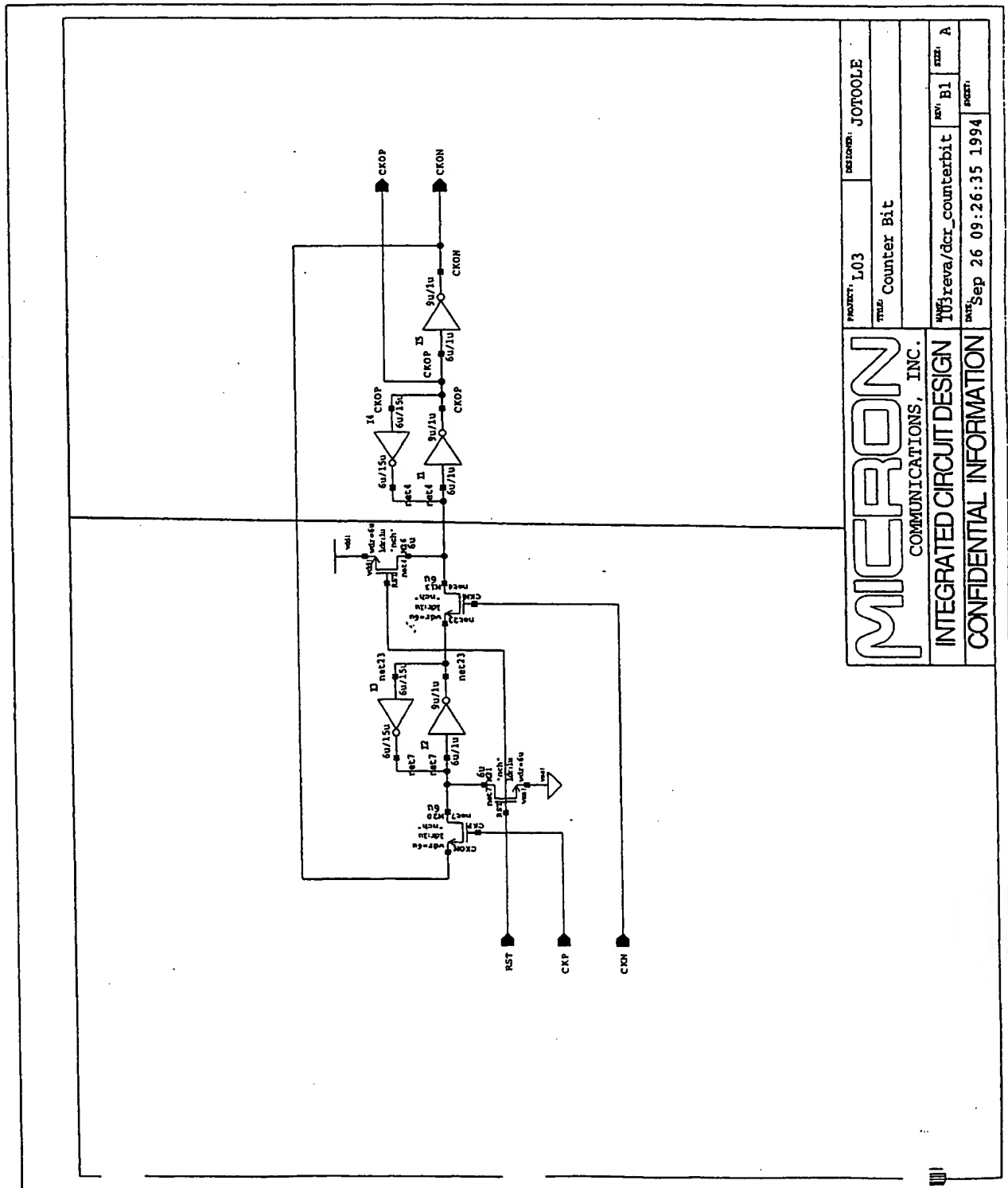


FIG. 8.050102

|          |          |          |          |
|----------|----------|----------|----------|
| 8.0502AA | 8.0502AB | 8.0502AC | 8.0502AD |
| 8.0502BA | 8.0502BB | 8.0502BC | 8.0502BD |
| 8.0502CA | 8.0502CB | 8.0502CC | 8.0502CD |

[illegible]

TABLE "C" 8000000000

|          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.05034A | 8.05034B | 8.05034C | 8.05034D | 8.05034E | 8.05034F | 8.05034G | 8.05034H | 8.05034I | 8.05034J | 8.05034K | 8.05034L | 8.05034M | 8.05034N | 8.05034O |
| 8.05038A | 8.05038B | 8.05038C | 8.05038D | 8.05038E | 8.05038F | 8.05038G | 8.05038H | 8.05038I | 8.05038J | 8.05038K | 8.05038L | 8.05038M | 8.05038N | 8.05038O |
| 8.05032A | 8.05032B | 8.05032C | 8.05032D | 8.05032E | 8.05032F | 8.05032G | 8.05032H | 8.05032I | 8.05032J | 8.05032K | 8.05032L | 8.05032M | 8.05032N | 8.05032O |
| 8.05030A | 8.05030B | 8.05030C | 8.05030D | 8.05030E | 8.05030F | 8.05030G | 8.05030H | 8.05030I | 8.05030J | 8.05030K | 8.05030L | 8.05030M | 8.05030N | 8.05030O |
| 8.05036A | 8.05036B | 8.05036C | 8.05036D | 8.05036E | 8.05036F | 8.05036G | 8.05036H | 8.05036I | 8.05036J | 8.05036K | 8.05036L | 8.05036M | 8.05036N |          |
| 8.05035A | 8.05035B | 8.05035C | 8.05035D | 8.05035E | 8.05035F | 8.05035G | 8.05035H | 8.05035I | 8.05035J | 8.05035K | 8.05035L | 8.05035M | 8.05035N |          |

TABLE "C" 8000000000





"0000" 00000000

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 8.0504AA | 8.0504AB | 8.0504AC | 8.0504AD |          |
| 8.0504BA | 8.0504BB | 8.0504BC | 8.0504BD |          |
| 8.0506CA | 8.0504CB | 8.0504CC | 8.0504CD | 8.0504CE |
| 8.0504DA | 8.0504DB | 8.0504DC | 8.0504DD | 8.0504DE |
| 8.0504EA | 8.0504EB | 8.0504EC | 8.0504ED | 8.0504EE |

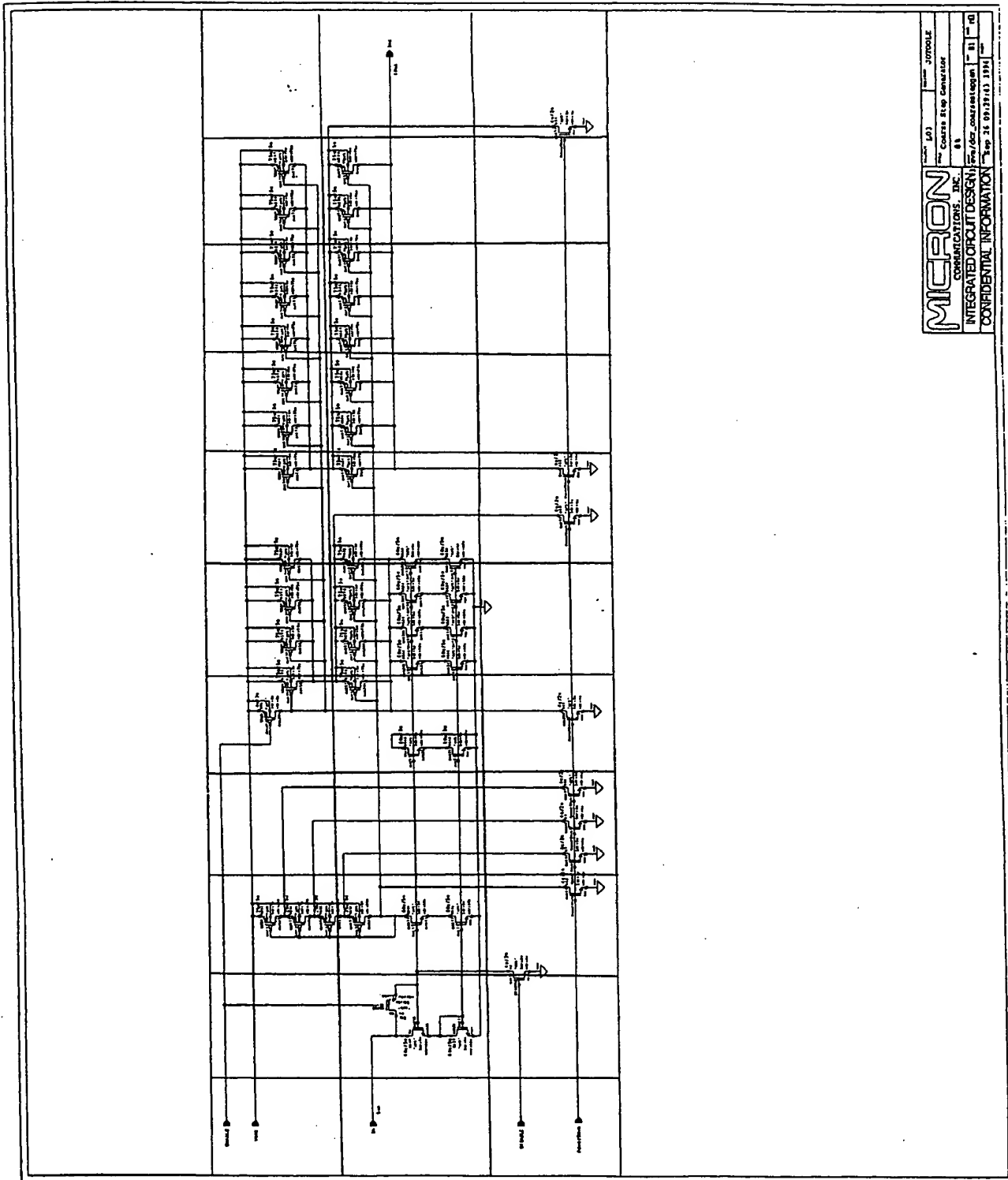
BB.00500000



|            |            |            |            |            |            |            |            |            |            |
|------------|------------|------------|------------|------------|------------|------------|------------|------------|------------|
| 8.050401AA | 8.050401AB | 8.050401AC | 8.050401AD | 8.050401AE | 8.050401AF | 8.050401AG | 8.050401AH | 8.050401AJ | 8.050401AJ |
| 8.050401BA | 8.050401BB | 8.050401BC | 8.050401BD | 8.050401BE | 8.050401BF | 8.050401BG | 8.050401BH | 8.050401BI | 8.050401BJ |
| 8.050401CA | 8.050401CB | 8.050401CC | 8.050401CD | 8.050401CE | 8.050401CF | 8.050401CG | 8.050401CH | 8.050401CI | 8.050401CJ |
|            |            |            |            |            |            |            |            |            | 8.050401CK |

11 11 11 11 11 11 11 11 11 11

FOUO' e9022600



|                           |          |
|---------------------------|----------|
| <b>MICRON</b>             |          |
| COMMUNICATIONS, INC.      |          |
| INTEGRATED CIRCUIT DESIGN |          |
| CONFIDENTIAL INFORMATION  |          |
| Part No. 643              | Rev. 1.0 |
| Counter Step Generator    |          |
| Pin 1                     | Pin 2    |
| Sep 26 09:29:43 1984      |          |

Fig 8050401

|            |            |            |            |            |            |            |            |            |            |
|------------|------------|------------|------------|------------|------------|------------|------------|------------|------------|
| 8.050402AA | 8.050402AB | 8.050402AC | 8.050402AD | 8.050402AE | 8.050402AF | 8.050402AG | 8.050402AH | 8.050402AI | 8.050402AJ |
| 8.050402BA | 8.050402BB | 8.050402BC | 8.050402BD | 8.050402BE | 8.050402BF | 8.050402BG | 8.050402BH | 8.050402BI | 8.050402BJ |
| 8.050402CA | 8.050402CB | 8.050402CC | 8.050402CD | 8.050402CE | 8.050402CF | 8.050402CG | 8.050402CH | 8.050402CI | 8.050402CJ |

8.050402

FIG. 8.050402

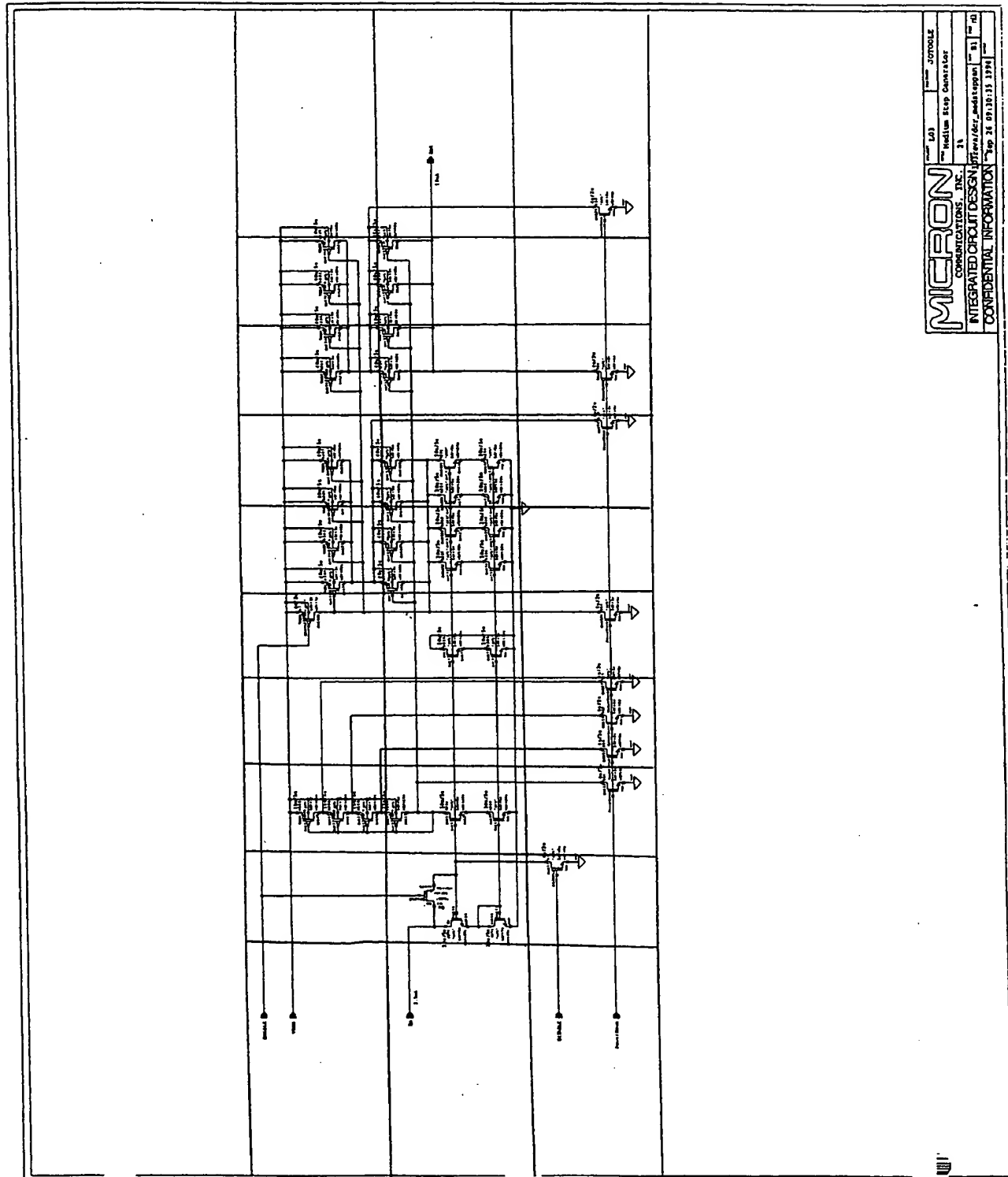


FIG. 8.050402

8.050403AA 8.050403AB 8.050403AC 8.050403AD 8.050403AE 8.050403AF 8.050403AG 8.050403AH 8.050403AI

|            |            |            |            |            |            |            |            |            |
|------------|------------|------------|------------|------------|------------|------------|------------|------------|
| 8.050403AA | 8.050403AB | 8.050403AC | 8.050403AD | 8.050403AE | 8.050403AF | 8.050403AG | 8.050403AH | 8.050403AI |
| 8.050403BA | 8.050403BB | 8.050403BC | 8.050403BD | 8.050403BE | 8.050403BF | 8.050403BG | 8.050403BH | 8.050403BI |

8.050403

FORM 8-66

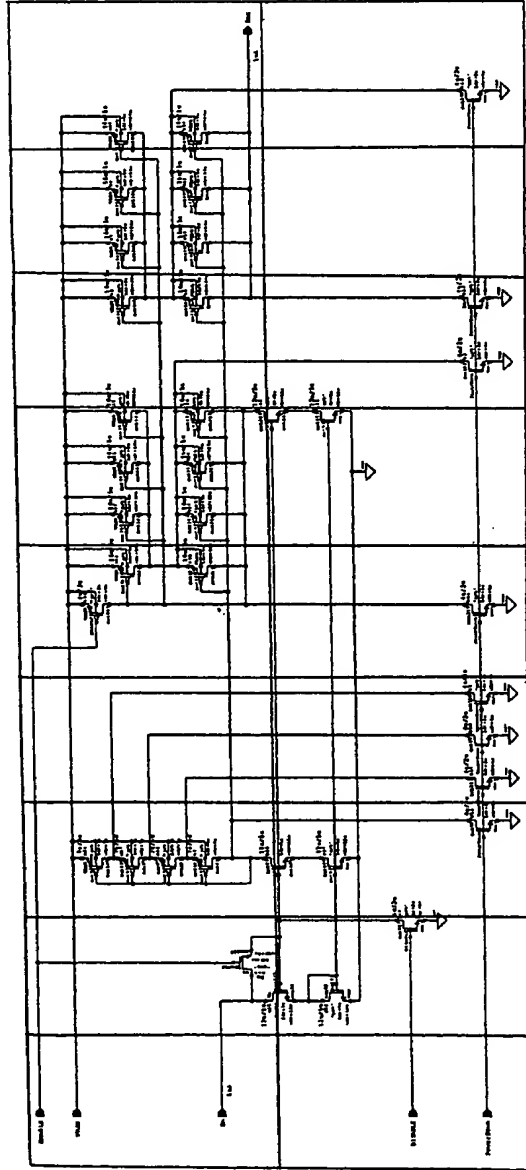


Fig. 8.050403

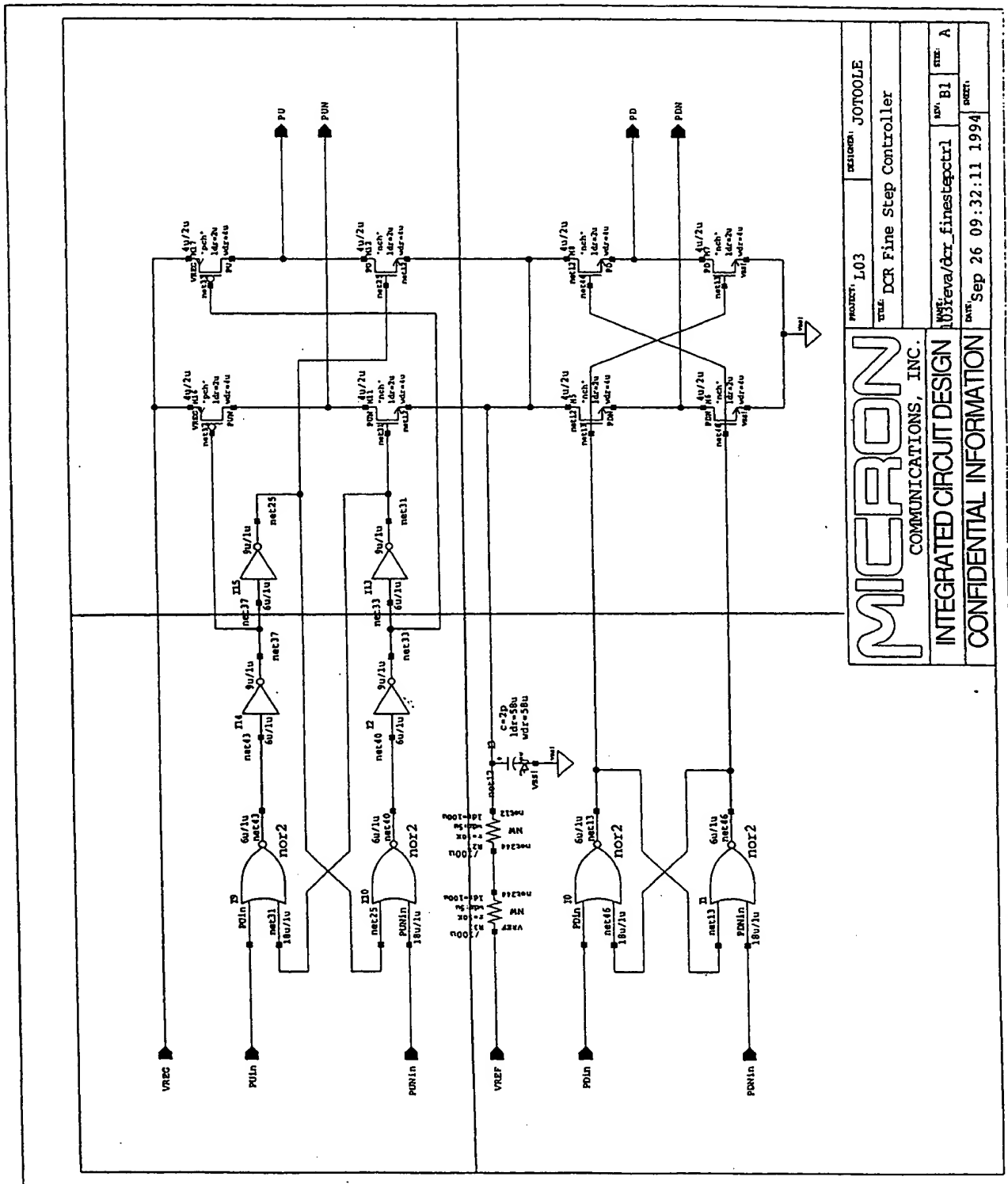
|                           |  |                            |  |        |  |
|---------------------------|--|----------------------------|--|--------|--|
| <b>MICRON</b>             |  | L43                        |  | JOTULE |  |
| INTEGRATED CIRCUIT DESIGN |  | Medium Fine Step Generator |  | 8.23   |  |
| CONFIDENTIAL INFORMATION  |  | Sep 28 09:11:18 1974       |  | a1     |  |



"COEE" E90E2B60

|            |            |
|------------|------------|
| 8.050404AA | 8.050404AB |
| 8.050404BA | 8.050404BB |

II II 88.050404



|                           |  |                                 |                   |
|---------------------------|--|---------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                    | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: DCR Fine Step Controller |                   |
| INTEGRATED CIRCUIT DESIGN |  | REV: B1                         | SIZE: A           |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 26 09:32:11 1994      | SHEET: 1          |

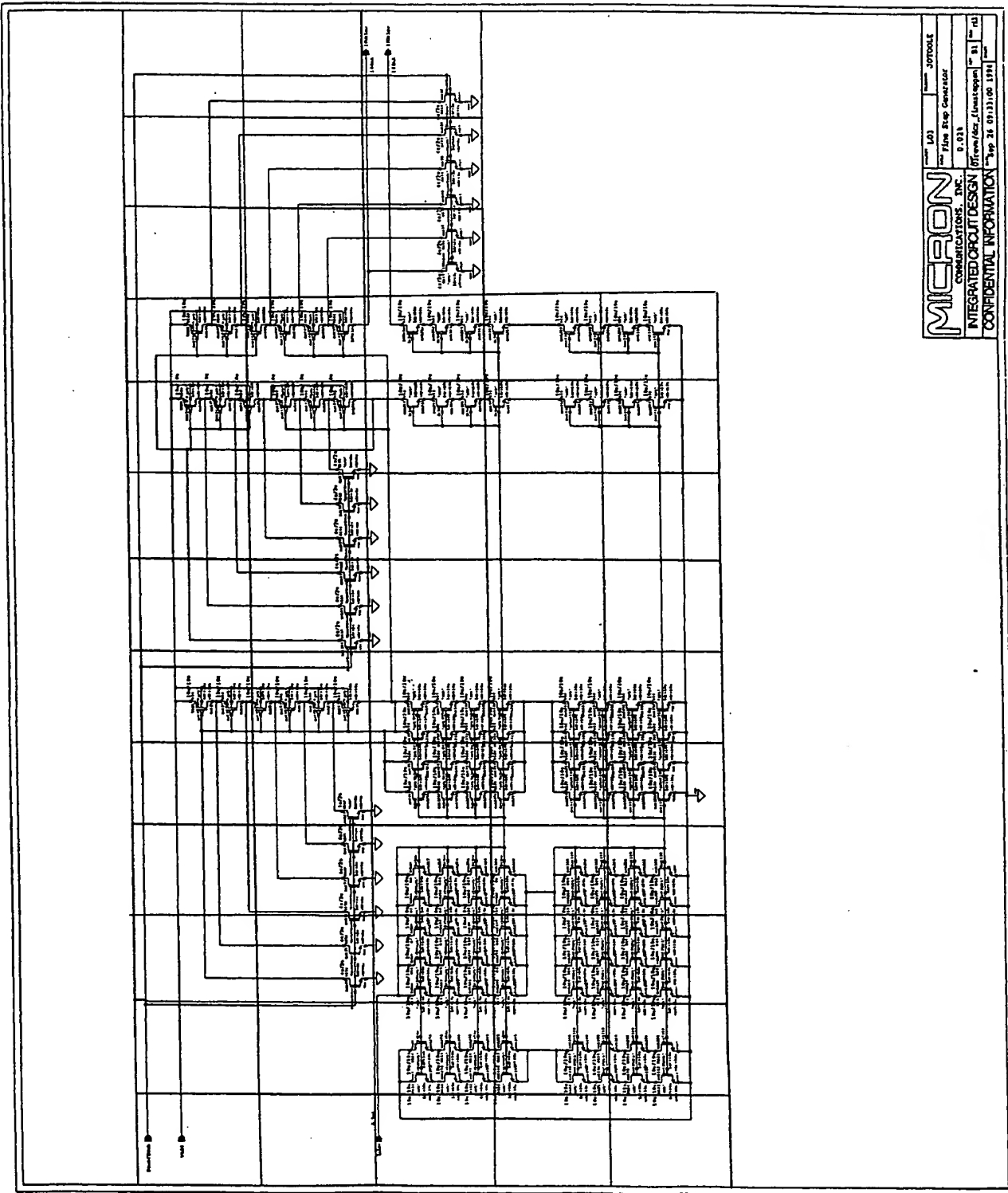
Fig. 8.050409

8.0504054A 8.0504054B 8.0504054C 8.0504054D 8.0504054E 8.0504054F 8.0504054G 8.0504054H 8.0504054J 8.0504054K 8.0504054L 8.0504054M

|            |            |            |            |            |            |            |            |            |            |            |            |
|------------|------------|------------|------------|------------|------------|------------|------------|------------|------------|------------|------------|
| 8.0504054A | 8.0504054B | 8.0504054C | 8.0504054D | 8.0504054E | 8.0504054F | 8.0504054G | 8.0504054H | 8.0504054J | 8.0504054K | 8.0504054L | 8.0504054M |
| 8.0504054A | 8.0504054B | 8.0504054C | 8.0504054D | 8.0504054E | 8.0504054F | 8.0504054G | 8.0504054H | 8.0504054J | 8.0504054K | 8.0504054L | 8.0504054M |
| 8.0504054A | 8.0504054B | 8.0504054C | 8.0504054D | 8.0504054E | 8.0504054F | 8.0504054G | 8.0504054H | 8.0504054J | 8.0504054K | 8.0504054L | 8.0504054M |
| 8.0504054A | 8.0504054B | 8.0504054C | 8.0504054D | 8.0504054E | 8.0504054F | 8.0504054G | 8.0504054H | 8.0504054J | 8.0504054K | 8.0504054L | 8.0504054M |
| 8.0504054A | 8.0504054B | 8.0504054C | 8.0504054D | 8.0504054E | 8.0504054F | 8.0504054G | 8.0504054H | 8.0504054J | 8.0504054K | 8.0504054L | 8.0504054M |

8.0504054A 8.0504054B 8.0504054C 8.0504054D 8.0504054E 8.0504054F 8.0504054G 8.0504054H 8.0504054J 8.0504054K 8.0504054L 8.0504054M

TOP SECRET



|                           |  |                          |                |
|---------------------------|--|--------------------------|----------------|
| <b>MICRON</b>             |  | LOT                      | J07004E        |
| CORPORATION, INC.         |  | File Name                | Chip Generator |
| INTEGRATED CIRCUIT DESIGN |  | 0.023                    |                |
| CONFIDENTIAL INFORMATION  |  | Offices/Rev./Last Update | 01 01 02       |
|                           |  | Page 26                  | 01/11/00 1111  |

FIG 8050405

|          |          |          |          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.05054A | 8.05054B | 8.05054C | 8.05054D | 8.05054E | 8.05054F | 8.05054G | 8.05054H | 8.05054I | 8.05054J | 8.05054K | 8.05054L | 8.05054M | 8.05054N |
| 8.05055A | 8.05055B | 8.05055C | 8.05055D | 8.05055E | 8.05055F | 8.05055G | 8.05055H | 8.05055I | 8.05055J | 8.05055K | 8.05055L | 8.05055M | 8.05055N |
| 8.05056A | 8.05056B | 8.05056C | 8.05056D | 8.05056E | 8.05056F | 8.05056G | 8.05056H | 8.05056I | 8.05056J | 8.05056K | 8.05056L | 8.05056M | 8.05056N |
| 8.05057A | 8.05057B | 8.05057C | 8.05057D | 8.05057E | 8.05057F | 8.05057G | 8.05057H | 8.05057I | 8.05057J | 8.05057K | 8.05057L | 8.05057M | 8.05057N |
| 8.05058A | 8.05058B | 8.05058C | 8.05058D | 8.05058E | 8.05058F | 8.05058G | 8.05058H | 8.05058I | 8.05058J | 8.05058K | 8.05058L | 8.05058M | 8.05058N |
| 8.05059A | 8.05059B | 8.05059C | 8.05059D | 8.05059E | 8.05059F | 8.05059G | 8.05059H | 8.05059I | 8.05059J | 8.05059K | 8.05059L | 8.05059M | 8.05059N |
| 8.05060A | 8.05060B | 8.05060C | 8.05060D | 8.05060E | 8.05060F | 8.05060G | 8.05060H | 8.05060I | 8.05060J | 8.05060K | 8.05060L | 8.05060M | 8.05060N |
| 8.05061A | 8.05061B | 8.05061C | 8.05061D | 8.05061E | 8.05061F | 8.05061G | 8.05061H | 8.05061I | 8.05061J | 8.05061K | 8.05061L | 8.05061M | 8.05061N |

FOOED "E30EE800

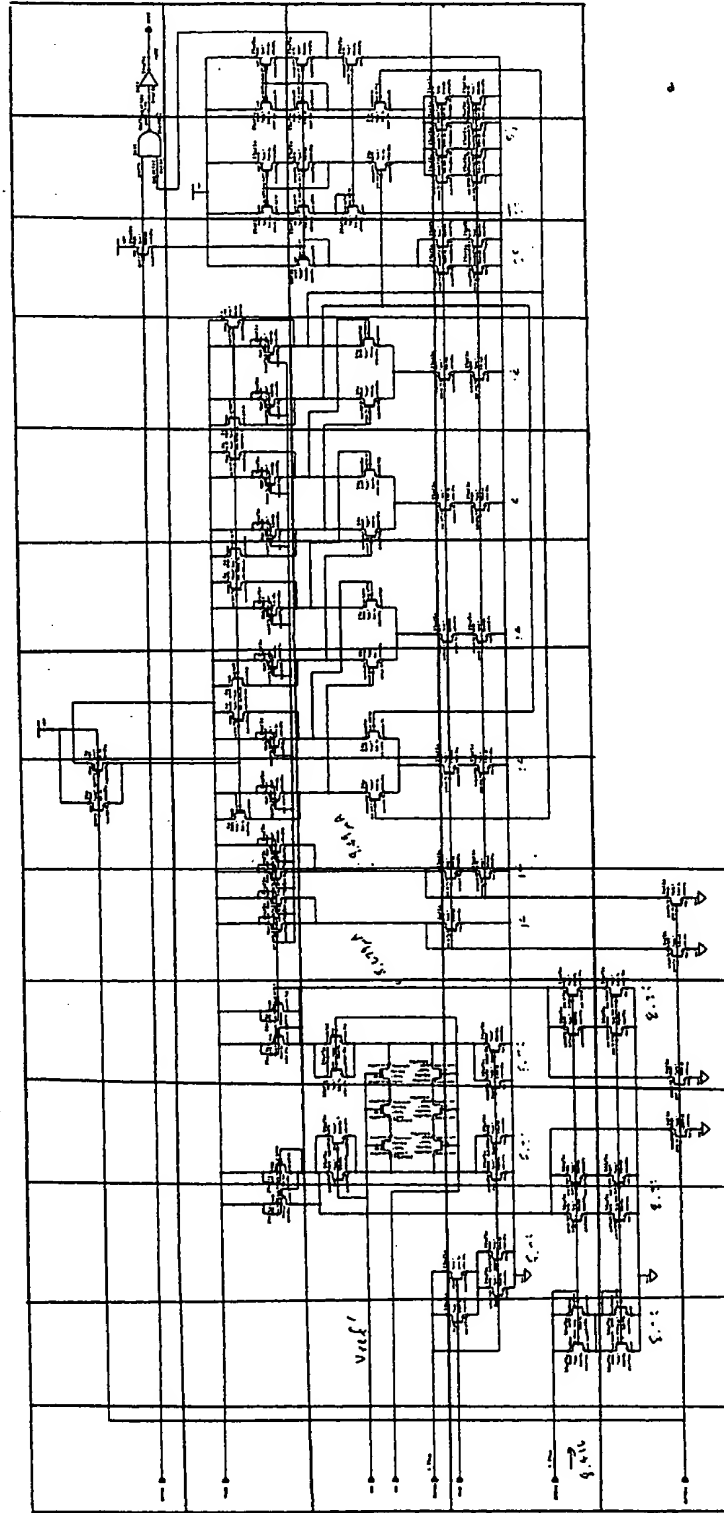


FIG. 8.0505

277A

Page 2 of 2

|          |          |
|----------|----------|
| 8.0506AA | 8.0506AB |
| 8.0506BA | 8.0506BB |

Page 2 of 2





FIG. 8.050601

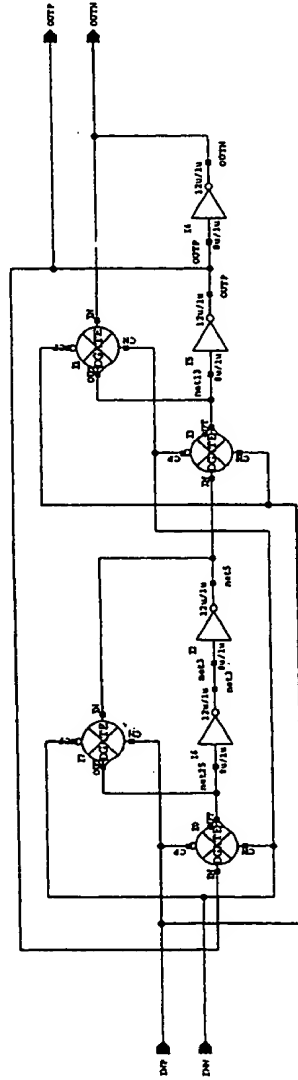


FIG. 8.050601

|                           |  |                         |                |         |
|---------------------------|--|-------------------------|----------------|---------|
| MICRON                    |  | PROJECT: L03            | DATE: 09/26/94 | J0700LE |
| COMMUNICATIONS, INC.      |  | Rx Clock Generator      |                |         |
| INTEGRATED CIRCUIT DESIGN |  | Flip-Flop               |                |         |
| CONFIDENTIAL INFORMATION  |  | T001revA/dcr_poc1ugentf |                |         |
|                           |  | Rev. B1                 |                |         |
|                           |  | Sep 26 09:36:05 1994    |                |         |

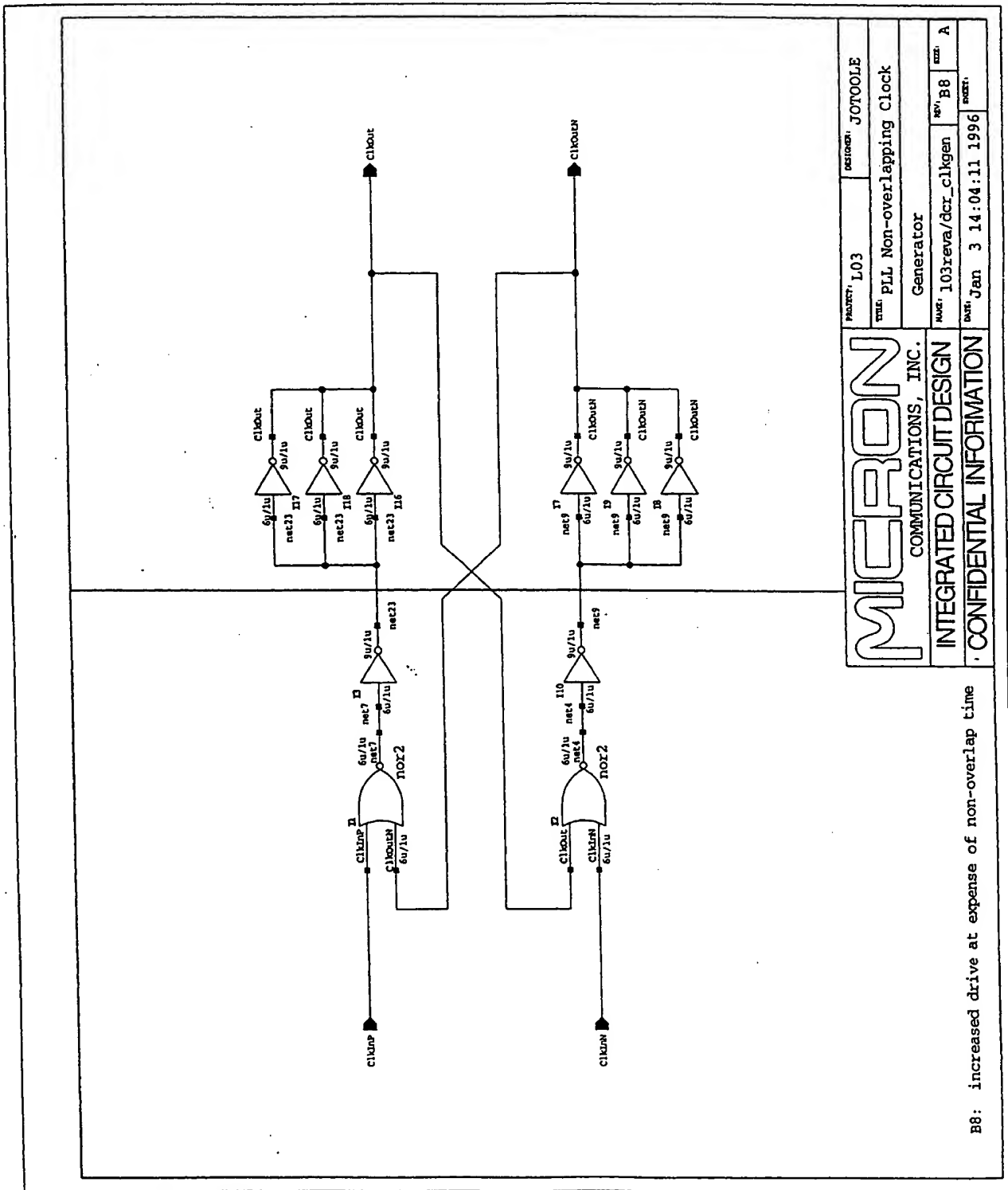
TABLE 8.0507

|  |  |
|--|--|
|  |  |
|--|--|

8.0507AB

8.0507AA

EX 8.0507



B8: increased drive at expense of non-overlap time

|                           |  |                                  |                   |
|---------------------------|--|----------------------------------|-------------------|
| <b>MICRON</b>             |  | PROJECT: L03                     | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: PLL Non-overlapping Clock |                   |
| INTEGRATED CIRCUIT DESIGN |  | Generator                        |                   |
| CONFIDENTIAL INFORMATION  |  | NAME: 103reva/dcr_clkgen         | REV: B8           |
|                           |  | DATE: Jan 3 14:04:11 1996        | ED: A             |
|                           |  | PAGE: 1                          |                   |

Fig. 8.0507

TABLE 2302360

|        |        |        |        |
|--------|--------|--------|--------|
| 8.06AA | 8.06AB | 8.06AC | 8.06AD |
| 8.06BA | 8.06BB | 8.06BC | 8.06BD |
| 8.06CA | 8.06CB | 8.06CC | 8.06CD |
| 8.06DA | 8.06DB | 8.06DC | 8.06DD |
| 8.06EA | 8.06EB | 8.06EC | 8.06ED |

II II BB.0015



8.0601AA 8.0601AB

|          |          |
|----------|----------|
| 8.0601AA | 8.0601AB |
| 8.0601BA | 8.0601BB |

8.0601AA 8.0601AB

FIG. 8.0601

B6: make blockdet driver smaller

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

PRODUCT: L03  
REVISION: J07000LE  
TITLE: Transmitter PLL  
PART: 103rev0/cpllsyn  
REV: B6  
DATE: Oct 30 10:32:47 1995  
MFG:

**B6: make b6lockdet driver smaller**

|                           |  |                            |  |                    |         |
|---------------------------|--|----------------------------|--|--------------------|---------|
| MICROON                   |  | PROPERTY LOG               |  | REFERENCE: JOTOOLE |         |
| COMMUNICATIONS, INC.      |  | ITEM: Transmitter PL       |  |                    |         |
| INTEGRATED CIRCUIT DESIGN |  | PARTS: 5, 43Ma             |  |                    |         |
| CONFIDENTIAL INFORMATION  |  | PART: J01rev0/ppl/lyson    |  | Q86                | REV. PL |
|                           |  | DATE: Oct 30 10:32:47 1995 |  | PART:              |         |

"0020" 2502280

|            |            |            |
|------------|------------|------------|
| 8.060101AA | 8.060101AB | 8.060101AC |
| 8.060101BA | 8.060101BB | 8.060101BC |
| 8.060101CA | 8.060101CB | 8.060101CC |

II II II II II II II II II II



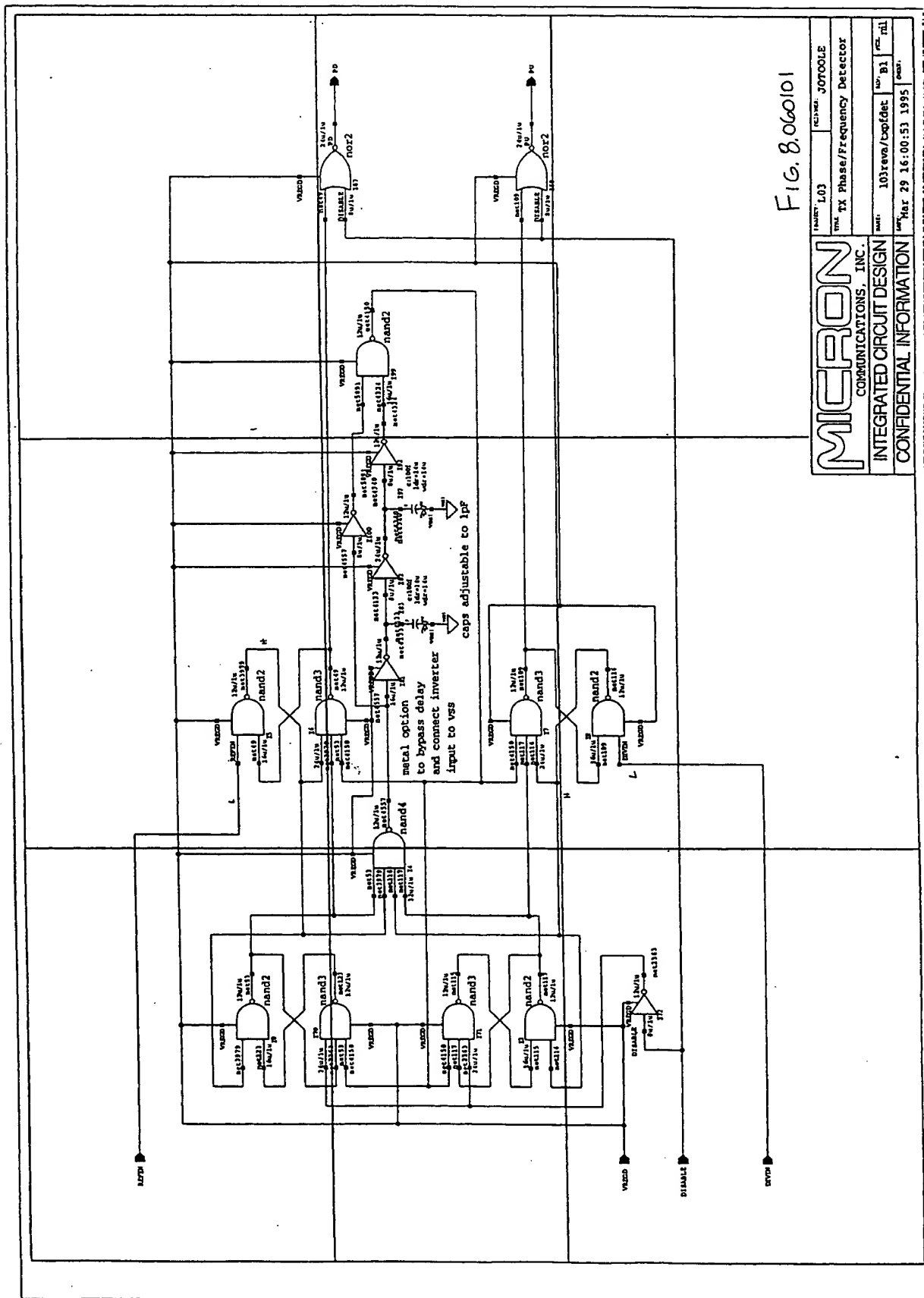


FIG. 8.060101

|                           |  |                                    |               |
|---------------------------|--|------------------------------------|---------------|
| MICRON                    |  | NAME: L03                          | REV: J0700LE  |
| COMMUNICATIONS, INC.      |  | TITLE: TX Phase/Frequency Detector |               |
| INTEGRATED CIRCUIT DESIGN |  | DATE: 1031rev/bopdet               | REV: B1       |
| CONFIDENTIAL INFORMATION  |  | DATE: Mar 29 16:00:53 1995         | DESIGNER: rll |

|            |            |
|------------|------------|
| 8.060102AA | 8.060102AB |
| 8.060102BA | 8.060102BB |

Simulation schematic: txchgump\_sim

MICRON COMMUNICATIONS, INC.

|             |             |         |
|-------------|-------------|---------|
| VALLEY, L03 | NO. 1000000 | JOTOOLE |
|-------------|-------------|---------|

TX PLL Charge Pump

6

|                  |         |    |
|------------------|---------|----|
| 103reva/exchgump | REV. B1 | בב |
|------------------|---------|----|

Feb 28 09:55:50 1995

|      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |        |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--------|
| 1990 | 1991 | 1992 | 1993 | 1994 | 1995 | 1996 | 1997 | 1998 | 1999 | 2000 | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 | 2014 | 2015 | 2016 | 2017 | 2018 | 2019 | 2020 | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 | 2032 | 2033 | 2034 | 2035 | 2036 | 2037 | 2038 | 2039 | 2040 | 2041 | 2042 | 2043 | 2044 | 2045 | 2046 | 2047 | 2048 | 2049 | 2050 | 2051 | 2052 | 2053 | 2054 | 2055 | 2056 | 2057 | 2058 | 2059 | 2060 | 2061 | 2062 | 2063 | 2064 | 2065 | 2066 | 2067 | 2068 | 2069 | 2070 | 2071 | 2072 | 2073 | 2074 | 2075 | 2076 | 2077 | 2078 | 2079 | 2080 | 2081 | 2082 | 2083 | 2084 | 2085 | 2086 | 2087 | 2088 | 2089 | 2090 | 2091 | 2092 | 2093 | 2094 | 2095 | 2096 | 2097 | 2098 | 2099 | 2100 | 2101 | 2102 | 2103 | 2104 | 2105 | 2106 | 2107 | 2108 | 2109 | 2110 | 2111 | 2112 | 2113 | 2114 | 2115 | 2116 | 2117 | 2118 | 2119 | 2120 | 2121 | 2122 | 2123 | 2124 | 2125 | 2126 | 2127 | 2128 | 2129 | 2130 | 2131 | 2132 | 2133 | 2134 | 2135 | 2136 | 2137 | 2138 | 2139 | 2140 | 2141 | 2142 | 2143 | 2144 | 2145 | 2146 | 2147 | 2148 | 2149 | 2150 | 2151 | 2152 | 2153 | 2154 | 2155 | 2156 | 2157 | 2158 | 2159 | 2160 | 2161 | 2162 | 2163 | 2164 | 2165 | 2166 | 2167 | 2168 | 2169 | 2170 | 2171 | 2172 | 2173 | 2174 | 2175 | 2176 | 2177 | 2178 | 2179 | 2180 | 2181 | 2182 | 2183 | 2184 | 2185 | 2186 | 2187 | 2188 | 2189 | 2190 | 2191 | 2192 | 2193 | 2194 | 2195 | 2196 | 2197 | 2198 | 2199 | 2200 | 2201 | 2202 | 2203 | 2204 | 2205 | 2206 | 2207 | 2208 | 2209 | 2210 | 2211 | 2212 | 2213 | 2214 | 2215 | 2216 | 2217 | 2218 | 2219 | 2220 | 2221 | 2222 | 2223 | 2224 | 2225 | 2226 | 2227 | 2228 | 2229 | 2230 | 2231 | 2232 | 2233 | 2234 | 2235 | 2236 | 2237 | 2238 | 2239 | 2240 | 2241 | 2242 | 2243 | 2244 | 2245 | 2246 | 2247 | 2248 | 2249 | 2250 | 2251 | 2252 | 2253 | 2254 | 2255 | 2256 | 2257 | 2258 | 2259 | 2260 | 2261 | 2262 | 2263 | 2264 | 2265 | 2266 | 2267 | 2268 | 2269 | 2270 | 2271 | 2272 | 2273 | 2274 | 2275 | 2276 | 2277 | 2278 | 2279 | 2280 | 2281 | 2282 | 2283 | 2284 | 2285 | 2286 | 2287 | 2288 | 2289 | 2290 | 2291 | 2292 | 2293 | 2294 | 2295 | 2296 | 2297 | 2298 | 2299 | 2300 | 2301 | 2302 | 2303 | 2304 | 2305 | 2306 | 2307 | 2308 | 2309 | 2310 | 2311 | 2312 | 2313 | 2314 | 2315 | 2316 | 2317 | 2318 | 2319 | 2320 | 2321 | 2322 | 2323 | 2324 | 2325 | 2326 | 2327 | 2328 | 2329 | 2330 | 2331 | 2332 | 2333 | 2334 | 2335 | 2336 | 2337 | 2338 | 2339 | 2340 | 2341 | 2342 | 2343 | 2344 | 2345 | 2346 | 2347 | 2348 | 2349 | 2350 | 2351 | 2352 | 2353 | 2354 | 2355 | 2356 | 2357 | 2358 | 2359 | 2360 | 2361 | 2362 | 2363 | 2364 | 2365 | 2366 | 2367 | 2368 | 2369 | 2370 | 2371 | 2372 | 2373 | 2374 | 2375 | 2376 | 2377 | 2378 | 2379 | 2380 | 2381 | 2382 | 2383 | 2384 | 2385 | 2386 | 2387 | 2388 | 2389 | 2390 | 2391 | 2392 | 2393 | 2394 | 2395 | 2396 | 2397 | 2398</ |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--------|

|            |            |
|------------|------------|
| 8.060103AA | 8.060103AB |
| 8.060103BA | 8.060103BB |
| 8.060103CA | 8.060103CB |

LEONARD B. ELLIOTT

10020"23022000

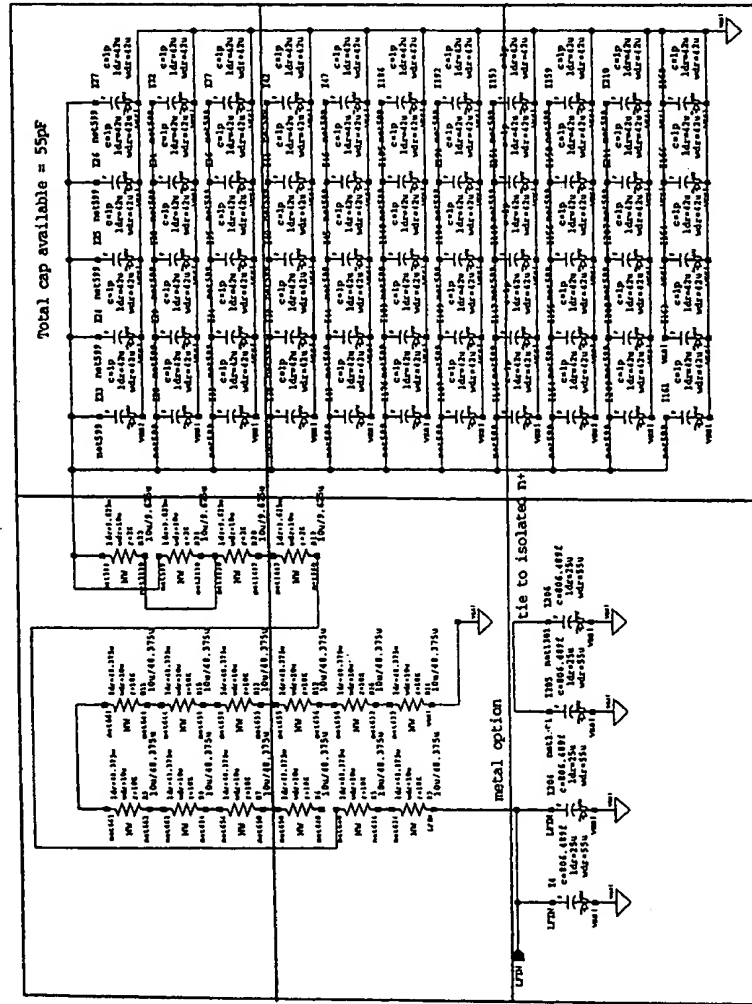


Fig. 8.060103

|                           |                   |                      |
|---------------------------|-------------------|----------------------|
| MICRON                    |                   | COMMUNICATIONS, INC. |
| PROJECT: L03              | REVISION: J0700LE |                      |
| TITLE: TX PLL Loop Filter |                   |                      |
| BW=700KHz                 | PH=60deg          |                      |
| DATE: 10/29/94            | BY: B8            | REV: 10/29/94        |
| CONFIDENTIAL INFORMATION  |                   |                      |

B2: moved extra caps to biasok  
B8: moved 2 2K resistors to tmbis

TABLE "C9022B60

MI40-030

|            |            |            |
|------------|------------|------------|
| 8.060104AA | 8.060104AB | 8.060104AC |
| 8.060104BA | 8.060104BB | 8.060104BC |
| 8.060104CA | 8.060104CB | 8.060104CC |
| 8.060104DA | 8.060104DB | 8.060104DC |

И И Г Б.М.М.И.И.И.И.

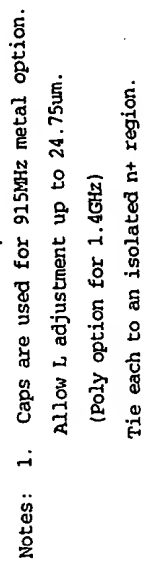
|                           |  |                           |  |                  |  |
|---------------------------|--|---------------------------|--|------------------|--|
| MICRON                    |  | PART# 103                 |  | REVISION J070016 |  |
| COMMUNICATIONS, INC.      |  | TX VCO                    |  | BIAS=3.78mA      |  |
| INTEGRATED CIRCUIT DESIGN |  | 103revA/LXVCO             |  | REV. B1          |  |
| CONFIDENTIAL INFORMATION  |  | DATE: Apr 4 08:52:23 1995 |  | REV. D1          |  |

Fig. 8.060104

|              |              |              |              |
|--------------|--------------|--------------|--------------|
| 8.06010401AA | 8.06010401AB | 8.06010401AC | 8.06010401AD |
| 8.06010401BA | 8.06010401BB | 8.06010401BC | 8.06010401BD |

11070109008  
622





Notes: 1. Caps are used for 915MHz metal option.  
Allow L adjustment up to 24.75um.  
(Poly option for 1.4GHz)  
Tie each to an isolated n+ region.

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99

MI40-030

|                |                |                |
|----------------|----------------|----------------|
| 8.0601040101AA | 8.0601040101AB | 8.0601040101AC |
| 8.0601040101BA | 8.0601040101BB | 8.0601040101BC |

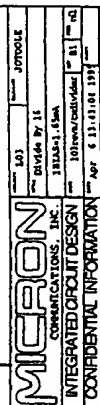
11.05.2008

Architectural floor plan showing a large building layout with various rooms, corridors, and structural elements. Key labels include:

- VACUOL 419**: Located in the lower-left section.
- VREG-400**: Located in the upper-right section.
- 406**, **400**, **404**: Numerical identifiers for specific areas or rooms.
- Source Yellowing & Discoloration**: A note indicating material issues on the right side.
- Handwritten notes at the bottom**: "915 W H2", "916", "stege", and "P. channel".

|            |            |            |            |
|------------|------------|------------|------------|
| 8.060105AA | 8.060105AB | 8.060105AC | 8.060105AD |
| 8.060105BA | 8.060105BB | 8.060105BC | 8.060105BD |
| 8.060105CA | 8.060105CB | 8.060105CC | 8.060105CD |
| 8.060105DA | 8.060105DB | 8.060105DC | 8.060105DD |

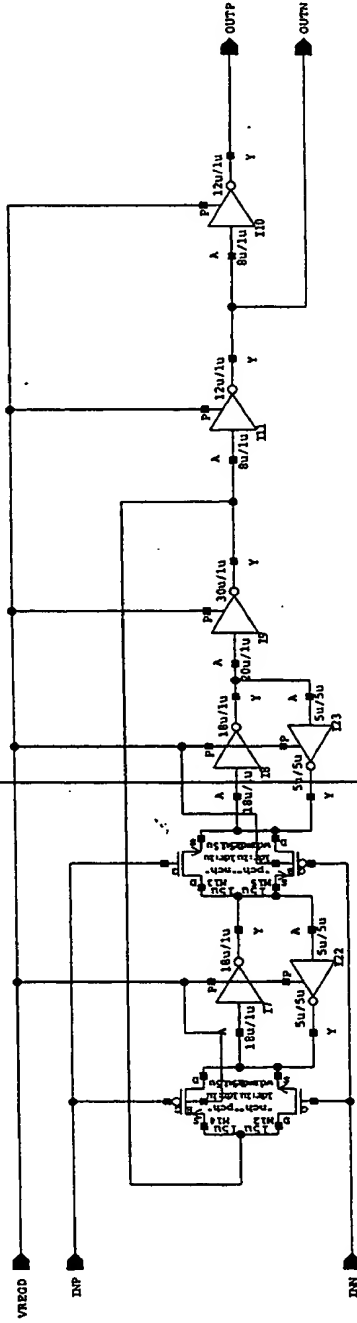
FIG. 8.06.0105



MI40-030

|              |              |
|--------------|--------------|
| 8.06010501AA | 8.06010501AB |
|--------------|--------------|

MI40-030



|   |  |  |  |                            |  |                   |         |
|---|--|--|--|----------------------------|--|-------------------|---------|
| <div>MICRON</div> <div>COMMUNICATIONS, INC.</div> |  |  |  | PROJECT: L03               |  | DESIGNER: JOTOOLE |         |
|   |  |  |  | TITLE: Divider Flip-flop   |  |                   |         |
|   |  |  |  |                            |  |                   |         |
|   |  |  |  | NAME: 103reva/bdvtff       |  | REV: B1           | SIZE: A |
|   |  |  |  | DATE: Mar 30 09:47:12 1995 |  | PART:             |         |
|   |  |  |  |                            |  |                   |         |

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

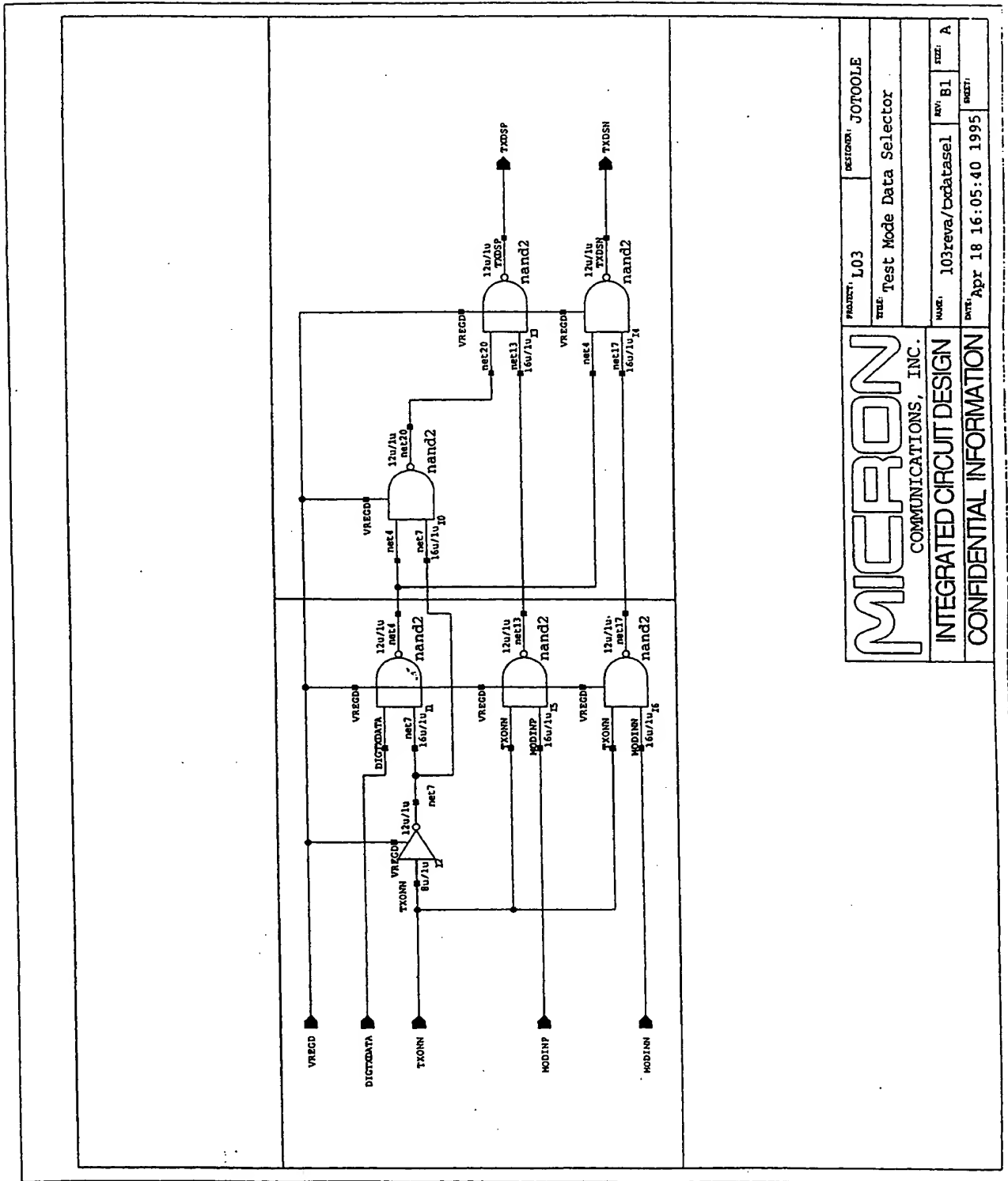
CONFIDENTIAL INFORMATION

TABLE 2000

|          |          |
|----------|----------|
| 8.0602AA | 8.0602AB |
|----------|----------|

EX 8.0602





|                           |  |                                |                   |
|---------------------------|--|--------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                   | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: Test Mode Data Selector |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/bdatsel          | REV: B1           |
| CONFIDENTIAL INFORMATION  |  | DATE: Apr 18 16:05:40 1995     | SIZE: A           |

FIG. 8.0602

FOUO" e9ee300

|                 |                 |
|-----------------|-----------------|
| <p>8.0603AA</p> | <p>8.0603AB</p> |
|-----------------|-----------------|

IX BB.0603

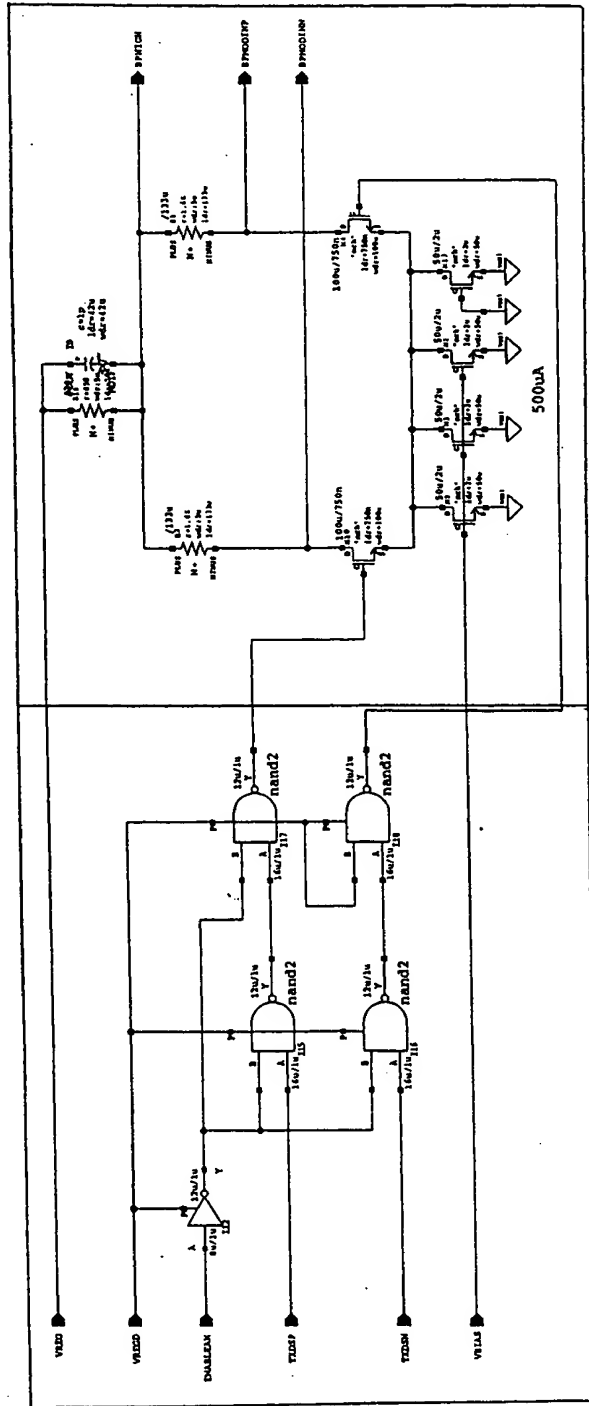


FIG. 8.0603

|  |                              |                      |                      |
|--|------------------------------|----------------------|----------------------|
| <b>MICRON</b><br>COMMUNICATIONS, INC.<br>INTEGRATED CIRCUIT DESIGN<br>CONFIDENTIAL INFORMATION | PART NO. L03                 | REV. 1.00            | DATE 10/18/96        |
|  | TITLE BPSK Modulation Driver | IBINS=500uA          | B8 88 71             |
|  | 103tera/cdbpsk               | 103tera/cdbpsk       | 103tera/cdbpsk       |
|  | Jan 18 10:28:46 1996         | Jan 18 10:28:46 1996 | Jan 18 10:28:46 1996 |

B8: modified current source



CONFIDENTIAL

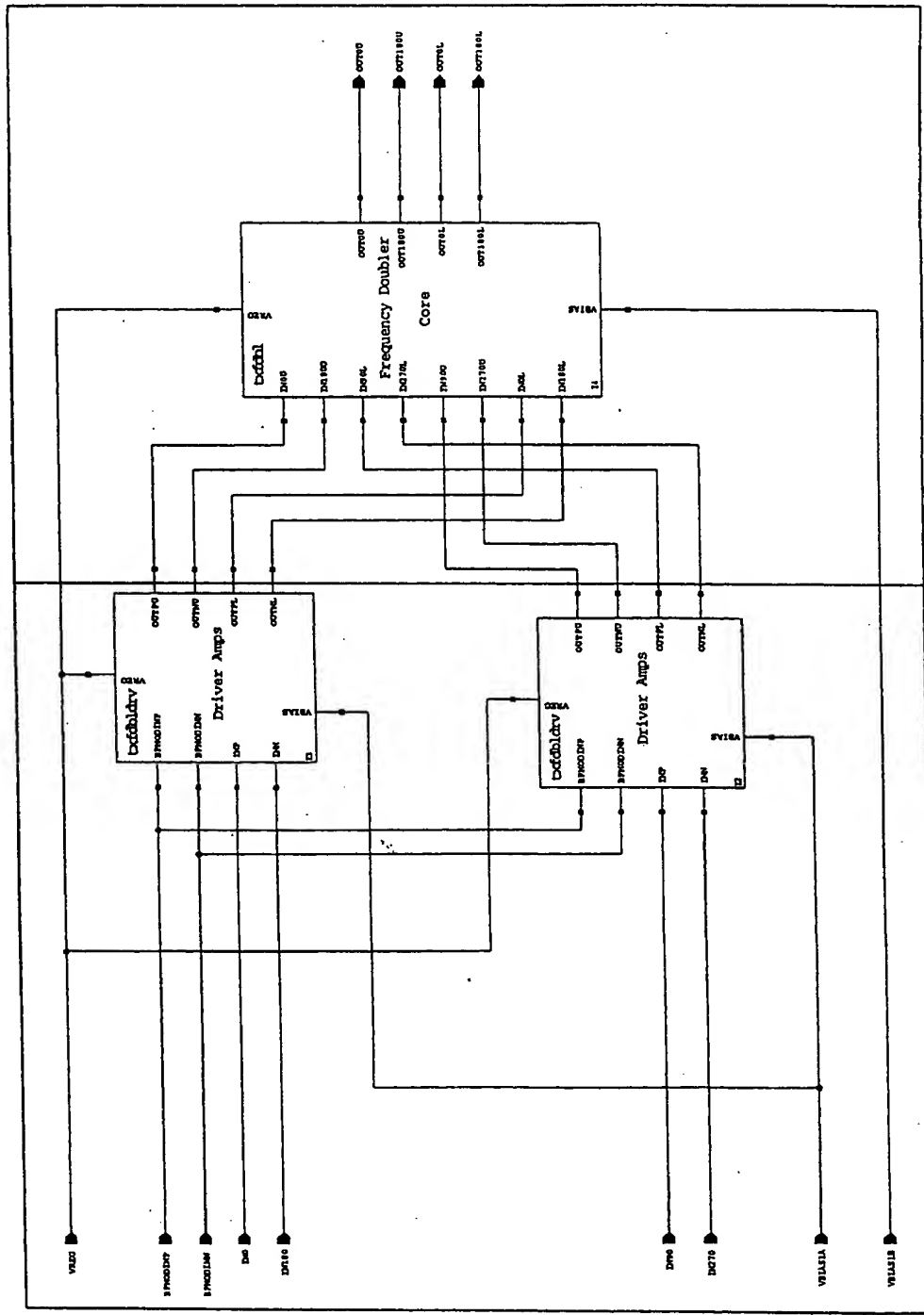


Fig. 8.0604

|                           |  |                           |                   |
|---------------------------|--|---------------------------|-------------------|
| MICRON                    |  | PROJECT: L03              | REVISION: J0700LE |
| COMMUNICATIONS, INC.      |  | Title: Frequency Doubler  |                   |
| INTEGRATED CIRCUIT DESIGN |  | IBIAS=4mA                 |                   |
| CONFIDENTIAL INFORMATION  |  | DATE: 103revs/cdbouler    | REV: B1           |
|                           |  | DATE: Apr 5 10:17:13 1995 | REV: B1           |

8.060401

|            |            |            |            |            |
|------------|------------|------------|------------|------------|
| 8.060401AA | 8.060401AB | 8.060401AC | 8.060401AD | 8.060401AE |
| 8.060401BA | 8.060401BB | 8.060401BC | 8.060401BD | 8.060401BE |
| 8.060401CA | 8.060401CB | 8.060401CC | 8.060401CD | 8.060401CE |
| 8.060401DA | 8.060401DB | 8.060401DC | 8.060401DD | 8.060401DE |
| 8.060401EA | 8.060401EB | 8.060401EC | 8.060401ED | 8.060401EE |
| 8.060401FA | 8.060401FB | 8.060401FC | 8.060401FD | 8.060401FE |

8.060401

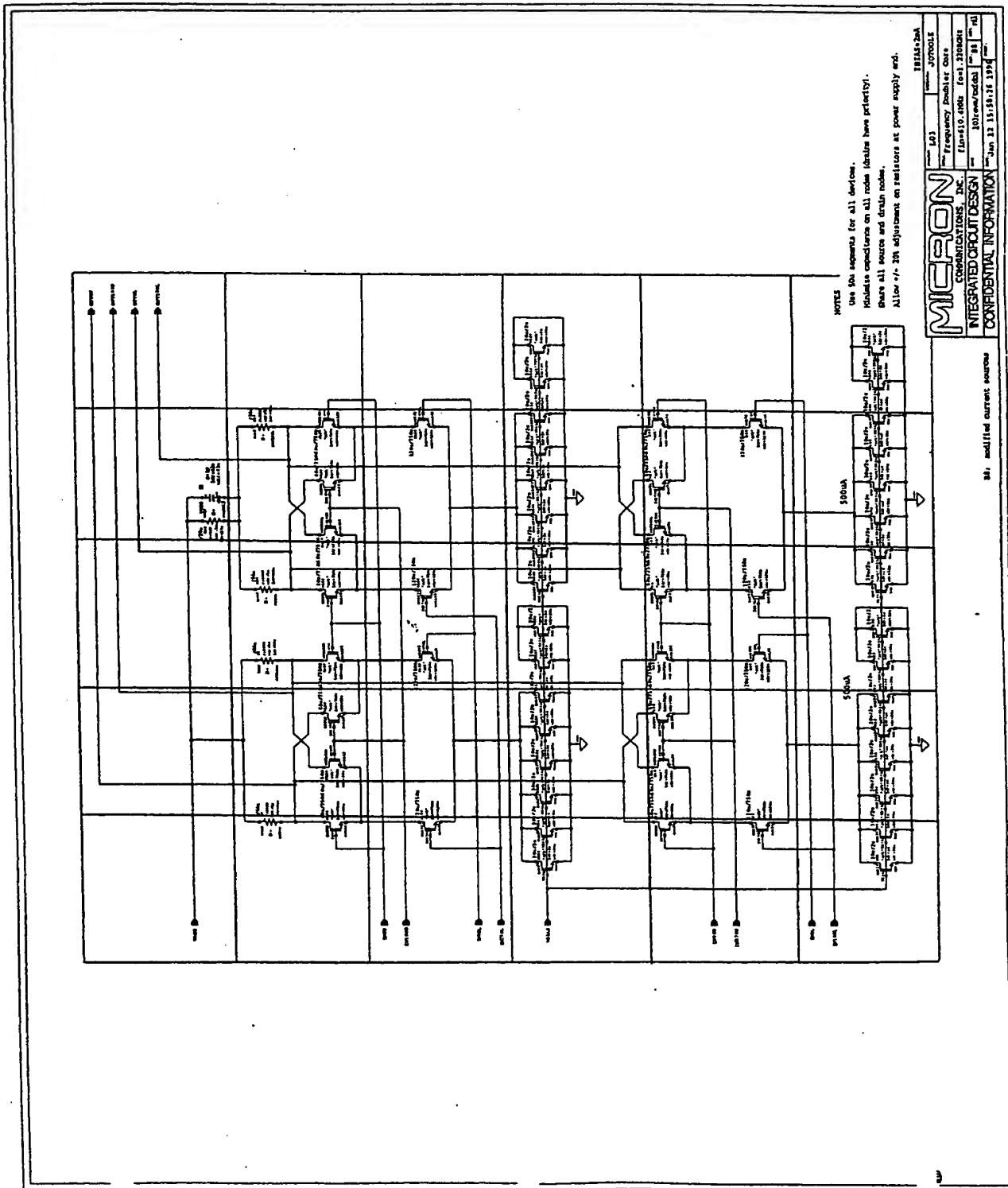


FIG. 8.060901

FOUO" E302280

|  |  |
|--|--|
|  |  |
|--|--|

8.0605AB

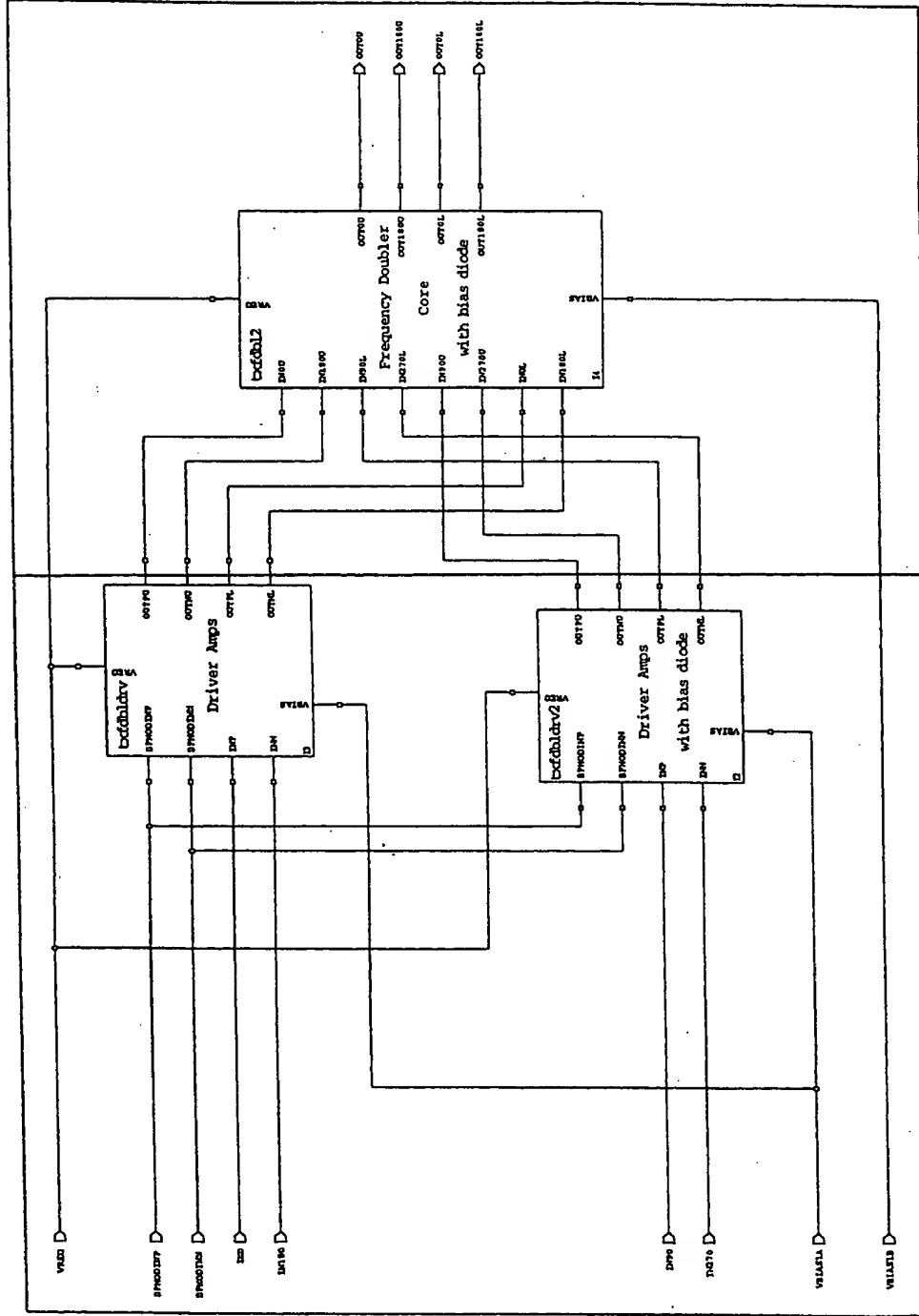
8.0605AA

II 8.0605



www.e3000.com

FIG. 8.0605



|                           |  |                            |                   |
|---------------------------|--|----------------------------|-------------------|
| MICRON                    |  | PROJECT: J03               | REVISION: J0000LE |
| COMMUNICATIONS, INC.      |  | TYPE: Frequency Doubler    |                   |
| INTEGRATED CIRCUIT DESIGN |  | IBIAS=4mA                  |                   |
| CONFIDENTIAL INFORMATION  |  | NAME: 101revA/bdoubler2    | REV: B8           |
|                           |  | DATE: Jan 12 17:22:51 1996 | SIZE: n1          |
|                           |  |                            | PAGE: 1           |

B8: current sources modified

|                   |                   |                   |                   |
|-------------------|-------------------|-------------------|-------------------|
| <i>8.060501AA</i> | <i>8.060501AB</i> | <i>8.060501AC</i> | <i>8.060501AD</i> |
| <i>8.060501BA</i> | <i>8.060501BB</i> | <i>8.060501BC</i> | <i>8.060501BD</i> |
| <i>8.060501CA</i> | <i>8.060501CB</i> | <i>8.060501CC</i> | <i>8.060501CD</i> |

И. И. Е. Б. О. Г. О. С. О. И



- Minimize capacitance on output nodes.
- Share all source/drain nodes.
- Allow +/- 20% adjustment on resistors at supply end.

Fig. 8.060501

**micron**  
COMMUNICATIONS, INC.

98: modified current sources

SUBJECT: L03 REQUESTED: JOTOOLE

| DATE | DESCRIPTION        | AMOUNT | BALANCE |
|------|--------------------|--------|---------|
| 1952 | Double Driver Amps | 100.00 | 100.00  |

**IBIAS=1mA**

|                  |         |          |
|------------------|---------|----------|
| 103reva/bdfoldrv | REV: B8 | SER: nil |
|------------------|---------|----------|

JAN 12 15:37:26 1996

|                   |                   |                   |                   |
|-------------------|-------------------|-------------------|-------------------|
| <i>8.060502AA</i> | <i>8.060502AB</i> | <i>8.060502AC</i> | <i>8.060502AD</i> |
| <i>8.060502BA</i> | <i>8.060502BB</i> | <i>8.060502BC</i> | <i>8.060502BD</i> |
| <i>8.060502CA</i> | <i>8.060502CB</i> | <i>8.060502CC</i> | <i>8.060502CD</i> |

И. И. Е. Б. 060502

iii



FIG. 8.06.0502

|              |                    |
|--------------|--------------------|
| SUBJECT, L03 | DELICATES, JOTOOLE |
|--------------|--------------------|

BIAS=1mA

103revb/bxfgbldrv2

Jan 18 08:22:12 1996

**B8: modified current sources**

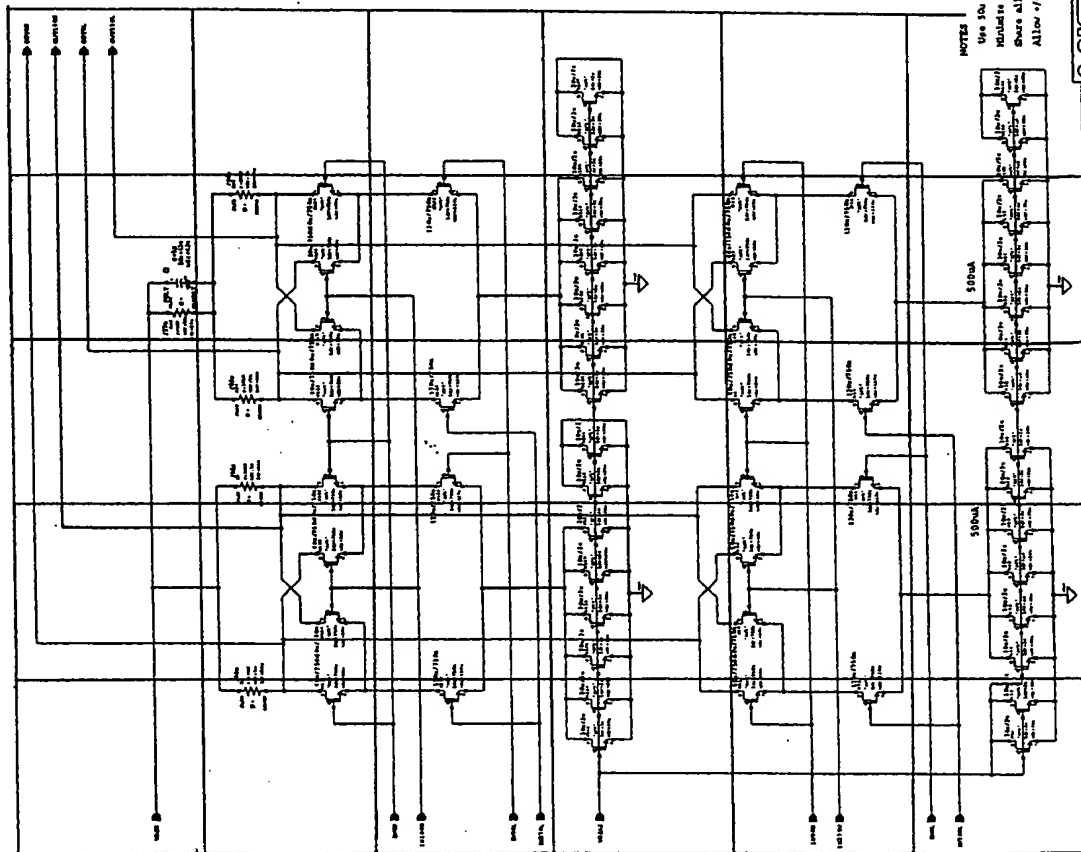
|                           |  |       |                   |                      |    |       |    |
|---------------------------|--|-------|-------------------|----------------------|----|-------|----|
| INTEGRATED CIRCUIT DESIGN |  | part# | 103eva/bcfdabldr2 | rev#                 | B8 | size  | ml |
| CONFIDENTIAL INFORMATION  |  | date  |                   | jan 18 08:22:12 1996 |    | month |    |

8.060503

|            |            |            |            |            |
|------------|------------|------------|------------|------------|
| 8.060503AA | 8.060503AB | 8.060503AC | 8.060503AD | 8.060503AE |
| 8.060503BA | 8.060503BB | 8.060503BC | 8.060503BD | 8.060503BE |
| 8.060503CA | 8.060503CB | 8.060503CC | 8.060503CD | 8.060503CE |
| 8.060503DA | 8.060503DB | 8.060503DC | 8.060503DD | 8.060503DE |
| 8.060503EA | 8.060503EB | 8.060503EC | 8.060503ED | 8.060503EE |
| 8.060503FA | 8.060503FB | 8.060503FC | 8.060503FD | 8.060503FE |

8.060503

FIG. 8.060503



- Use 50u segments for all devices.
- Minimize capacitance on all nodes (drains have priority).
- Share all source and drain nodes.
- Allow +/- 20% adjustment on resistors at power supply end.

10YAS-02m3

1000000

Case

0101

1.76072.1

05

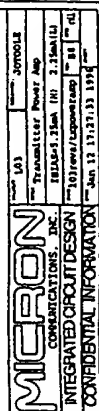
76

1

|          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.0606AA | 8.0606AB | 8.0606AC | 8.0606AD | 8.0606AE | 8.0606AF | 8.0606AG | 8.0606AH |
| 8.0606BA | 8.0606BB | 8.0606BC | 8.0606BD | 8.0606BE | 8.0606BF | 8.0606BG | 8.0606BH |
| 8.0606CA | 8.0606CB | 8.0606CC | 8.0606CD | 8.0606CE | 8.0606CF | 8.0606CG | 8.0606CH |
| 8.0606DA | 8.0606DB | 8.0606DC | 8.0606DD | 8.0606DE | 8.0606DF | 8.0606DG | 8.0606DH |
| 8.0606EA | 8.0606EB | 8.0606EC | 8.0606ED | 8.0606EE | 8.0606EF | 8.0606EG | 8.0606EH |
| 8.0606FA | 8.0606FB | 8.0606FC | 8.0606FD | 8.0606FE | 8.0606FF | 8.0606FG | 8.0606FH |
|          |          | 8.0606GC | 8.0606GD | 8.0606GE |          |          |          |
| 8.0606HA | 8.0606HB | 8.0606HC | 8.0606HD | 8.0606HE |          |          |          |
|          | 8.0606IB | 8.0606IC | 8.0606ID | 8.0606IE |          |          |          |



FIG. 8.0606



"0000" 000000

|          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 8.0607AA | 8.0607AB | 8.0607AC | 8.0607AD | 8.0607AE | 8.0607AF | 8.0607AG | 8.0607AH | 8.0607AI | 8.0607AJ |
| 8.0607BA | 8.0607BB | 8.0607BC | 8.0607BD | 8.0607BE | 8.0607BF | 8.0607BG | 8.0607BH | 8.0607BI | 8.0607BJ |
| 8.0607CA | 8.0607CB | 8.0607CC | 8.0607CD | 8.0607CE | 8.0607CF | 8.0607CG | 8.0607CH | 8.0607CI | 8.0607CJ |
| 8.0607DA | 8.0607DB | 8.0607DC | 8.0607DD | 8.0607DE | 8.0607DF | 8.0607DG | 8.0607DH | 8.0607DI | 8.0607DJ |
| 8.0607EA | 8.0607EB | 8.0607EC | 8.0607ED | 8.0607EE | 8.0607EF | 8.0607EG | 8.0607EH | 8.0607EI | 8.0607EJ |
| 8.0607FA | 8.0607FB | 8.0607FC | 8.0607FD | 8.0607FE | 8.0607FF | 8.0607FG | 8.0607FH | 8.0607FI | 8.0607FJ |
| 8.0607GA | 8.0607GB | 8.0607GC | 8.0607GD | 8.0607GE | 8.0607GF | 8.0607GG | 8.0607GH | 8.0607GI | 8.0607GJ |
| 8.0607HA | 8.0607HB | 8.0607HC | 8.0607HD | 8.0607HE | 8.0607HF | 8.0607HG | 8.0607HH | 8.0607HI | 8.0607HJ |
| 8.0607IA | 8.0607IB | 8.0607IC | 8.0607ID | 8.0607IE | 8.0607IF | 8.0607IG | 8.0607IH | 8.0607II | 8.0607IJ |
| 8.0607JA | 8.0607JB | 8.0607JC | 8.0607JD | 8.0607JE | 8.0607JF | 8.0607JG | 8.0607JH | 8.0607JI | 8.0607JJ |

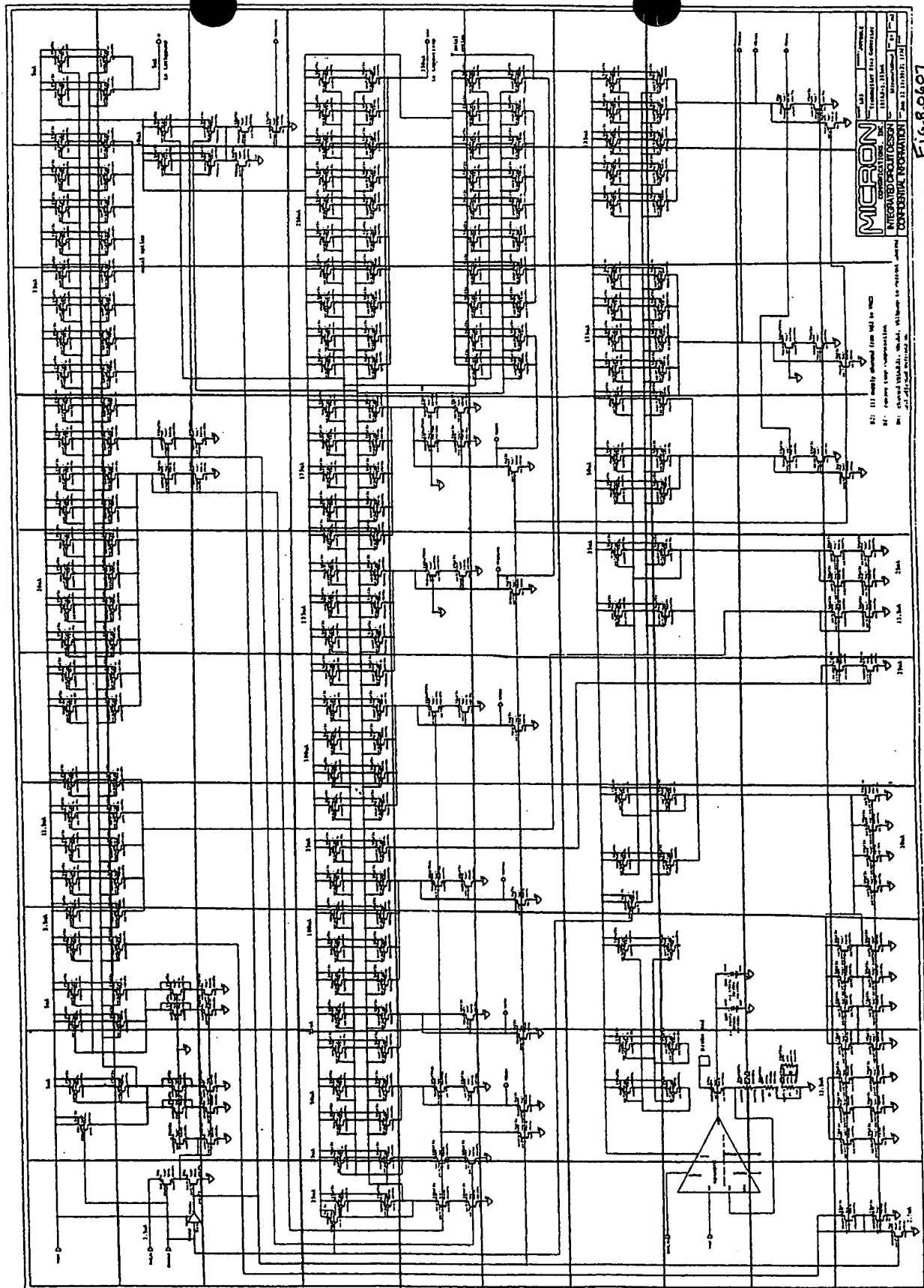
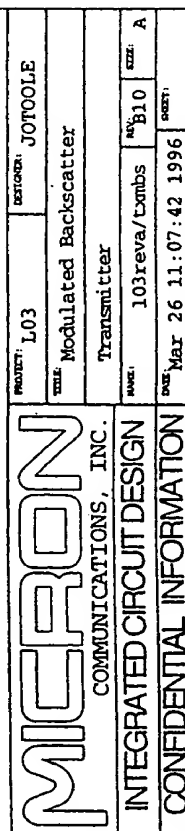
[illegible]

Fig. 8.0607

FOREFOREWORD

|          |          |
|----------|----------|
| 8.0608AA | 8.0608AB |
| 8.0608BA | 8.0608BB |

II II 8.0608BB



**B10: disconnected inverter**

## Transmitter

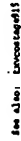
|      |                      |        |
|------|----------------------|--------|
| DATE | Mar 26 11:07:42 1996 | SECRET |
|------|----------------------|--------|

Page 6 of 6

|        |        |
|--------|--------|
| 8.07AA | 8.07AB |
| 8.07BA | 8.07BB |

8.07

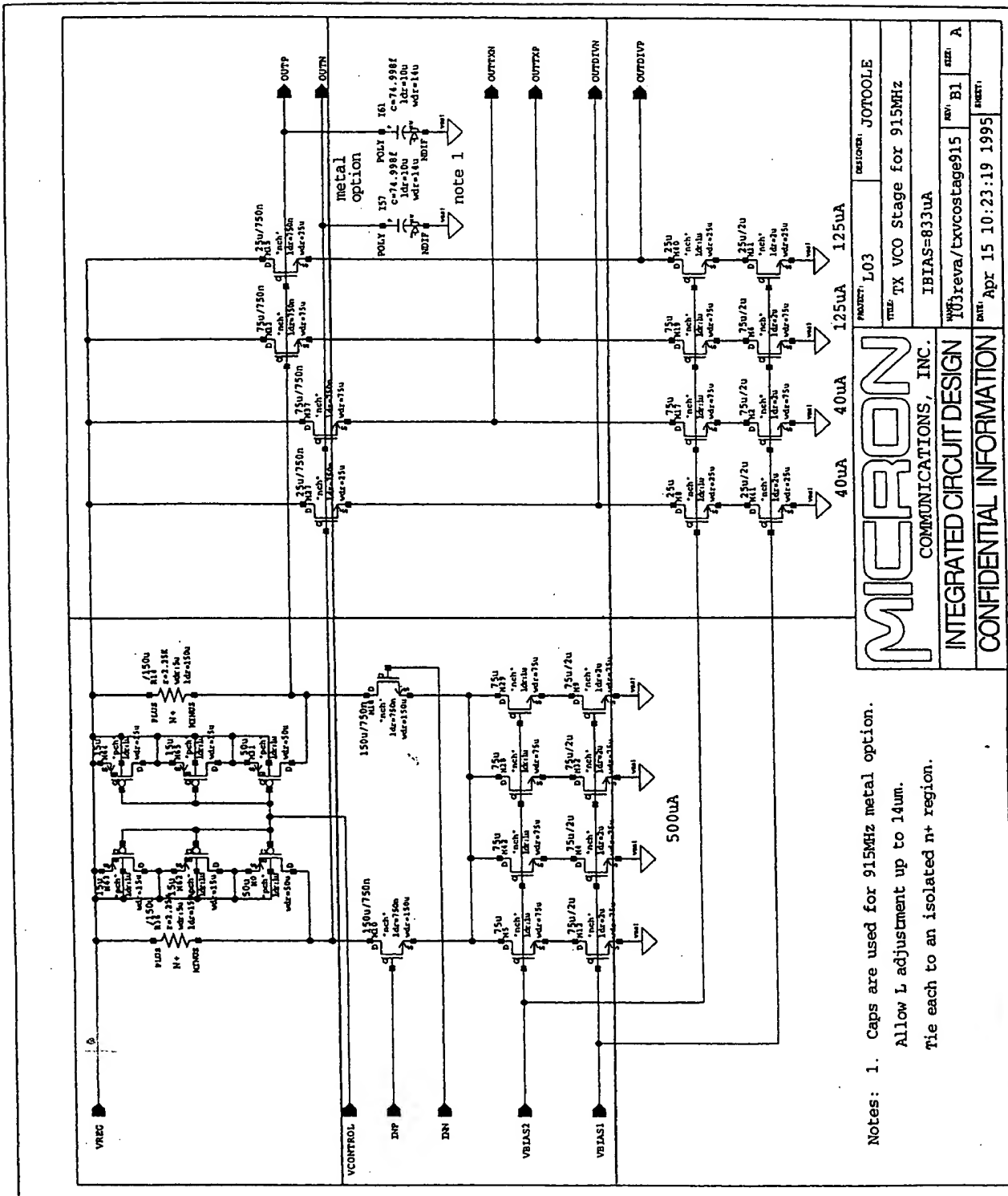
FIG. 8.07



|          |          |
|----------|----------|
| 8.0701AA | 8.0701AB |
| 8.0701BA | 8.0701BB |
| 8.0701CA | 8.0701CB |

И. И. О.





Notes: 1. Caps are used for 915MHz metal option.  
 Allow L adjustment up to 14um.  
 Tie each to an isolated n+ region.

|                           |  |                                |                    |
|---------------------------|--|--------------------------------|--------------------|
| <b>MICRON</b>             |  | PRODUCT: L03                   | DESIGNER: JOTPOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: TX VCO Stage for 915MHz |                    |
| INTEGRATED CIRCUIT DESIGN |  | IBIAS=833uA                    |                    |
| CONFIDENTIAL INFORMATION  |  | W03revA/txvcostage915          | REV: B1            |
|                           |  | DATE: Apr 15 10:23:19 1995     | DESIGNER: A        |

Fig. 8.0701

TABLE 2-30

|     |     |
|-----|-----|
| 9AA | 9AB |
| 9BA | 9BB |
| 9CA | 9CB |

11 11 11



TABLE 9.01

|        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|
| 9.01AA | 9.01AB | 9.01AC | 9.01AD | 9.01AE | 9.01AF | 9.01AG | 9.01AH |
| 9.01BA | 9.01BB | 9.01BC | 9.01BD | 9.01BE | 9.01BF | 9.01BG | 9.01BH |
| 9.01CA | 9.01CB | 9.01CC | 9.01CD | 9.01CE | 9.01CF | 9.01CG | 9.01CH |
| 9.01DA | 9.01DB | 9.01DC | 9.01DD | 9.01DE | 9.01DF | 9.01DG | 9.01DH |

II II II II

4020 50200

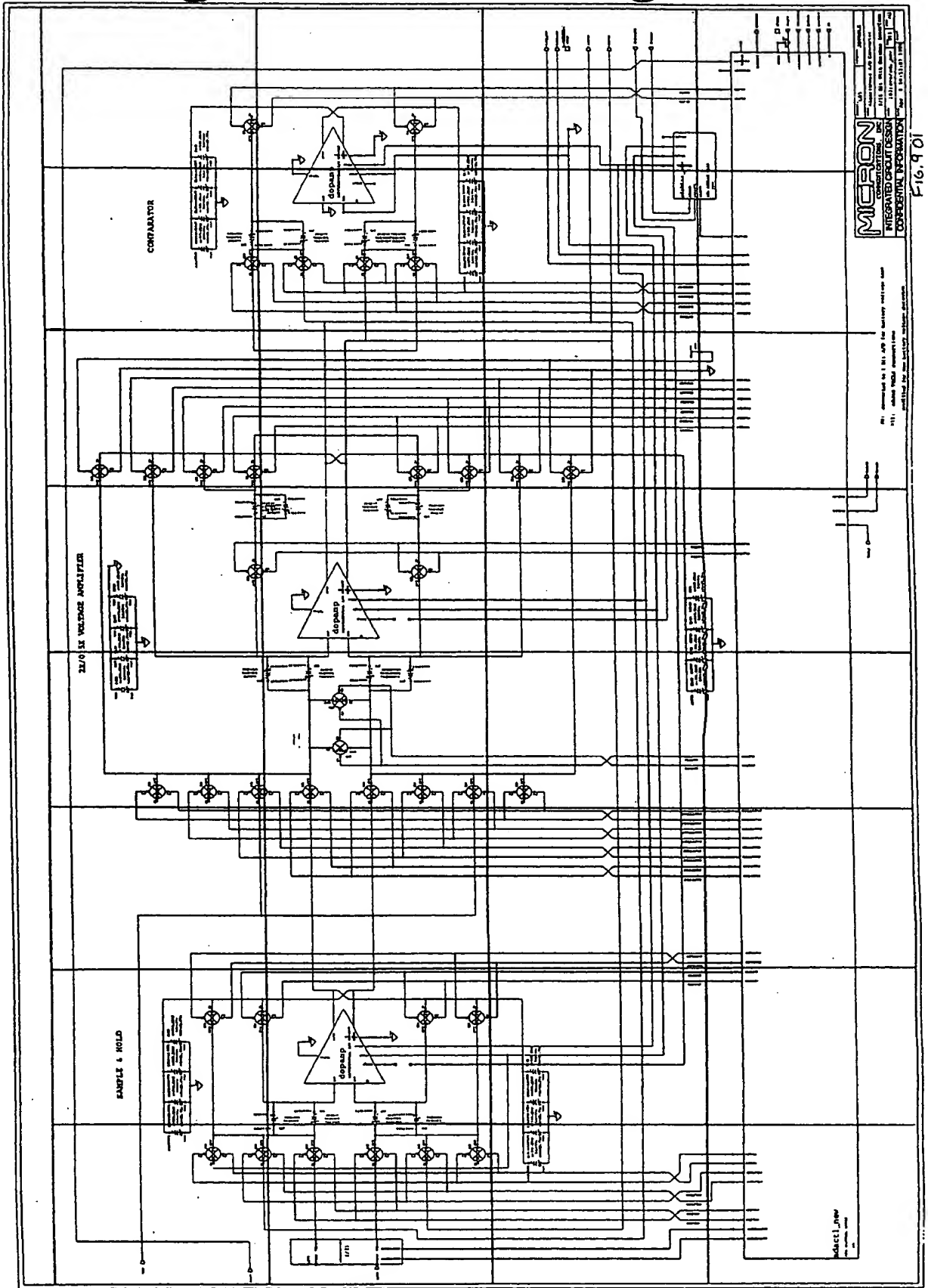


Fig. 901

9.0101AA 9.0101AB 9.0101AC 9.0101AD 9.0101AE 9.0101AF 9.0101AG 9.0101AH 9.0101AI 9.0101AJ 9.0101AK

|          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 9.0101AA | 9.0101AB | 9.0101AC | 9.0101AD | 9.0101AE | 9.0101AF | 9.0101AG | 9.0101AH | 9.0101AI | 9.0101AJ | 9.0101AK |
| 9.0101BA | 9.0101BB | 9.0101BC | 9.0101BD | 9.0101BE | 9.0101BF | 9.0101BG | 9.0101BH | 9.0101BI | 9.0101BJ | 9.0101BK |
| 9.0101CA | 9.0101CB | 9.0101CC | 9.0101CD | 9.0101CE | 9.0101CF | 9.0101CG | 9.0101CH | 9.0101CI | 9.0101CJ | 9.0101CK |

9.0101DL 9.0101DM 9.0101DN 9.0101DO 9.0101DP 9.0101DQ 9.0101DR 9.0101DS 9.0101DT 9.0101DU 9.0101DV

2000 年 12 月 31 日 止 的 年 度 內 的 經 營 業 績 及 財 務 狀 況 概 況 表  
 2000 年 12 月 31 日 止 的 年 度 內 的 經 營 業 績 及 財 務 狀 況 概 況 表

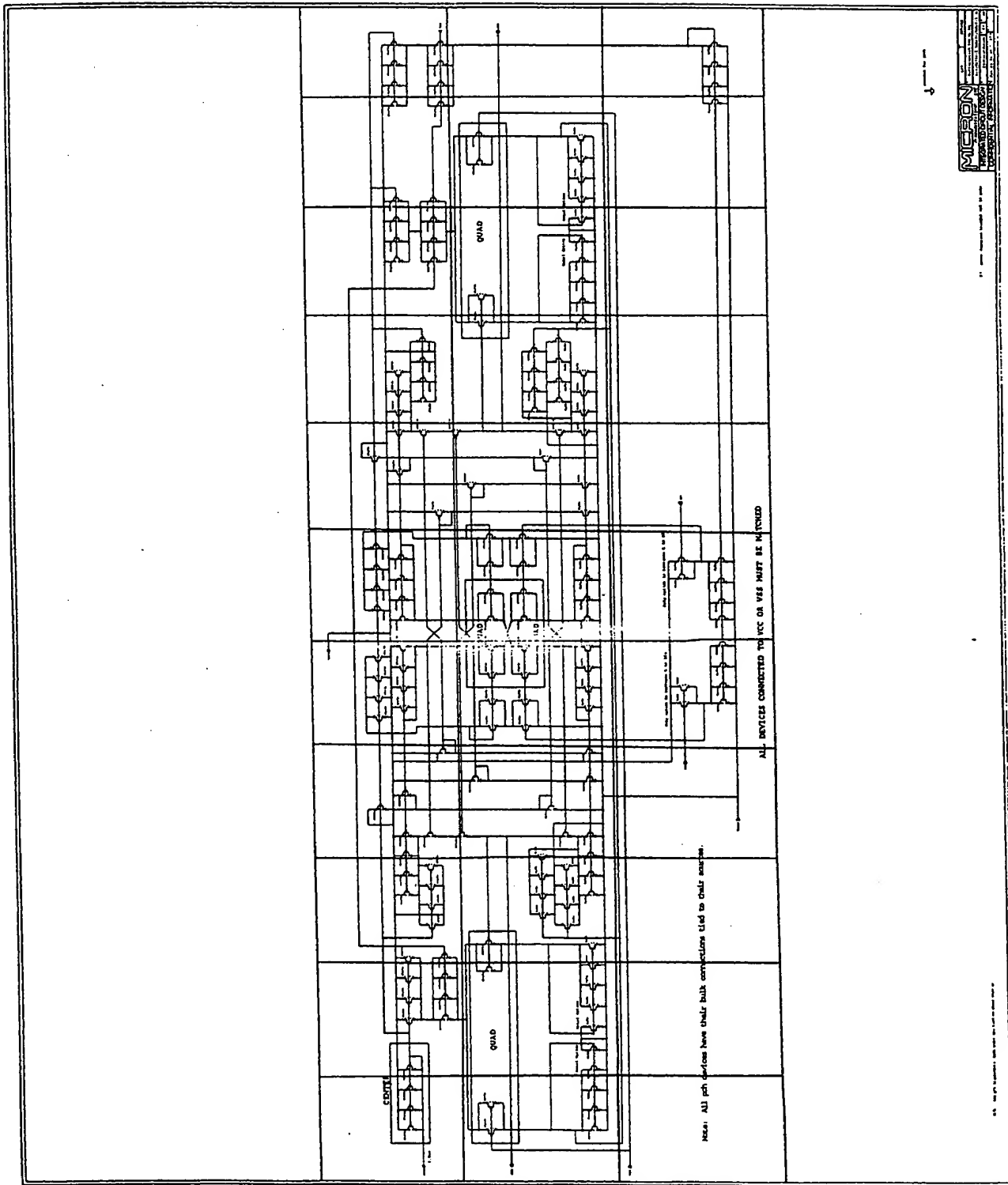
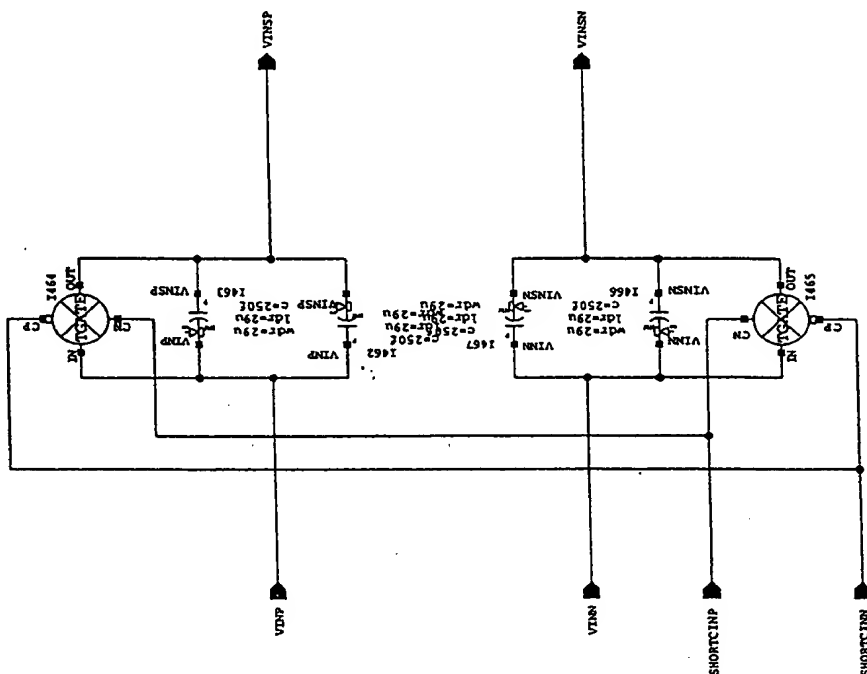


FIG. 9.0101



MICROON COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

|              |                   |
|--------------|-------------------|
| PROJECT: L03 | DESIGNER: JOTOOLE |
|--------------|-------------------|

**mm.** Analog Divide by 2

|      |         |         |
|------|---------|---------|
| NAME | REV, B1 | SIZE, A |
|------|---------|---------|

|                      |        |
|----------------------|--------|
| DATE                 | SHEET: |
| May 19 16:34:53 1995 |        |

Fig. 9.0102



T 30663 2306630

|          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 9.0103BA | 9.0103BB | 9.0103BC | 9.0103BD | 9.0103BE | 9.0103BF | 9.0103BG | 9.0103BH | 9.0103AJ | 9.0103AK | 9.0103AL | 9.0103AM | 9.0103AN | 9.0103AO | 9.0103AP |
| 9.0103CA | 9.0103CB | 9.0103CC | 9.0103CD | 9.0103CE | 9.0103CF | 9.0103CG | 9.0103CH | 9.0103CJ | 9.0103CK | 9.0103CL | 9.0103CM | 9.0103CN | 9.0103CO | 9.0103CP |
| 9.0103DA | 9.0103DB | 9.0103DC | 9.0103DD | 9.0103DE | 9.0103DF | 9.0103DG | 9.0103DH | 9.0103DJ | 9.0103DK | 9.0103DL | 9.0103DM | 9.0103DN | 9.0103DO | 9.0103DP |
| 9.0103EA | 9.0103EB | 9.0103EC | 9.0103ED | 9.0103EE | 9.0103EF | 9.0103EG | 9.0103EH | 9.0103EJ | 9.0103EK | 9.0103EL | 9.0103EM | 9.0103EN | 9.0103EO | 9.0103EP |
| 9.0103FA | 9.0103FB | 9.0103FC | 9.0103FD | 9.0103FE | 9.0103FF | 9.0103FG | 9.0103FH | 9.0103FJ | 9.0103FK | 9.0103FL | 9.0103FM | 9.0103FN | 9.0103FO | 9.0103FP |

T 30663 2306630

CODED" 6902200

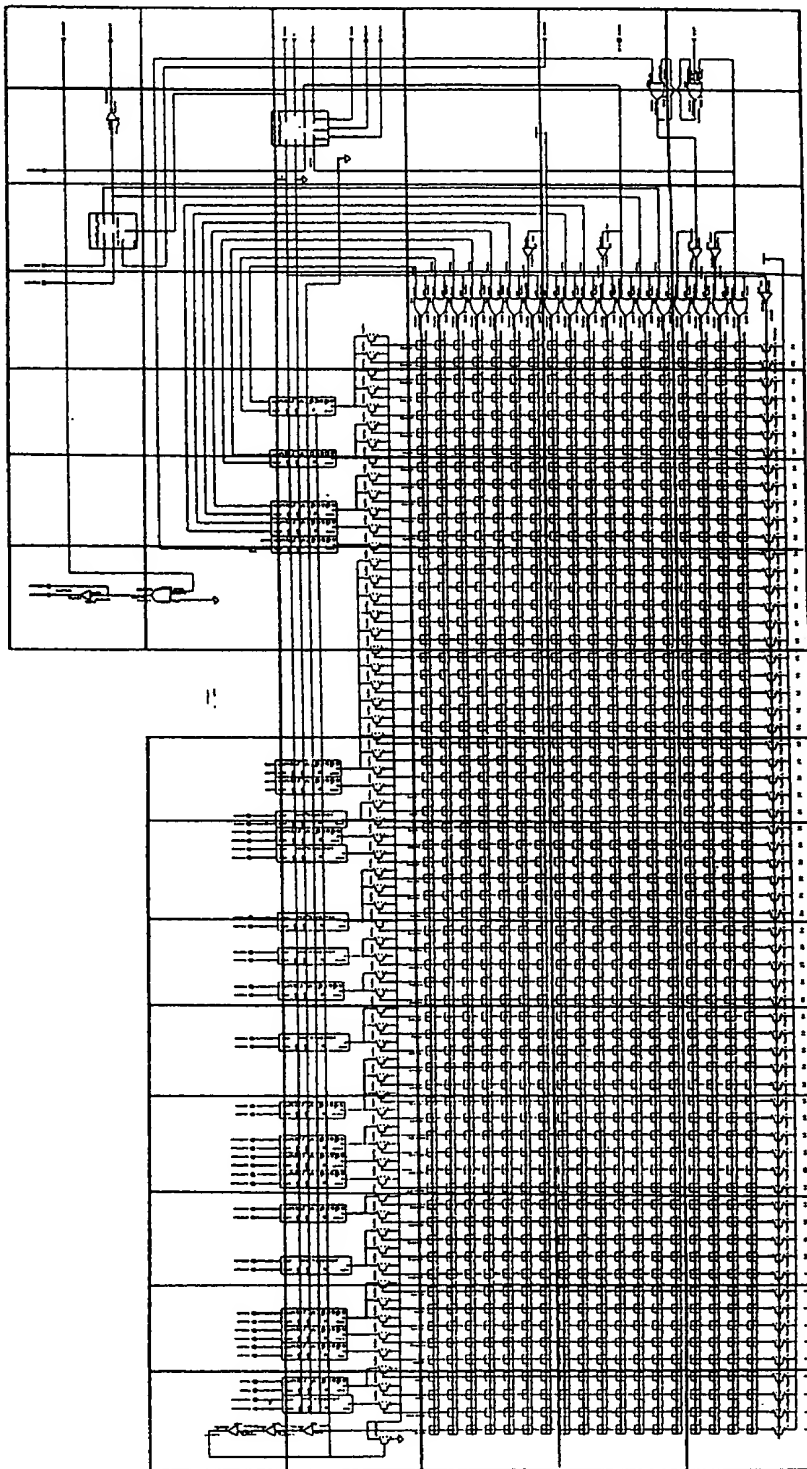


Fig. 9.0103

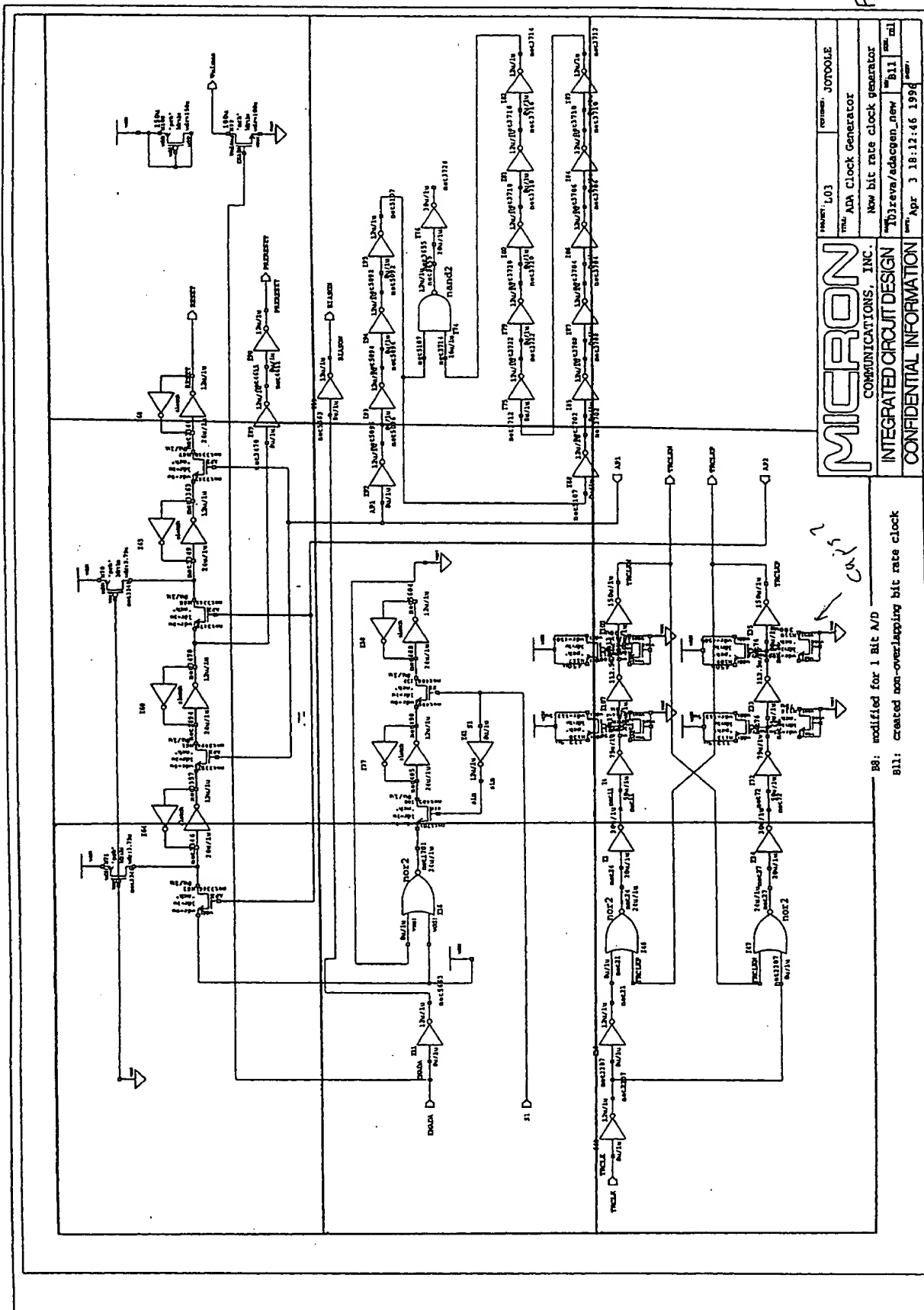
WILSON  
ELECTRONIC EQUIPMENT

T0000"000000

|            |            |            |
|------------|------------|------------|
| 9.010301AA | 9.010301AB | 9.010301AC |
| 9.010301BA | 9.010301BB | 9.010301BC |
| 9.010301CA | 9.010301CB | 9.010301CC |

II II II II III II

TOP SECRET



**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

PROJECT: L03  
REVISION: J0000LE  
NEW bit rate clock generator  
T3dreva/adagen\_new  
B11  
Apr 3 18:12:46 1998

B8: modified for 1 Bit A/D  
B11: created non-overlapping bit rate clock

Fig. 9.010301

TABLE 1

|            |            |
|------------|------------|
| 9.010302AA | 9.010302AB |
|------------|------------|

TABLE 1

FIG. 9.010302

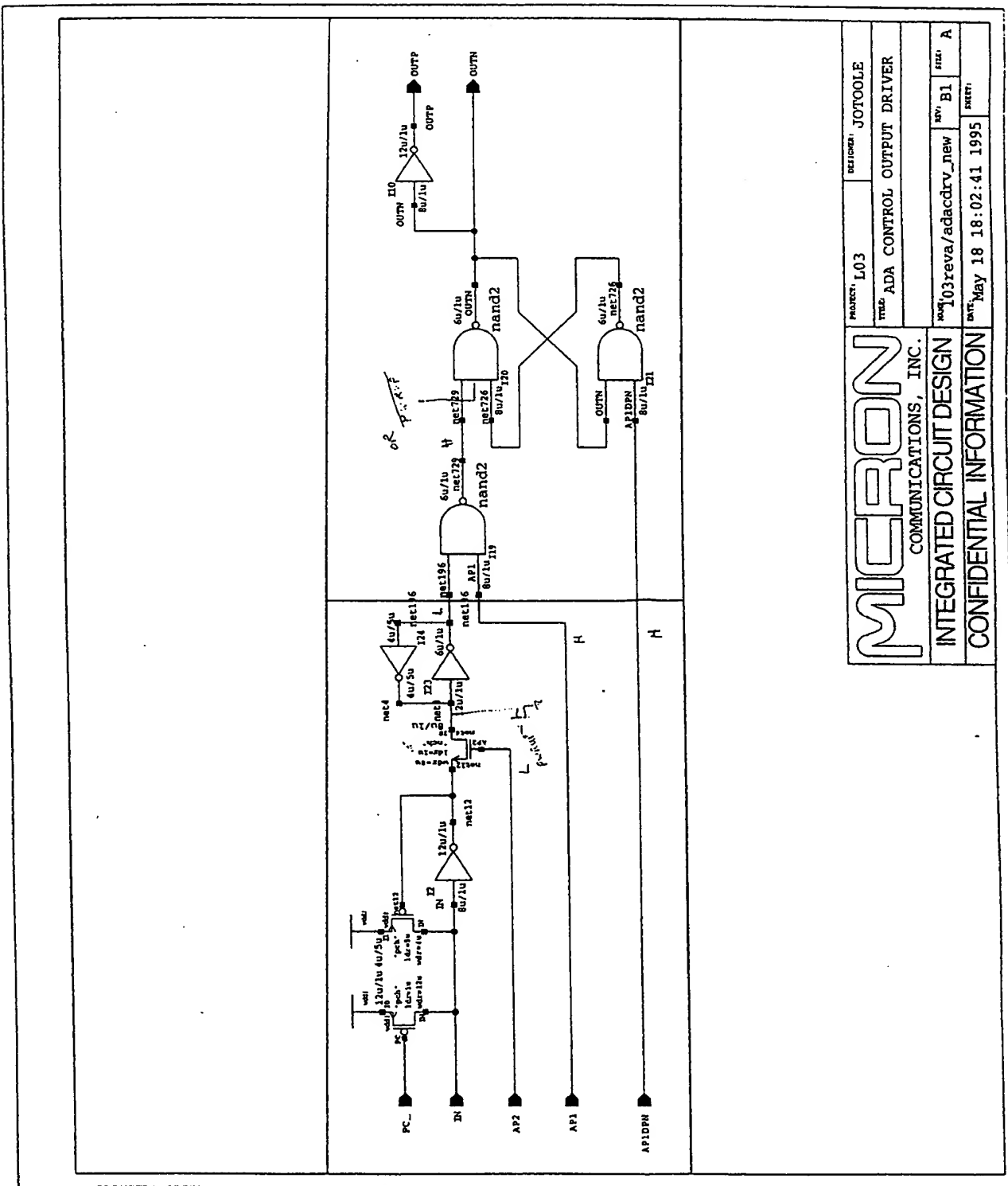


FIG. 9.010302

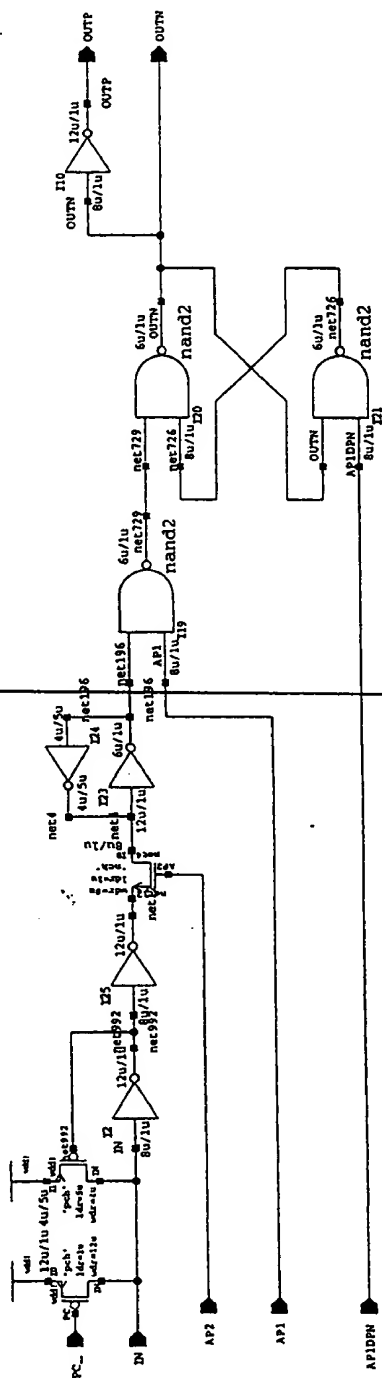
|                           |  |          |                           |
|---------------------------|--|----------|---------------------------|
| MICRON                    |  | DESIGNER | JOTOOLE                   |
| COMMUNICATIONS, INC.      |  | PROJECT  | L03                       |
| INTEGRATED CIRCUIT DESIGN |  | TITLE    | ADA CONTROL OUTPUT DRIVER |
| CONFIDENTIAL INFORMATION  |  | DATE     | 103revA/adacdrv_new       |
|                           |  | REV      | B1                        |
|                           |  | SHEET    | A                         |
|                           |  | DATE     | May 18 18:02:41 1995      |

FOUO" 000000

|            |            |
|------------|------------|
| 9.010303AA | 9.010303AB |
|------------|------------|

II II 9.010303

The first part of the book is devoted to a detailed study of the
 structure of the  $W$ -algebra. In particular, the authors
 prove that the  $W$ -algebra is a simple Lie algebra.



|                           |  |                                 |  |           |  |         |  |
|---------------------------|--|---------------------------------|--|-----------|--|---------|--|
| MICRON                    |  | PROPERTY L03                    |  | DELIVERED |  | JOTOOLE |  |
| COMMUNICATIONS, INC.      |  | TITLE ADA CONTROL OUTPUT DRIVER |  |           |  |         |  |
| INTEGRATED CIRCUIT DESIGN |  | PART# B1                        |  | REV# new  |  | DATE    |  |
| CONFIDENTIAL INFORMATION  |  | MAY 19 15:33:27 1995            |  | PART#     |  | DATE    |  |

Fig. 9.010303



9.010304AA

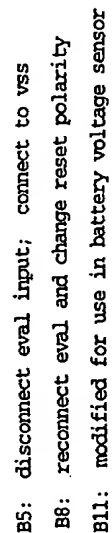
9.010304AB

9.010304AA

9.010304BB

9.010304BA

9.010304



# NOBIS

# INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

|                           |                   |
|---------------------------|-------------------|
| PROJECT: L03              | DESIGNER: JOTOOLE |
| TITLE: ADA Data Latch     |                   |
|                           |                   |
| NAME: _03reva/adadlat_new | REV: Bill         |
| DATE: Apr 8 10:39:12 1996 | SIZE: A           |

9.0104AA 9.0104AB 9.0104AC 9.0104AD 9.0104AE

|          |          |          |          |          |
|----------|----------|----------|----------|----------|
| 9.0104AA | 9.0104AB | 9.0104AC | 9.0104AD | 9.0104AE |
| 9.0104BA | 9.0104BB | 9.0104BC | 9.0104BD | 9.0104BE |
| 9.0104CA | 9.0104CB | 9.0104CC | 9.0104CD |          |
| 9.0104DA | 9.0104DB | 9.0104DC | 9.0104DD |          |

9.0104EA 9.0104EB 9.0104EC 9.0104ED 9.0104EE

[illegible]

|                                |      |                      |       |
|--------------------------------|------|----------------------|-------|
| <b>Micron</b>                  |      | COMMUNICATIONS, INC. |       |
| INTEGRATED CIRCUIT DESIGN      |      |                      |       |
| CONFIDENTIAL INFORMATION       |      |                      |       |
| DATE                           | TIME | LOCATION             | NOTES |
| 10/3                           |      | ADN Analog Bias      |       |
| 10/3-rev/adabias_new 81 cm mil |      |                      |       |
| May 18 17:28:39 1995           |      |                      |       |

TABLE 9.02

|        |        |        |        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|--------|
| 9.02AA | 9.02AB | 9.02AC | 9.02AD | 9.02AE | 9.02AF | 9.02AG | 9.02AH | 9.02AI | 9.02AJ | 9.02AK |
| 9.02BA | 9.02BB | 9.02BC | 9.02BD | 9.02BE | 9.02BF | 9.02BG | 9.02BH | 9.02BI | 9.02BJ | 9.02BK |
| 9.02CA |        | 9.02CC | 9.02CD | 9.02CE | 9.02CF | 9.02CG | 9.02CH | 9.02CI | 9.02CJ | 9.02CK |
| 9.02DA | 9.02DB | 9.02DC | 9.02DD |        | 9.02DF | 9.02DG | 9.02DH | 9.02DI | 9.02DJ | 9.02DK |

9.02

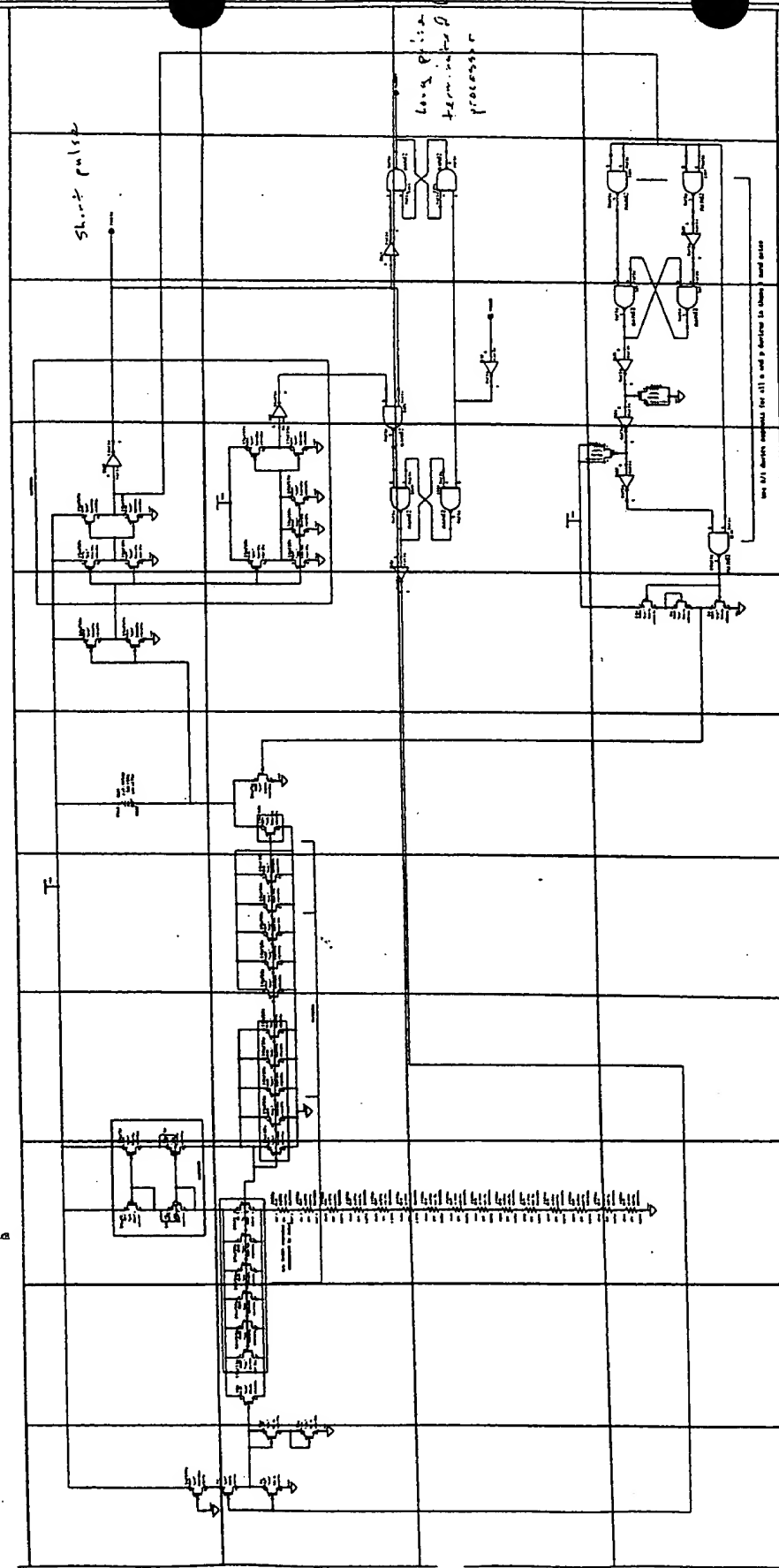
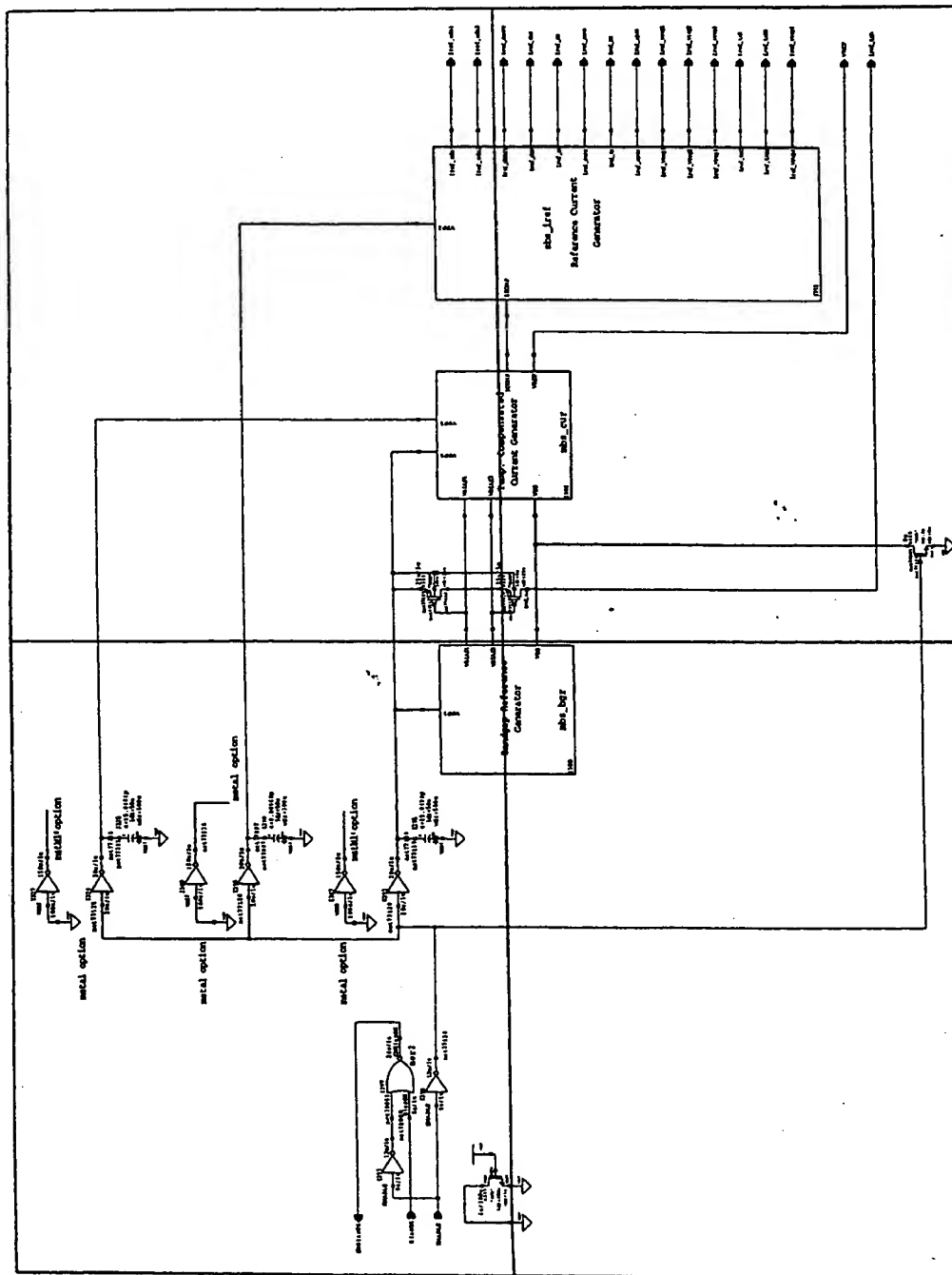


FIG. 9.02

Table 9.0300

|        |        |
|--------|--------|
| 9.03AA | 9.03AB |
| 9.03BA | 9.03BB |

MI 40 9.0300

[illegible]

02: deleted TESTED function  
added DISCARD logic  
created buffered VOP

|                           |  |                      |  |         |  |
|---------------------------|--|----------------------|--|---------|--|
| <b>MICRON</b>             |  | L03                  |  | INGOINS |  |
| COMMUNICATIONS, INC.      |  | Master Data Source   |  |         |  |
| INTEGRATED CIRCUIT DESIGN |  | 103 ewe/rdb          |  | 103 rdl |  |
| CONFIDENTIAL INFORMATION  |  | JUL 28 11:04:42 1995 |  |         |  |



|          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 9.0301AA | 9.0301AB | 9.0301AC | 9.0301AD | 9.0301AE | 9.0301AF | 9.0301AG | 9.0301AH | 9.0301AI | 9.0301AJ |
| 9.0301BA | 9.0301BB | 9.0301BC | 9.0301BD | 9.0301BE | 9.0301BF | 9.0301BG | 9.0301BH | 9.0301BI | 9.0301BJ |
| 9.0301CB | 9.0301CC | 9.0301CD | 9.0301CE | 9.0301CF | 9.0301CG | 9.0301CH | 9.0301CI | 9.0301CJ |          |
| 9.0301DB | 9.0301DC | 9.0301DD | 9.0301DE | 9.0301DF | 9.0301DG | 9.0301DH | 9.0301DI | 9.0301DJ |          |

TOP SECRET

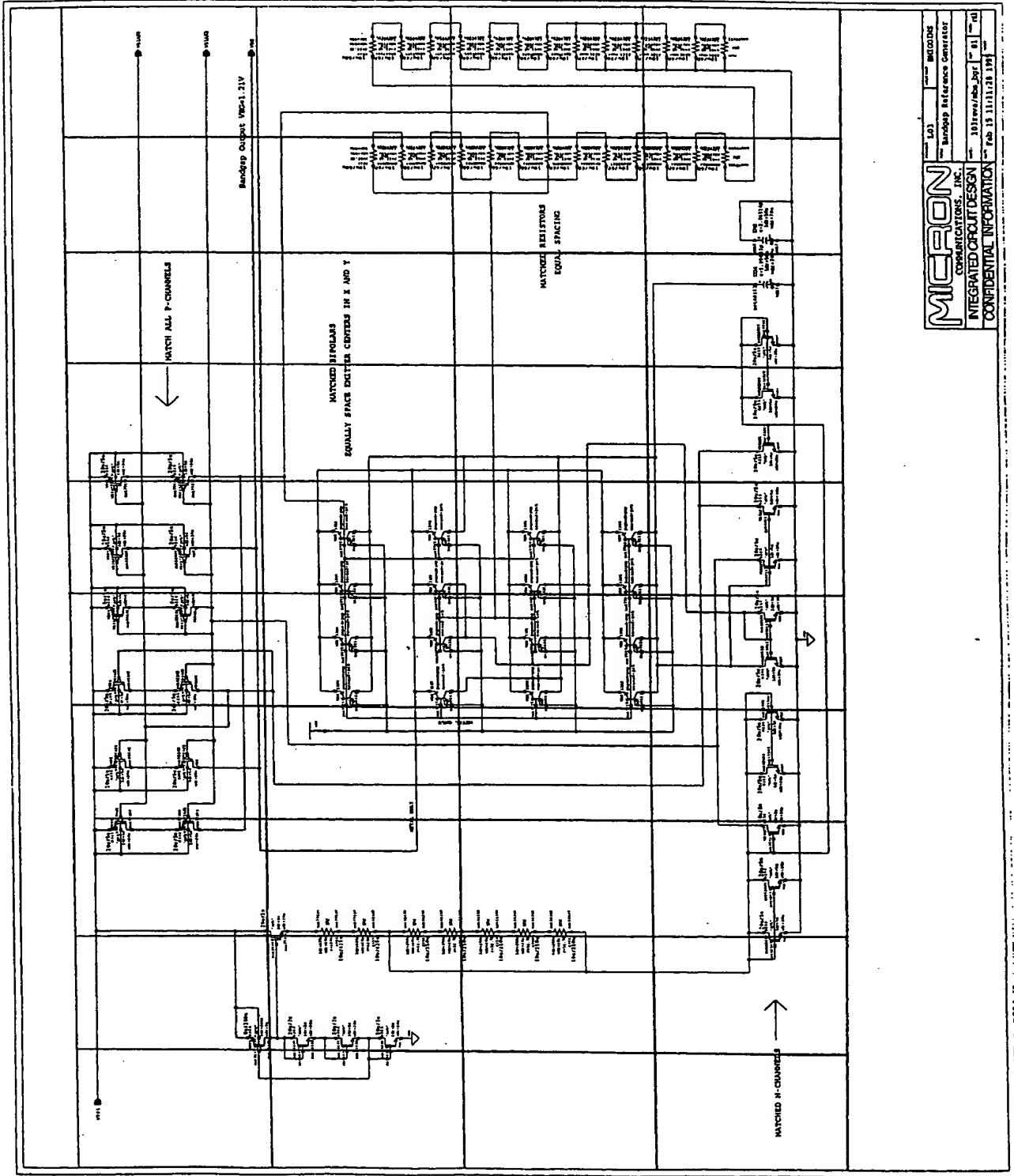


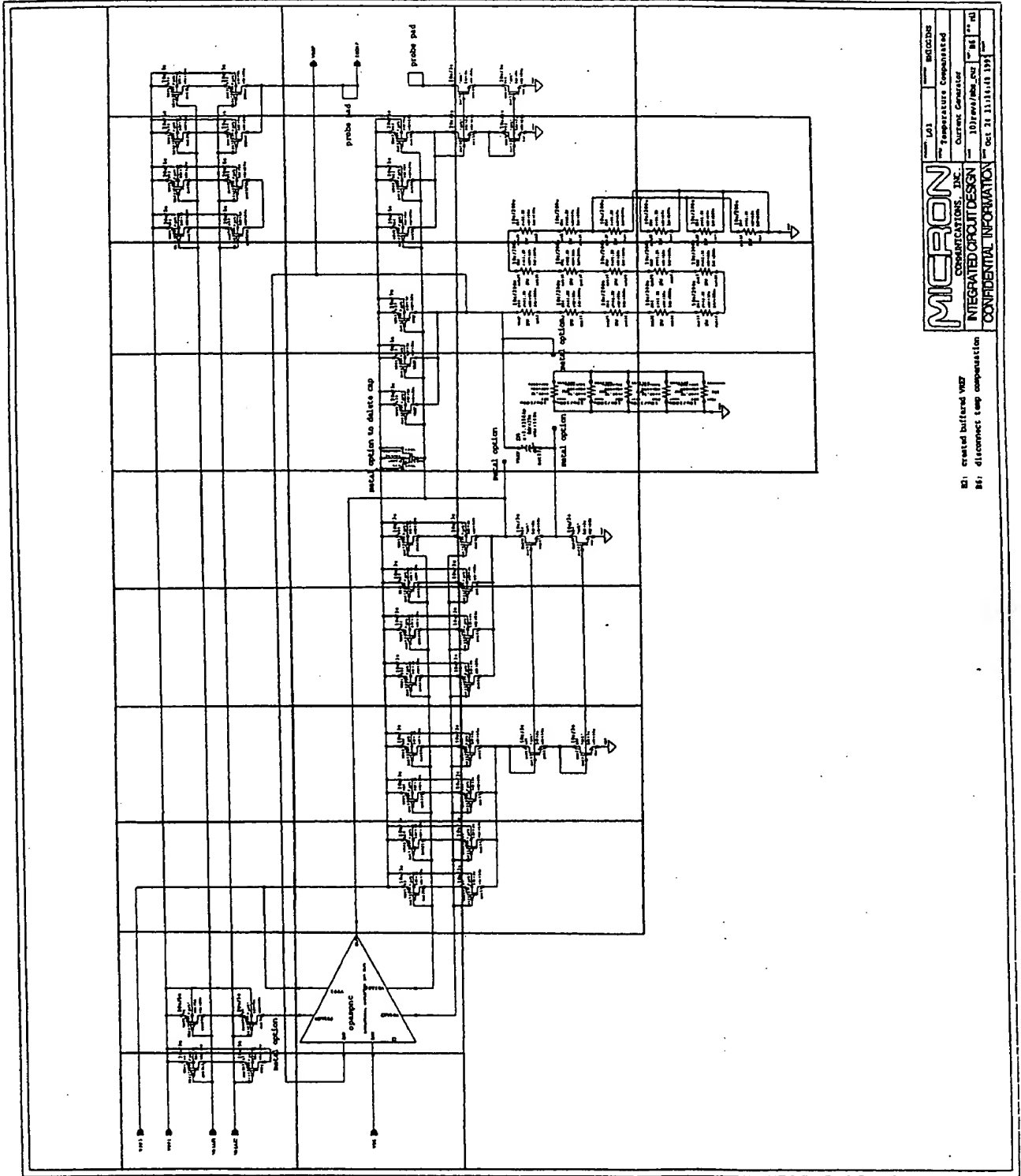
FIG. 9.0301

|                             |              |
|-----------------------------|--------------|
| <b>MICRON</b>               |              |
| DESIGN                      | CONFIDENTIAL |
| INTEGRATED CIRCUIT DESIGN   |              |
| CONFIDENTIAL INFORMATION    |              |
| Lot                         | 881010105    |
| Bandgap Reference Generator |              |
| 101rev/Dec 87               | 81           |
| Feb 15 11:11:38 1987        |              |

|          |          |          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| 9.0302AA | 9.0302AB | 9.0302AC | 9.0302AD | 9.0302AE | 9.0302AF | 9.0302AG | 9.0302AH | 9.0302AI | 9.0302AJ |
| 9.0302BA | 9.0302BB | 9.0302BC | 9.0302BD | 9.0302BE | 9.0302BF | 9.0302BG | 9.0302BH | 9.0302BI | 9.0302BJ |
|          |          | 9.0302CC | 9.0302CD | 9.0302CE | 9.0302CF | 9.0302CG | 9.0302CH | 9.0302CI | 9.0302CJ |
|          |          |          |          |          |          |          |          | 9.0302DI |          |

60660" 69022600

Fig. 9.0302

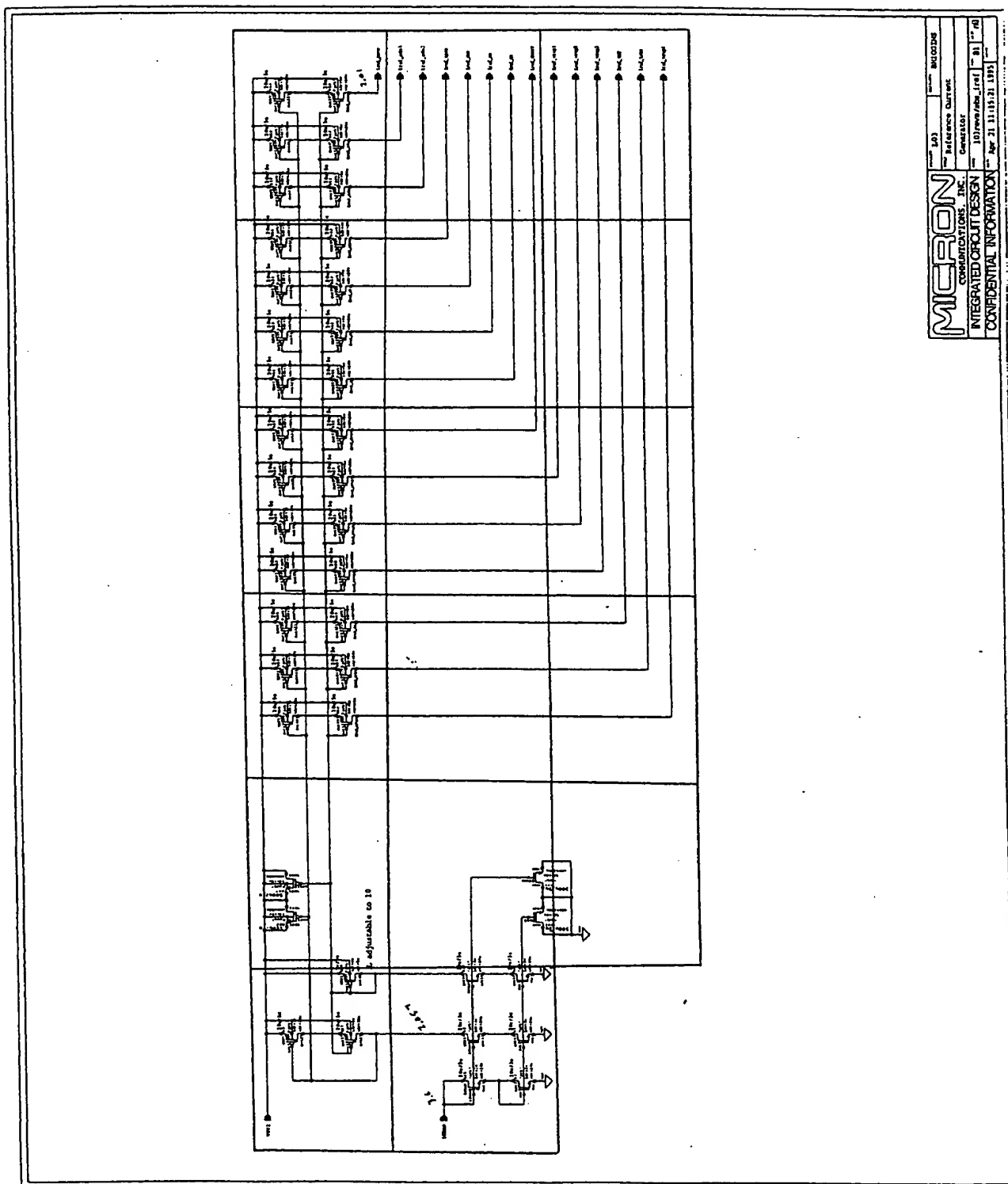


|                           |  |                          |         |
|---------------------------|--|--------------------------|---------|
| MICRON                    |  | LA1                      | BD00005 |
| INTEGRATED CIRCUIT DESIGN |  | Temperature Compensation |         |
| CONFIDENTIAL INFORMATION  |  | Current Compensation     |         |
|                           |  | 10/rev/Rev. 202          | 81      |
|                           |  | Oct 24 11:14:18 1991     | 81      |

Q1: current buffered VDD  
B1: disconnect temp compensation

|          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|
| 9.0303AA | 9.0303AB | 9.0303AC | 9.0303AD | 9.0303AE | 9.0303AF |
| 9.0303BA | 9.0303BB | 9.0303BC | 9.0303BD | 9.0303BE | 9.0303BF |
|          | 9.0303CB | 9.0303CC | 9.0303CD | 9.0303CE | 9.0303CF |

On the other hand, these results suggest that the effect of the  $\beta$  parameter on the  $\alpha$  parameter is not significant in the case of the  $\beta$  parameter. This is because the  $\beta$  parameter is not significant in the case of the  $\beta$  parameter.



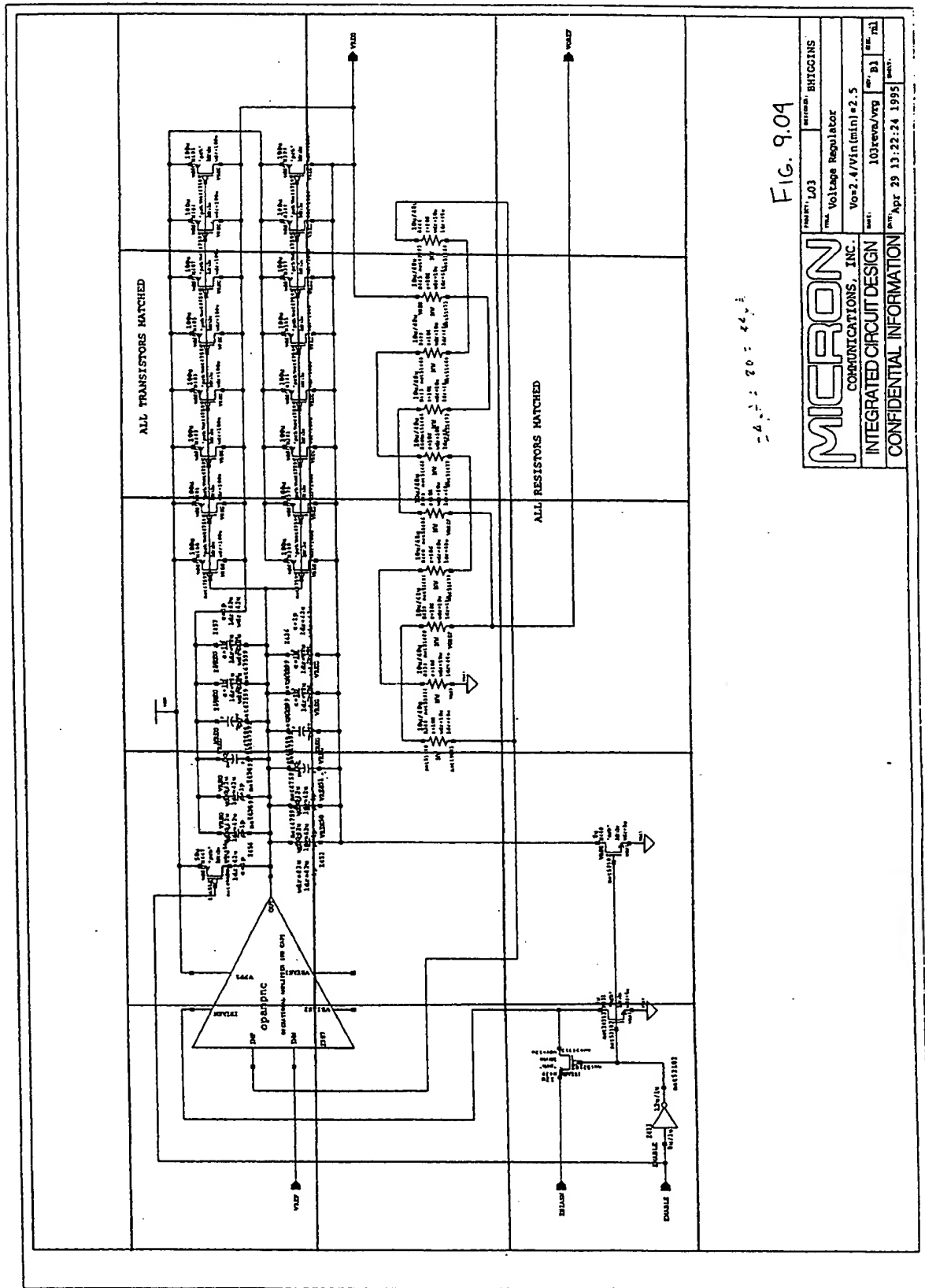
[illegible]

MI40-030

|        |        |        |        |        |
|--------|--------|--------|--------|--------|
| 9.04AA | 9.04AB | 9.04AC | 9.04AD | 9.04AE |
| 9.04BA | 9.04BB | 9.04BC | 9.04BD | 9.04BE |
| 9.04CA | 9.04CB | 9.04CC | 9.04CD | 9.04CE |

И. И. Б.

SECRET



20:44

Fig. 9.04

|                           |  |                            |                   |
|---------------------------|--|----------------------------|-------------------|
| MICRON                    |  | PROJECT: L03               | REVISION: BHTGINS |
| COMMUNICATIONS, INC.      |  | Voltage Regulator          |                   |
| INTEGRATED CIRCUIT DESIGN |  | Vo=2.4V/Vin(min)=2.5       |                   |
| CONFIDENTIAL INFORMATION  |  | 103revs/vrg                |                   |
|                           |  | DATE: Apr 29 13:22:24 1995 |                   |

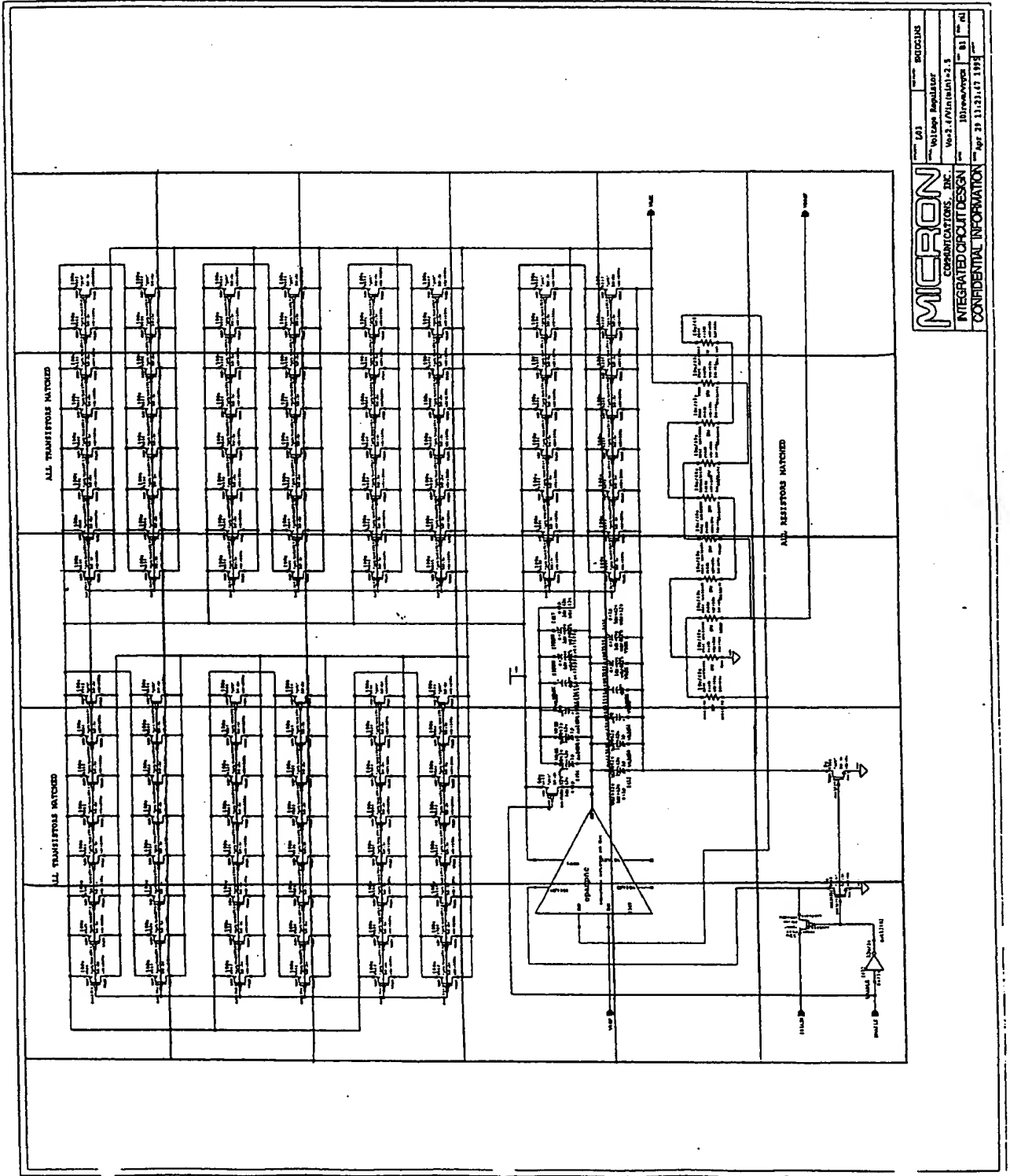


9.05AA 9.05AB 9.05AC 9.05AD 9.05AE

|        |        |        |        |        |
|--------|--------|--------|--------|--------|
| 9.05AA | 9.05AB | 9.05AC | 9.05AD | 9.05AE |
| 9.05BA | 9.05BB | 9.05BC | 9.05BD | 9.05BE |
| 9.05CA | 9.05CB | 9.05CC | 9.05CD | 9.05CE |
| 9.05DA | 9.05DB | 9.05DC | 9.05DD | 9.05DE |
| 9.05EA | 9.05EB | 9.05EC | 9.05ED | 9.05EE |
| 9.05FA | 9.05FB | 9.05FC | 9.05FD | 9.05FE |

9.05

FIG. 9.05



FORM 9-60

MI40-030

|          |          |          |          |
|----------|----------|----------|----------|
| 9.0501AA | 9.0501AB | 9.0501AC | 9.0501AD |
| 9.0501BA | 9.0501BB | 9.0501BC | 9.0501BD |
| 9.0501CA | 9.0501CB | 9.0501CC | 9.0501CD |

II 9.0501





TABLE "continued"

|          |          |          |          |          |         |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|---------|----------|----------|----------|----------|----------|----------|
| 9.0902AA | 9.0902AB | 9.0902AC | 9.0902AD | 9.0902AE | 9.0902F | 9.0902AG | 9.0902AH | 9.0902AI | 9.0902AJ | 9.0902AK | 9.0902AL |
| 9.0902BA | 9.0902BB | 9.0902BC | 9.0902BD | 9.0902BE | 9.0902F | 9.0902BG | 9.0902BH | 9.0902BI | 9.0902BJ | 9.0902BK | 9.0902BL |
|          |          | 9.0902CC | 9.0902CD | 9.0902CE | 9.0902F | 9.0902CG | 9.0902CH | 9.0902CI | 9.0902CJ | 9.0902CK | 9.0902CL |
|          |          | 9.0902DC | 9.0902DD | 9.0902DE | 9.0902F |          |          |          |          |          | 9.0902DL |
| 9.0902EA | 9.0902EB | 9.0902EC | 9.0902ED | 9.0902EE | 9.0902F | 9.0902EG | 9.0902EH | 9.0902EI | 9.0902EJ | 9.0902EK | 9.0902EL |
|          |          |          | 9.0902FD | 9.0902FE | 9.0902F | 9.0902FG | 9.0902FH | 9.0902FI | 9.0902FJ | 9.0902FK | 9.0902FL |

ISSUED 9.09.02







Fig. 9.0903



|              |                 |
|--------------|-----------------|
| PROJECT: L03 | DESIGN: JOTOOLE |
|--------------|-----------------|

**Clock Generator**

**B8: wired cross-couples to ground**

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

|                            |         |          |
|----------------------------|---------|----------|
| NAME: 103reva/rcg_clkgen   | REV: 88 | REV: nil |
| DATE: Jan 24 09:56:43 1996 |         |          |

"00000" 00000000

|      |      |      |      |
|------|------|------|------|
| 10AA | 10AB | 10AC | 10AD |
| 10BA | 10BB | 10BC | 10BD |
| 10CA | 10CB | 10CC | 10CD |
| 10DA | 10DB | 10DC | 10DD |

1111 1111

Fig. 10

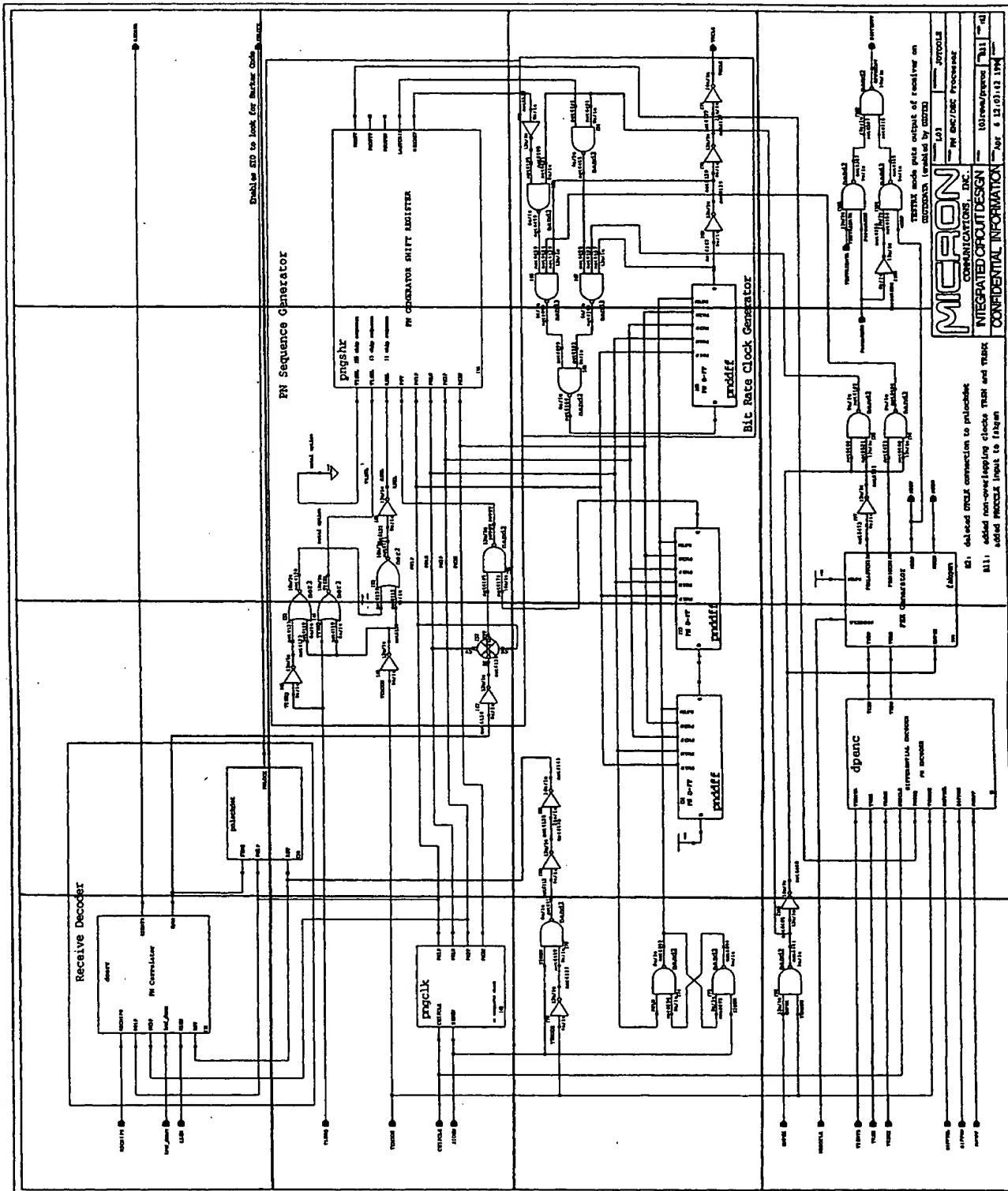


TABLE "E" SECTION

8

|         |         |         |         |         |         |         |         |         |         |
|---------|---------|---------|---------|---------|---------|---------|---------|---------|---------|
| 10.01AA | 10.01AB | 10.01AC | 10.01AD | 10.01AE | 10.01AF | 10.01AG |         |         |         |
| 10.01BA | 10.01BB | 10.01BC | 10.01BD | 10.01BE | 10.01BF | 10.01BG | 10.01BH | 10.01BI | 10.01BJ |
| 10.01CA | 10.01CB | 10.01CC | 10.01CD | 10.01CE | 10.01CF | 10.01CG | 10.01CH | 10.01CI | 10.01CJ |
|         | 10.01DB | 10.01DC | 10.01DD | 10.01DE | 10.01DF | 10.01DG | 10.01DH | 10.01DI | 10.01DJ |

II. II. II. II. II

The diagram is a complex schematic of a Micron 10D01 integrated circuit. It features a grid of horizontal and vertical lines representing signal paths. Various logic components are distributed across the grid, including:

- Logic Gates:** Numerous AND, OR, and NOT gates are shown, some with multiple inputs and outputs.
- Flip-Flops:** Several D-type flip-flops are present, each with a clock input, a data input, and a data output.
- Registers:** Two 4-bit registers are shown at the bottom of the diagram, each with four data inputs and four data outputs.
- Interconnects:** A dense network of lines connects the various components, forming a complex web of signal paths.

The title block in the top right corner contains the following information:

**Fig. 10.01**

**MICRON**  
CORPORATION  
INTEGRATED CIRCUITS  
CONFIDENTIAL INFORMATION

Part No. 10D01  
Rev. 1.0  
Date 10/11/77  
By 10D01/1.0

[illegible]

[illegible]

|           |           |           |           |           |           |           |
|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| 10.0101AA | 10.0101AB | 10.0101AC | 10.0101AD | 10.0101AE | 10.0101AF | 10.0101AG |
| 10.0101BA | 10.0101BB | 10.0101BC | 10.0101BD | 10.0101BE | 10.0101BF | 10.0101BG |

И. П. Павлов И. П. Павлов

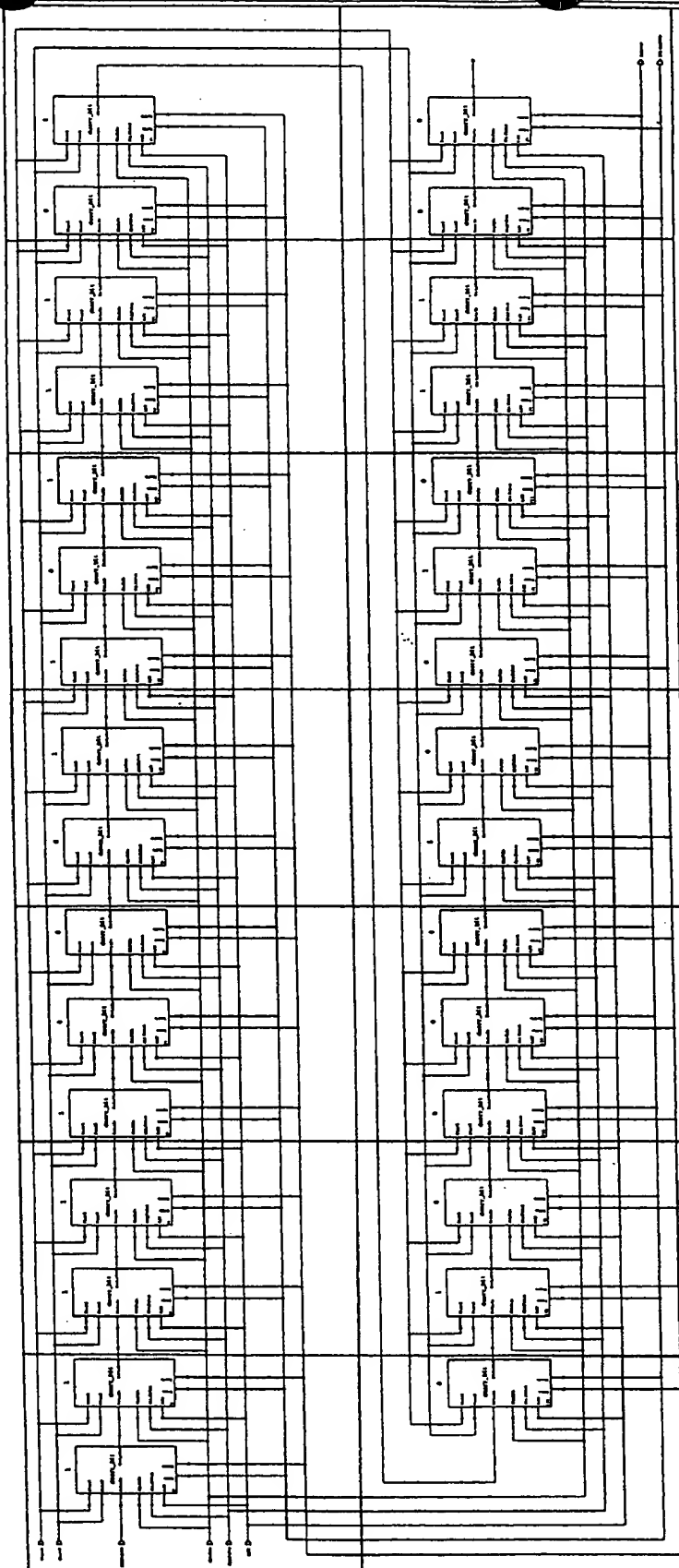
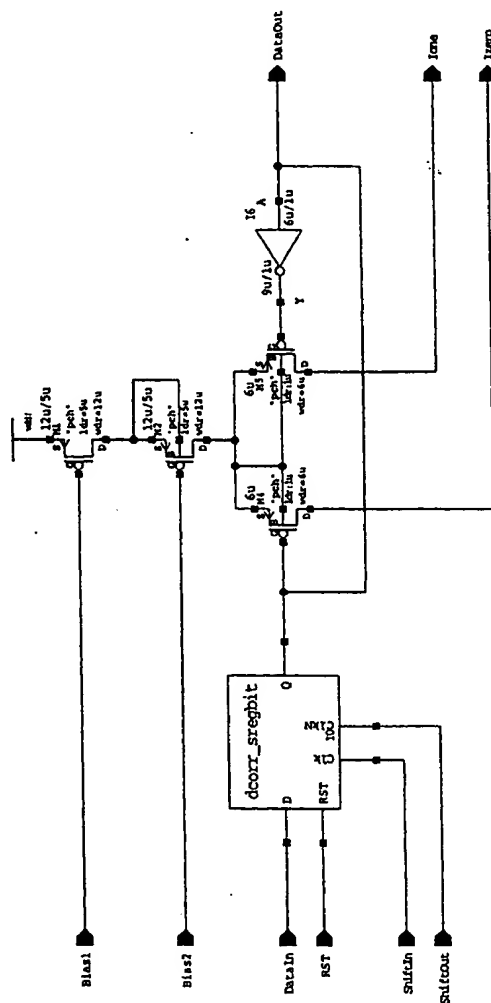
[illegible]

Fig. 10.0101

|                      |         |                           |            |                          |             |
|----------------------|---------|---------------------------|------------|--------------------------|-------------|
| <b>MICRON</b>        |         | INTEGRATED CIRCUIT DESIGN |            | CONFIDENTIAL INFORMATION |             |
| COMMUNICATIONS, INC. |         |                           |            |                          |             |
| DATE                 | REVISED | DESIGNED BY               | CHECKED BY | DATE                     | TIME        |
| 10-1-78              |         | WILLIAM W. GEAR, JR.      |            | 10-1-78                  | 11:25:00 PM |

FIG. 10.010101



|                                  |                   |                             |         |
|----------------------------------|-------------------|-----------------------------|---------|
| <b>MICRON</b>                    |                   | <b>COMMUNICATIONS, INC.</b> |         |
| <b>INTEGRATED CIRCUIT DESIGN</b> |                   |                             |         |
| <b>CONFIDENTIAL INFORMATION</b>  |                   |                             |         |
| PROJECT: L03                     | DESIGNER: JOTOOLE |                             |         |
| TITLE: Correlator Bit            |                   | MADE: 103revA/dcorr_bit     | REV: B1 |
|                                  |                   | SIZE: A                     |         |
|                                  |                   | DATE: Sep 9 11:37:26 1994   |         |
|                                  |                   | SHEET: 1                    |         |



Figure 10.01010101

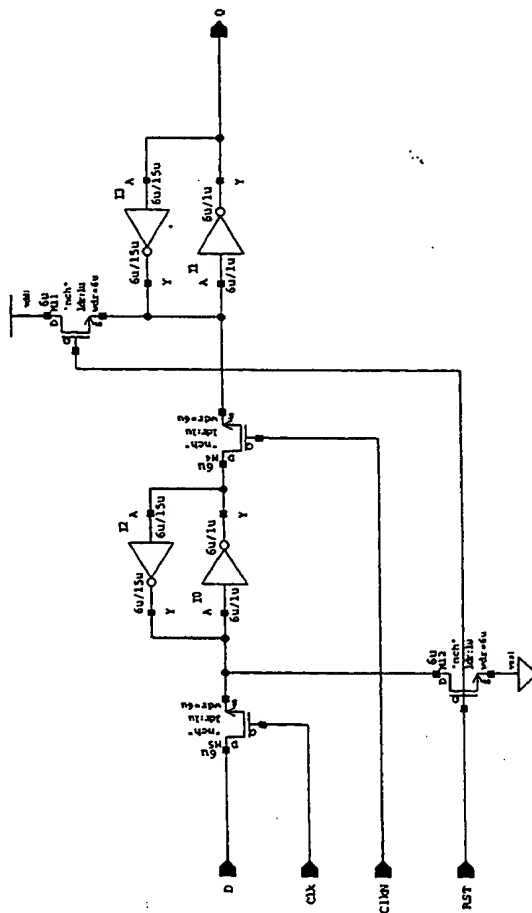


Fig. 10.01010101

|                           |  |                            |                   |
|---------------------------|--|----------------------------|-------------------|
| MICRON                    |  | PROJECT: L03               | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: Shift Register Cell |                   |
| INTEGRATED CIRCUIT DESIGN |  | REV: B1                    | SIZE: A           |
| CONFIDENTIAL INFORMATION  |  | DATE: Sep 9 14:08:50 1994  | DESIGNER:         |

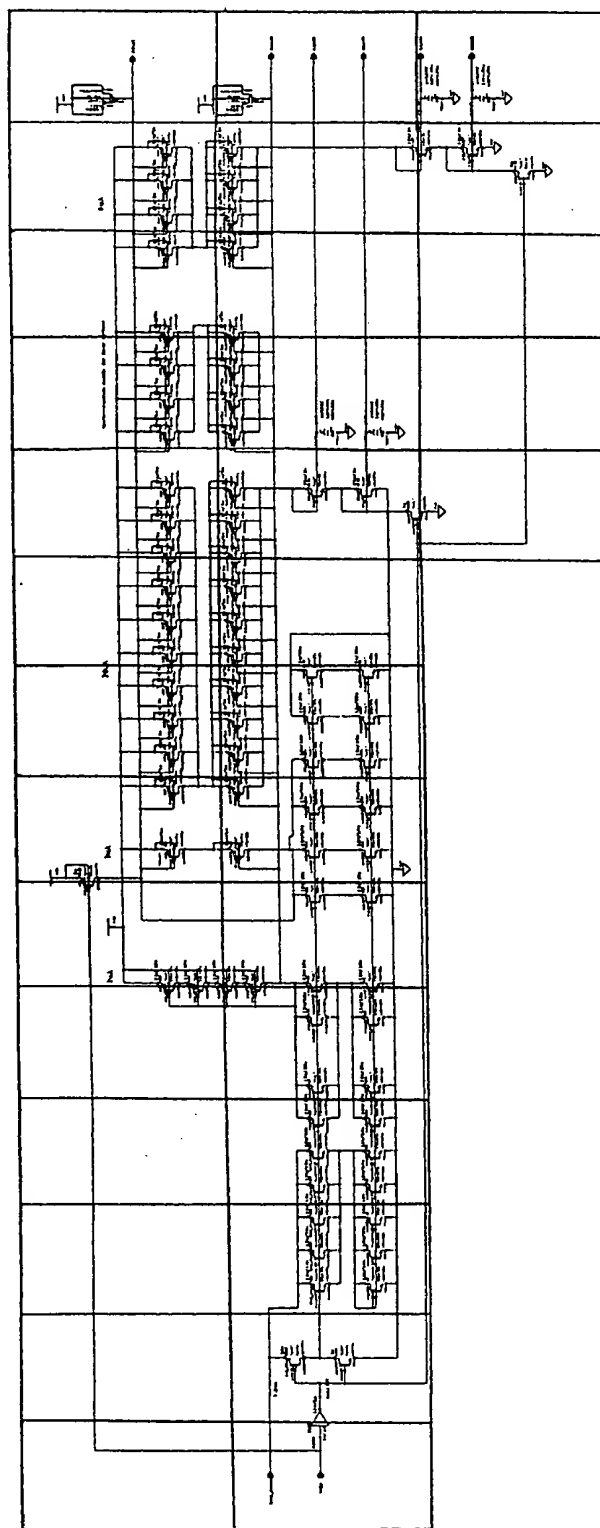
[illegible]

MI40-030

|   |           |           |           |           |           |           |           |           |           |           |           |           |           |
|---|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| 10.0102AA   | 10.0102AB | 10.0102AC | 10.0102AD | 10.0102AE | 10.0102AF | 10.0102AG | 10.0102AH | 10.0102AI | 10.0102AJ | 10.0102AK | 10.0102AL | 10.0102AM | 10.0102AN |
| 10.0102BA   | 10.0102BB | 10.0102BC | 10.0102BD | 10.0102BE | 10.0102BF | 10.0102BG | 10.0102BH | 10.0102BI | 10.0102BJ | 10.0102BK | 10.0102BL | 10.0102BM | 10.0102BN |
|   |           |           |           |           |           |           |           |           |           |           |           |           |           |
| 10.0102CJ 10.0102CK 10.0102CL 10.0102CM 10.0102CN |           |           |           |           |           |           |           |           |           |           |           |           |           |

II II III    III. III. III. III.

2



III: ending of the film is strong

10.02

10.02

2025-03-23 14:30:00

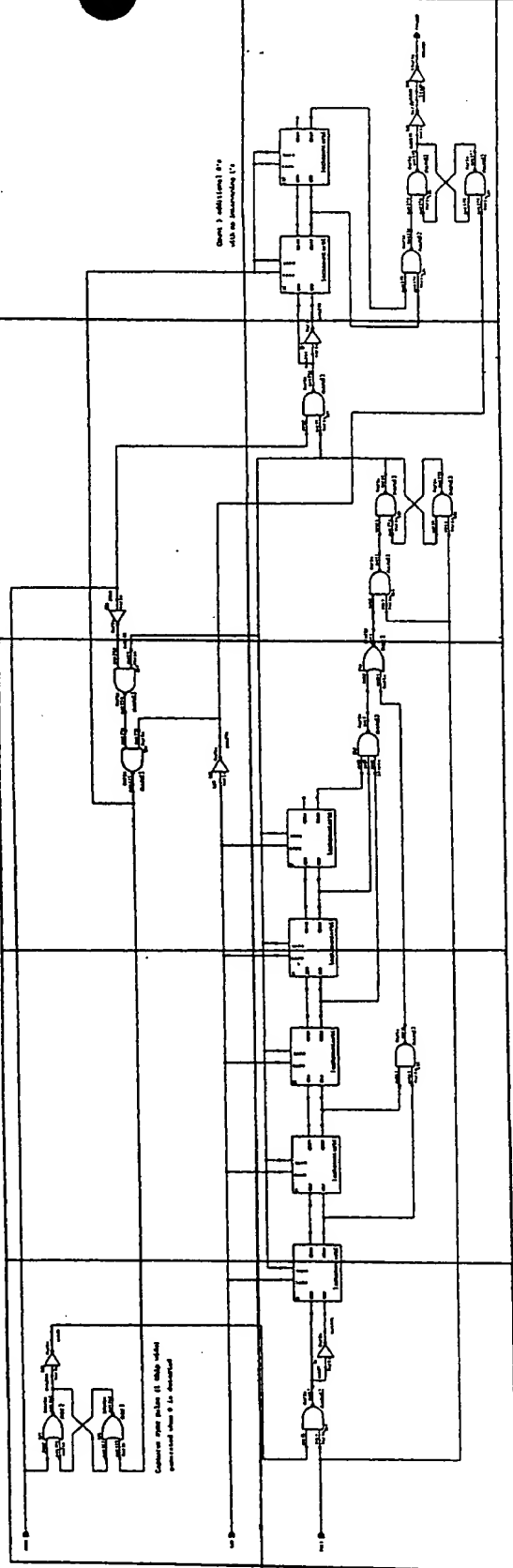
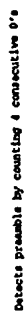


Fig. 10.02

10.0201AA 10.0201AB

|           |           |
|-----------|-----------|
| 10.0201AA | 10.0201AB |
|-----------|-----------|

10.0201

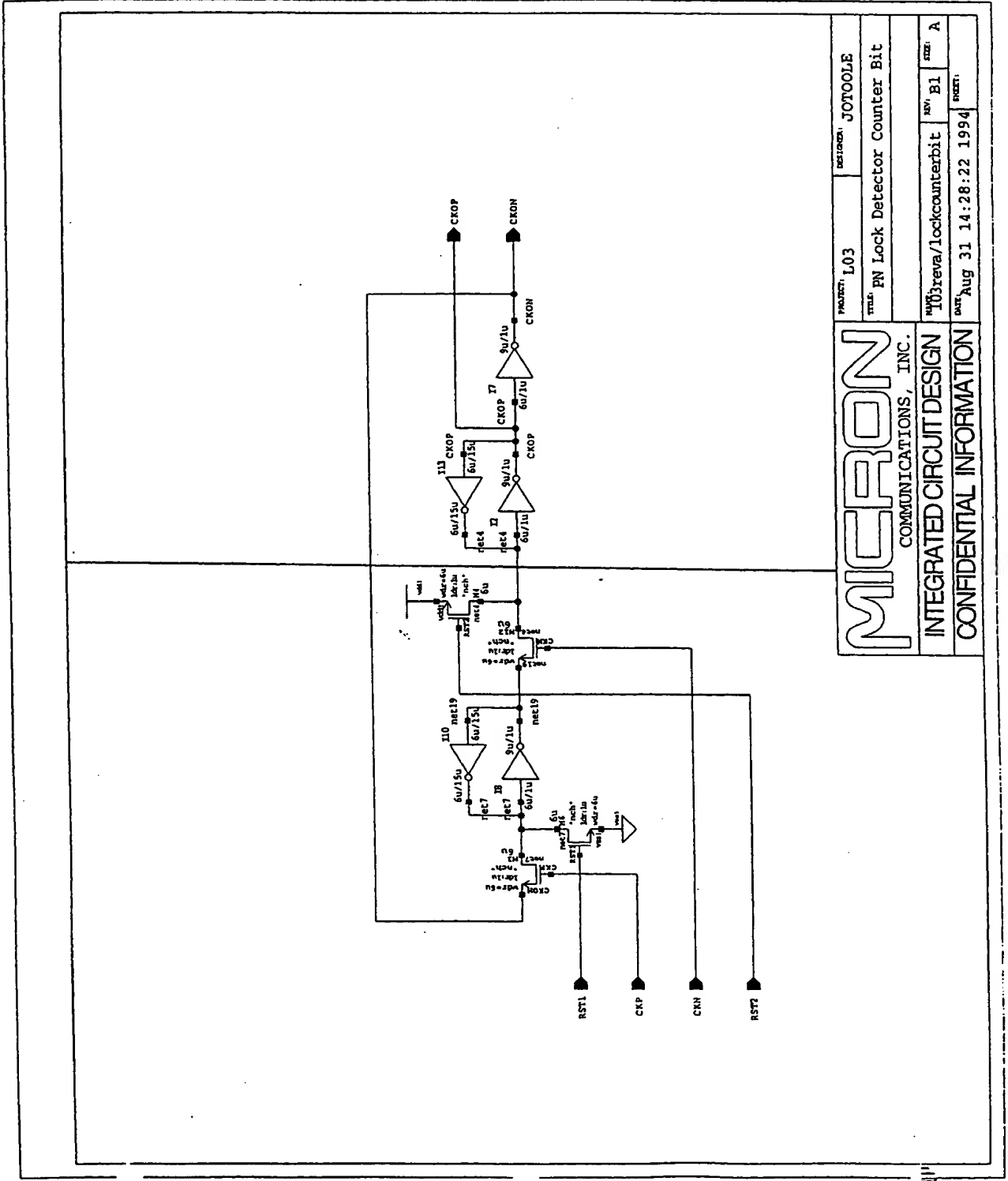


FIG. 10.0201

|                           |  |          |                              |
|---------------------------|--|----------|------------------------------|
| MICRON                    |  | DESIGNER | JOTOOLE                      |
| COMMUNICATIONS, INC.      |  | PROJECT  | L03                          |
| INTEGRATED CIRCUIT DESIGN |  | TITLE    | PN Lock Detector Counter Bit |
| CONFIDENTIAL INFORMATION  |  | REV.     | B1                           |
|                           |  | REV.     | A                            |
|                           |  | DATE     | Aug 31 14:28:22 1994         |

10.03.00

|  |  |
|--|--|
|  |  |
|--|--|

10.03AB

10.03AA

10.03





[illegible]

|         |         |         |         |         |
|---------|---------|---------|---------|---------|
| 10.04AA | 10.04AB | 10.04AC | 10.04AD | 10.04AE |
| 10.04BA | 10.04BB | 10.04BC | 10.04BD | 10.04BE |
| 10.04CA | 10.04CB | 10.04CC | 10.04CD | 10.04CE |

II II





|           |           |
|-----------|-----------|
| 10.0402AA | 10.0402AB |
| 10.0402BA | 10.0402BB |
| 10.0402CA | 10.0402CB |

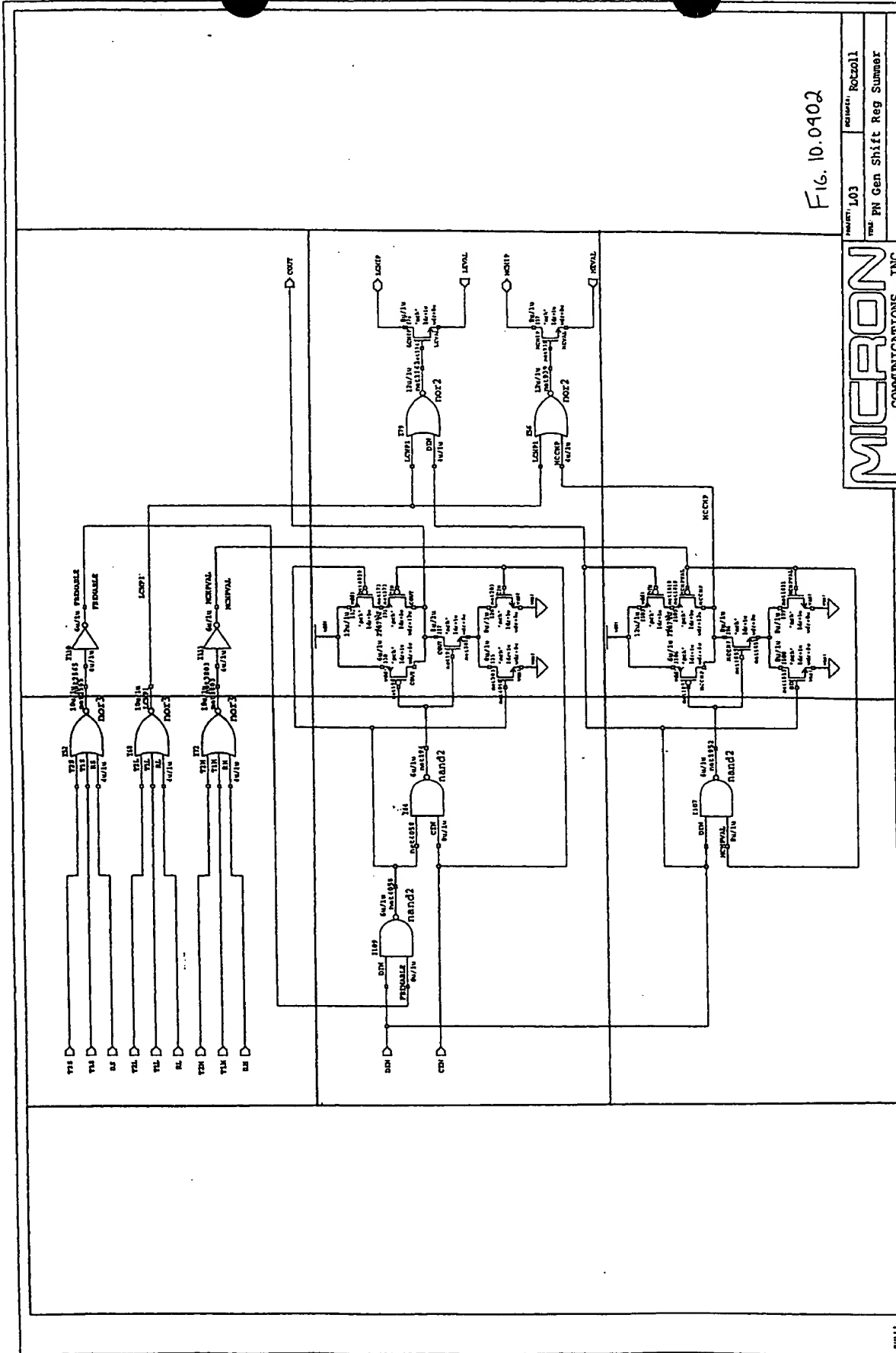


FIG. 10.0402

|                           |  |                           |              |
|---------------------------|--|---------------------------|--------------|
| MICRON                    |  | PRODUCT 103               | REVISION 103 |
| COMMUNICATIONS, INC.      |  | PN Gen Shift Reg Summer   |              |
| INTEGRATED CIRCUIT DESIGN |  | DATE 103-001              | REV - 103    |
| CONFIDENTIAL INFORMATION  |  | DATE Nov 20 21:23:00 1993 | REV - 103    |



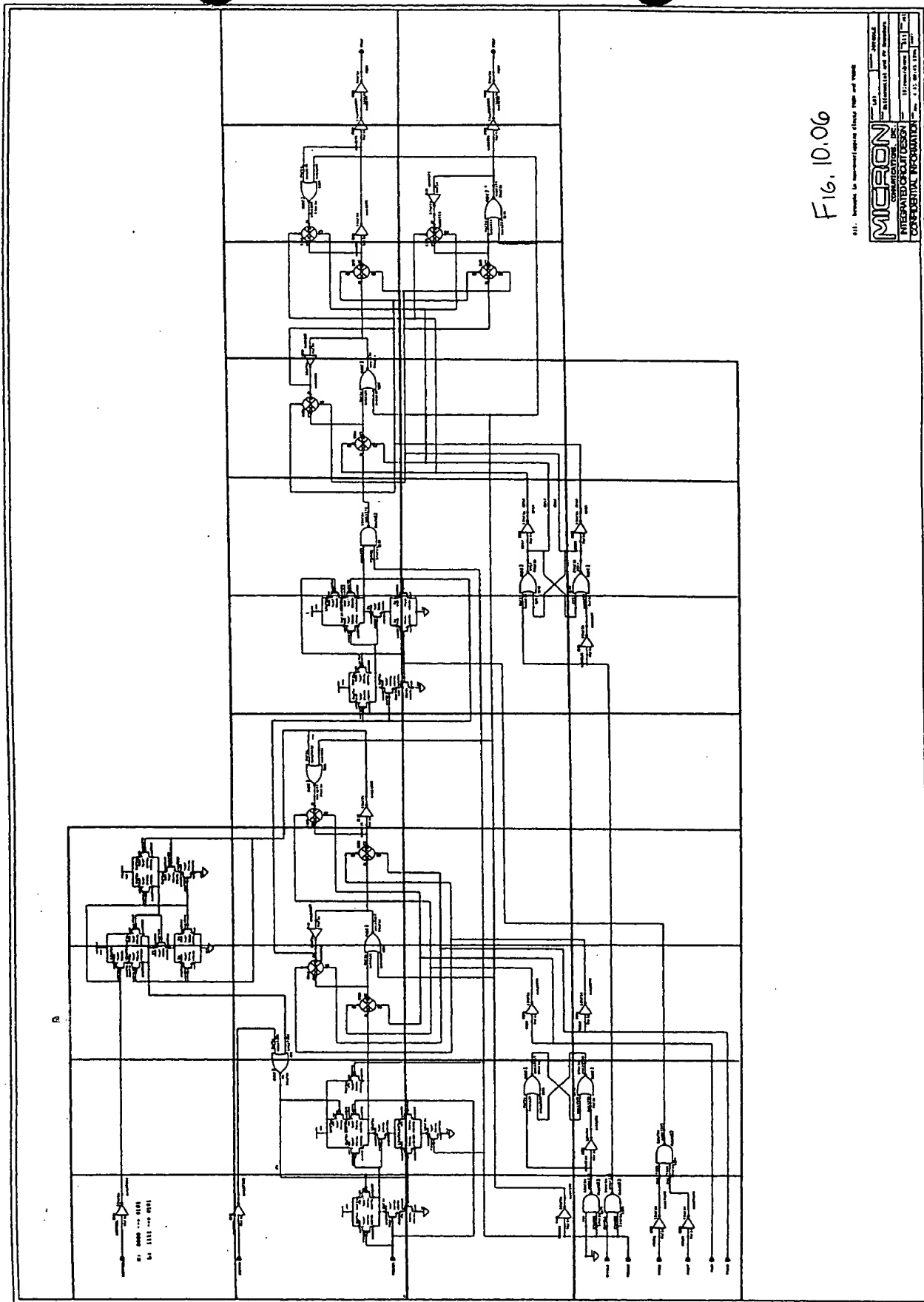
THE UNIVERSITY OF CHICAGO PRESS

|         |         |         |         |         |         |         |         |         |         |         |  |  |
|---------|---------|---------|---------|---------|---------|---------|---------|---------|---------|---------|--|--|
| 10.06AA | 10.06AB | 10.06AC | 10.06AD |         |         |         |         |         |         |         |  |  |
| 10.06BA | 10.06BB | 10.06BC | 10.06BD | 10.06BE | 10.06BF | 10.06BG | 10.06BH | 10.06BI | 10.06BJ | 10.06BK |  |  |
| 10.06CA | 10.06CB | 10.06CC | 10.06CD | 10.06CE | 10.06CF | 10.06CG | 10.06CH | 10.06CI | 10.06CJ | 10.06CK |  |  |
| 10.06DA | 10.06DB | 10.06DC | 10.06DD | 10.06DE | 10.06DF | 10.06DG | 10.06DH |         |         |         |  |  |

10.06  
11.07



100220 23022500



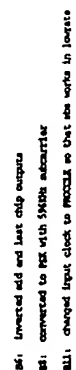
011. Network to monitor/operate (Status) type and value

|                           |     |
|---------------------------|-----|
| <b>MICRON</b>             |     |
| INTEGRATED CIRCUIT DESIGN |     |
| DATE                      | REV |
| 10/10/80                  | 1.1 |
| CONFIDENTIAL INFORMATION  |     |

|         |         |         |         |
|---------|---------|---------|---------|
| 10.07AA | 10.07AB | 10.07AC | 10.07AD |
| 10.07BA | 10.07BB | 10.07BC | 10.07BD |
| 10.07CA | 10.07CB | 10.07CC | 10.07CD |

FILED 10.07.11

Fig. 10.07



|                           |  |                      |  |          |  |
|---------------------------|--|----------------------|--|----------|--|
| <b>MICRON</b>             |  | DATE                 |  | PAGE     |  |
| COMMUNICATIONS, INC.      |  | L03                  |  | JOYTOOLS |  |
| INTEGRATED CIRCUIT DESIGN |  | PSI/PTE General Corp |  |          |  |
| CONFIDENTIAL INFORMATION  |  | 103 pages (13 pages) |  | 7/11/81  |  |
|                           |  | APR 6 11:07:00 1984  |  |          |  |

|           |           |
|-----------|-----------|
| 10.0701AA | 10.0701AB |
|-----------|-----------|

[illegible]

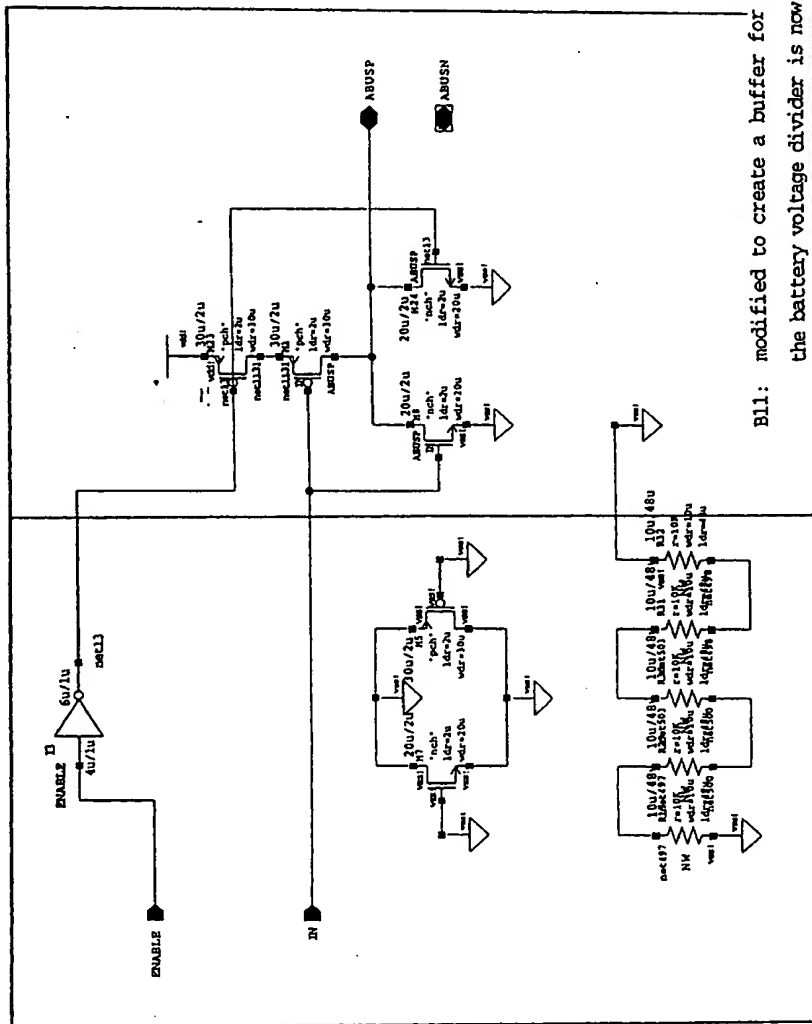


"FOOED" E30E2800

|      |      |
|------|------|
| 11AA | 11AB |
|------|------|

II II

1. **Introduction**  
 2. **Background**  
 3. **Methodology**  
 4. **Results**  
 5. **Discussion**  
 6. **Conclusion**  
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 218



B11: modified to create a buffer for the opamp output  
the battery voltage divider is now part of tsn

|                           |  |                                  |                   |
|---------------------------|--|----------------------------------|-------------------|
| <b>MICRON</b>             |  | PROJECT: L03                     | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: Battery Analog I/O Buffer |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103revA/batalg             | REV: B11          |
| CONFIDENTIAL INFORMATION  |  | DATE: Apr 8 10:19:56 1996        | SHEET: A          |

|      |  |
|------|--|
| 12AB |  |
| 12AA |  |

12AA

ΠΙΝΑΚΑΣ ΣΥΝΟΛΟΥ



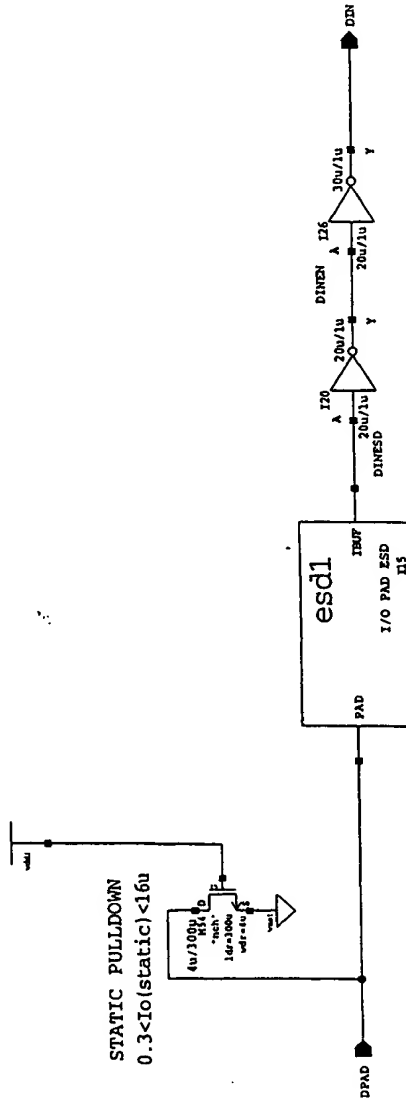
|                               |          |                            |
|-------------------------------|----------|----------------------------|
| PROJECT: L03                  |          | DESIGNER: JOTOOLE          |
| TITLE: Digital I/O Pad Buffer |          |                            |
| NAME: 103reva/paddig          | REV: B15 | DATE: Jul 23 15:14:52 1996 |
| INTEGRATED CIRCUIT DESIGN     |          |                            |
| CONFIDENTIAL INFORMATION      |          |                            |

B15: eliminated feedback devices to improve drive to large loads

|                                  |  |                                 |            |         |  |
|----------------------------------|--|---------------------------------|------------|---------|--|
| <b>MICRON</b>                    |  | PROJECT: L03                    | REV: 00001 | JOTOOLE |  |
| <b>COMMUNICATIONS, INC.</b>      |  | TITLE: Digital I/O Pad Buffer   |            |         |  |
|                                  |  |                                 |            |         |  |
|                                  |  | HAZID: 103:eva/paddig           | REV: B15   | SIZE: A |  |
| <b>INTEGRATED CIRCUIT DESIGN</b> |  | DATE: Jul 23 15:14:52 1996      |            | SHEET:  |  |
|                                  |  | <b>CONFIDENTIAL INFORMATION</b> |            |         |  |

Fig. 12 AA-AB

60020 " 23022300



|                           |  |                                 |                   |
|---------------------------|--|---------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                    | DESIGNER: JOTOOLE |
| COMMUNICATIONS, INC.      |  | TITLE: Digital Input Pad Buffer |                   |
| INTEGRATED CIRCUIT DESIGN |  | NAME: 103reva/paddigin          | REV: B1           |
| CONFIDENTIAL INFORMATION  |  | DATE: Apr 11 11:10:35 1995      | SIZE: A           |

FIG. 13



[illegible]

|                           |  |                              |  |                   |  |
|---------------------------|--|------------------------------|--|-------------------|--|
| MICRON                    |  | PROJECT: L03                 |  | DESIGNER: Rotzoll |  |
| COMMUNICATIONS, INC.      |  | TITLE: Analog I/O Pad Buffer |  |                   |  |
|                           |  |                              |  |                   |  |
|                           |  |                              |  |                   |  |
|                           |  | NAME: 103reva/padalg         |  | REV: -            |  |
|                           |  | DATE: Dec 12 21:55:41 1993   |  | SHEET: A          |  |
| INTEGRATED CIRCUIT DESIGN |  |                              |  |                   |  |
| CONFIDENTIAL INFORMATION  |  |                              |  |                   |  |

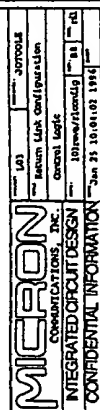
F16.19

[illegible]

|      |      |      |      |  |
|------|------|------|------|--|
| 15AA | 15AB | 15AC | 15AD |  |
|      | 15BA | 15BB | 15BC |  |

51 6111

51915



act; acted backscatter spread option in place of AM PDK

|      |      |      |      |      |      |      |      |
|------|------|------|------|------|------|------|------|
| 16AA | 16AB | 16AC | 16AD | 16AE | 16AF | 16AG | 16AH |
| 16BA | 16BB | 16BC | 16BD | 16BE | 16BF | 16BG | 16BH |
| 16CA | 16CB | 16CC | 16CD | 16CE | 16CF | 16CG | 16CH |
| 16DA | 16DB | 16DC | 16DD | 16DE | 16DF | 16DG | 16DH |
| 16EA | 16EB | 16EC | 16ED | 16EE | 16EF | 16EG | 16EH |

16 16 16 16 16 16 16 16

Fig. 16





|         |         |         |         |         |         |         |         |
|---------|---------|---------|---------|---------|---------|---------|---------|
| 16.01AA | 16.01AB | 16.01AC | 16.01AD | 16.01AE | 16.01AF | 16.01AG |         |
| 16.01BA | 16.01BB | 16.01BC | 16.01BD | 16.01BE | 16.01BF | 16.01BG | 16.01BH |
| 16.01CA | 16.01CB | 16.01CC | 16.01CD | 16.01CE | 16.01CF | 16.01CG | 16.01CH |
| 16.01DA | 16.01DB | 16.01DC | 16.01DD | 16.01DE | 16.01DF | 16.01DG | 16.01DH |
|         |         |         |         |         |         |         | 16.01DI |
|         |         |         |         |         |         |         | 16.01CI |
|         |         |         |         |         |         |         | 16.01DI |

II II II II II II

|                              |  |    |  |         |  |
|------------------------------|--|----|--|---------|--|
| <b>MICRON</b>                |  |    |  |         |  |
| CONSULTATIONS, INC.          |  |    |  |         |  |
| Av-716B / Ph-87 / Vcs=V00-1V |  |    |  |         |  |
| Operational Amplifier        |  |    |  |         |  |
| Lot #                        |  |    |  | Reorder |  |
| INTEGRATED CIRCUIT DESIGN    |  |    |  |         |  |
| 101 rees/loop                |  | B1 |  | d1      |  |
| CONFIDENTIAL INFORMATION     |  |    |  |         |  |
| Feb 10 11:51:03 1985         |  |    |  |         |  |

Fig. 16.01

TABLE "E" 302280

|      |      |
|------|------|
| 17AB | 17BB |
| 17AA | 17BA |

II II II

|                           |  |              |  |                       |  |
|---------------------------|--|--------------|--|-----------------------|--|
| MICRON                    |  | PART NO. 103 |  | PART NO. 103          |  |
| COMMUNICATIONS, INC.      |  | TYPE         |  | Magnetic Field Sensor |  |
| INTEGRATED CIRCUIT DESIGN |  | RANGE        |  | 101 revs / mag        |  |
| CONFIDENTIAL INFORMATION  |  | SIZE         |  | 88 mil                |  |
|                           |  | DATE         |  | Jan 9 08:58:38 1996   |  |

**B8: fixed control logic**

|      |      |
|------|------|
| 18AA | 18AB |
|------|------|

18AA

$$\frac{\pi}{2} \quad \frac{\pi}{2}$$

FIG. 18

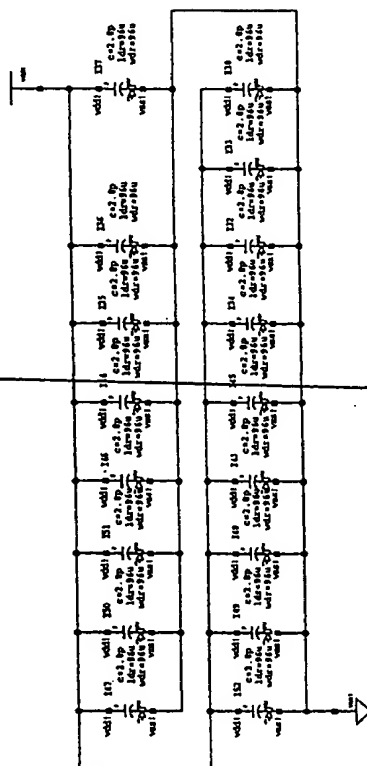
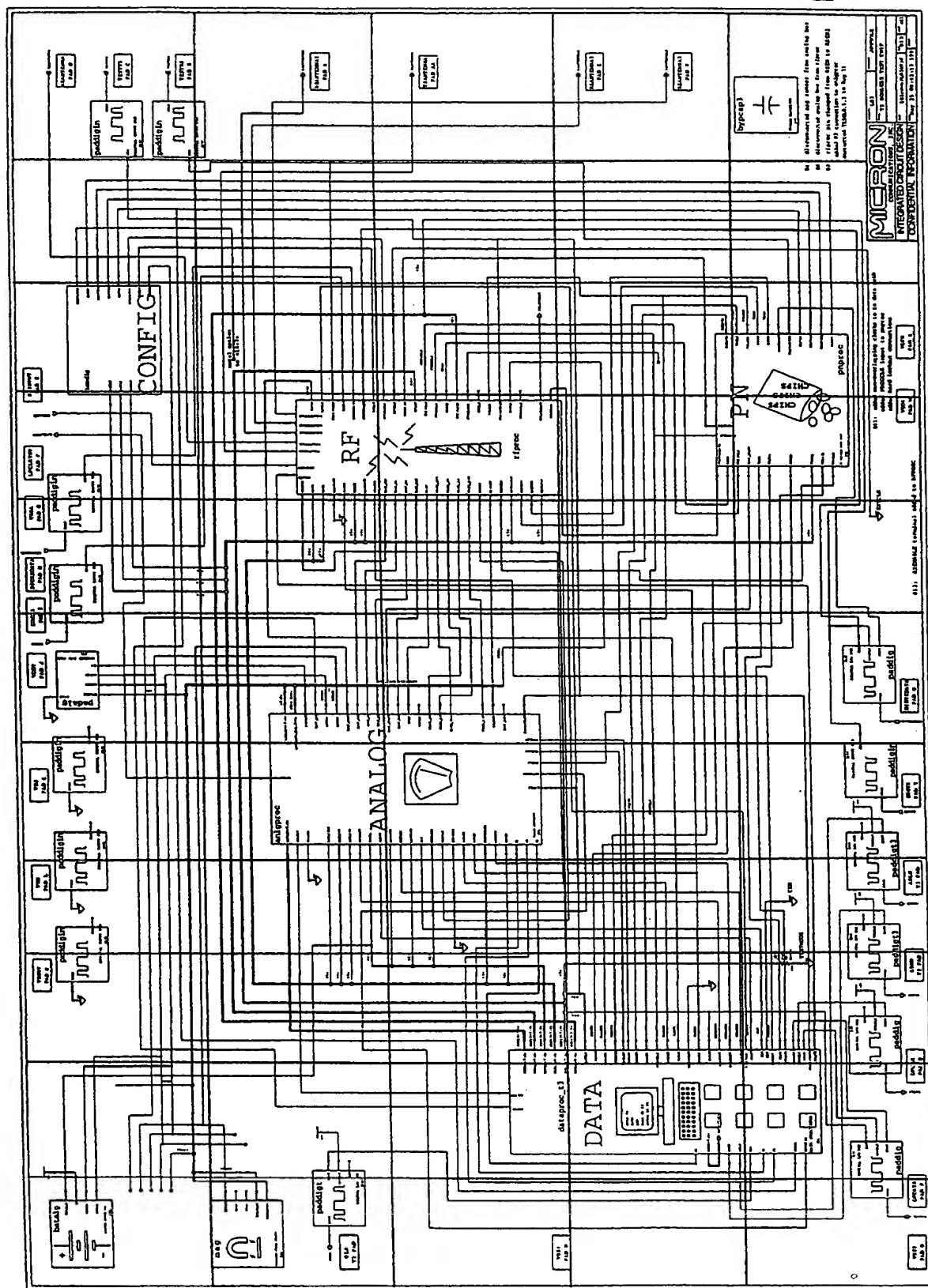


FIG. 18

|                           |  |                              |                    |
|---------------------------|--|------------------------------|--------------------|
| MICRON                    |  | PROJECT: 103                 | REVISION: J07000LE |
| COMMUNICATIONS, INC.      |  | Title: Chip Bypass Capacitor |                    |
| INTEGRATED CIRCUIT DESIGN |  | CT=PF                        |                    |
| CONFIDENTIAL INFORMATION  |  | 103revA/bypcap3              | B2                 |
|                           |  | DATE: Jul 28 17:43:25 1995   | FILE:              |

B2: deleted one cap

EST





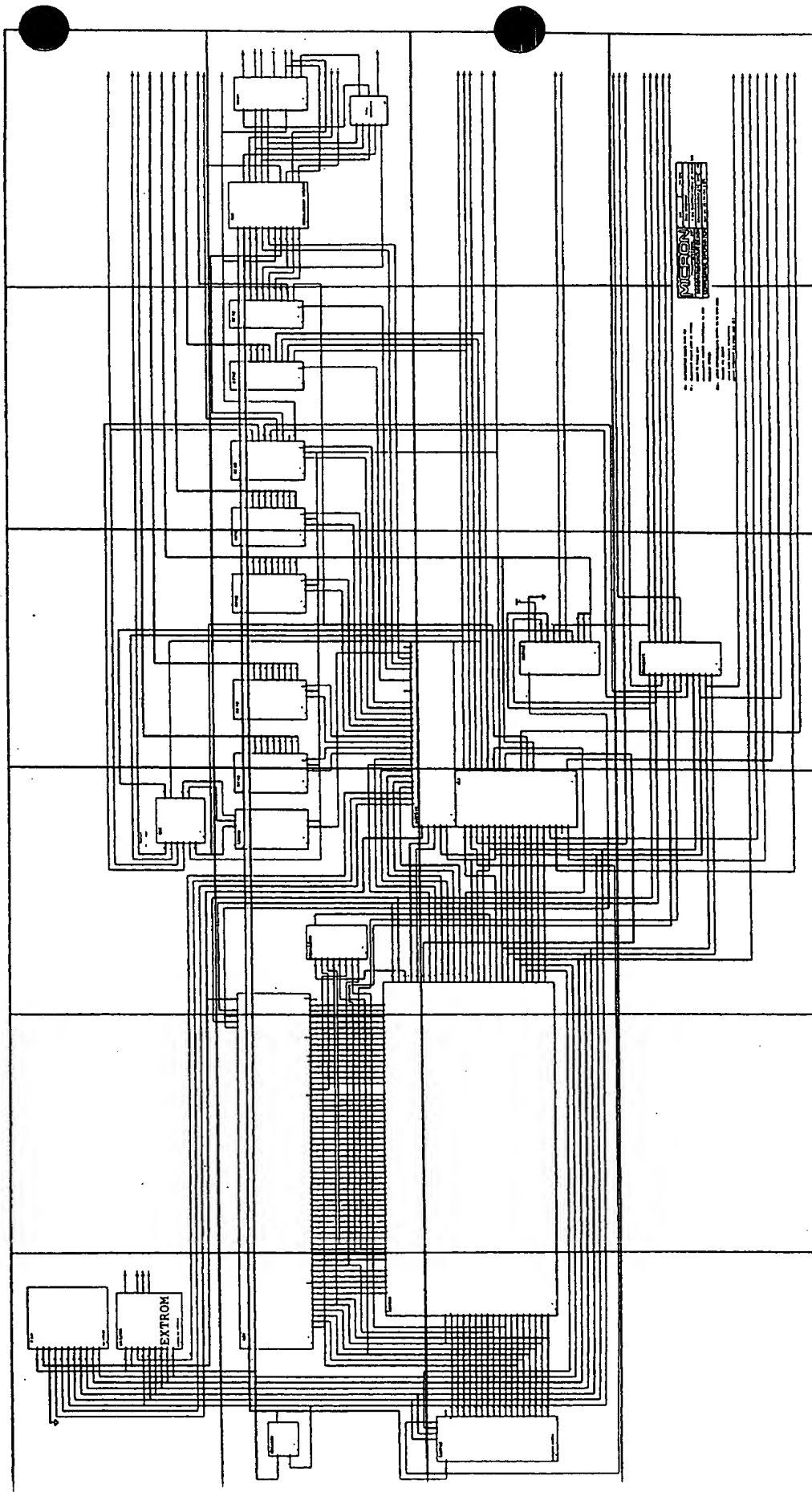
"PACED" E 302260

|      |      |      |      |      |      |
|------|------|------|------|------|------|
| 20AA | 20AB | 20AC | 20AD | 20AE | 20AF |
| 20BA | 20BB | 20BC | 20BD | 20BE | 20BF |
| 20CA | 20CB | 20CC | 20CD | 20CE | 20CF |
|      |      | 20DC | 20DD | 20DE | 20DF |

II 11 20

1994 1995 1996 1997 1998 1999 2000 2001 2002 2003 2004 2005 2006 2007 2008 2009 2010 2011 2012 2013 2014 2015 2016 2017 2018 2019 2020 2021 2022 2023 2024 2025 2026 2027 2028 2029 2030 2031 2032 2033 2034 2035 2036 2037 2038 2039 2040 2041 2042 2043 2044 2045 2046 2047 2048 2049 2050 2051 2052 2053 2054 2055 2056 2057 2058 2059 2060 2061 2062 2063 2064 2065 2066 2067 2068 2069 2070 2071 2072 2073 2074 2075 2076 2077 2078 2079 2080 2081 2082 2083 2084 2085 2086 2087 2088 2089 2090 2091 2092 2093 2094 2095 2096 2097 2098 2099 2100 2101 2102 2103 2104 2105 2106 2107 2108 2109 2110 2111 2112 2113 2114 2115 2116 2117 2118 2119 2120 2121 2122 2123 2124 2125 2126 2127 2128 2129 2130 2131 2132 2133 2134 2135 2136 2137 2138 2139 2140 2141 2142 2143 2144 2145 2146 2147 2148 2149 2150 2151 2152 2153 2154 2155 2156 2157 2158 2159 2160 2161 2162 2163 2164 2165 2166 2167 2168 2169 2170 2171 2172 2173 2174 2175 2176 2177 2178 2179 2180 2181 2182 2183 2184 2185 2186 2187 2188 2189 2190 2191 2192 2193 2194 2195 2196 2197 2198 2199 2200 2201 2202 2203 2204 2205 2206 2207 2208 2209 2210 2211 2212 2213 2214 2215 2216 2217 2218 2219 2220 2221 2222 2223 2224 2225 2226 2227 2228 2229 2230 2231 2232 2233 2234 2235 2236 2237 2238 2239 2240 2241 2242 2243 2244 2245 2246 2247 2248 2249 2250 2251 2252 2253 2254 2255 2256 2257 2258 2259 2260 2261 2262 2263 2264 2265 2266 2267 2268 2269 2270 2271 2272 2273 2274 2275 2276 2277 2278 2279 2280 2281 2282 2283 2284 2285 2286 2287 2288 2289 2290 2291 2292 2293 2294 2295 2296 2297 2298 2299 2300 2301 2302 2303 2304 2305 2306 2307 2308 2309 2310 2311 2312 2313 2314 2315 2316 2317 2318 2319 2320 2321 2322 2323 2324 2325 2326 2327 2328 2329 2330 2331 2332 2333 2334 2335 2336 2337 2338 2339 2340 2341 2342 2343 2344 2345 2346 2347 2348 2349 2350 2351 2352 2353 2354 2355 2356 2357 2358 2359 2360 2361 2362 2363 2364 2365 2366 2367 2368 2369 2370 2371 2372 2373 2374 2375 2376 2377 2378 2379 2380 2381 2382 2383 2384 2385 2386 2387 2388 2389 2390 2391 2392 2393 2394 2395 2396 2397 2398 2399 2400 2401 2402 2403 2404 2405 2406 2407 2408 2409 2410 2411 2412 2413 2414 2415 2416 2417 2418 2419 2420 2421 2422 2423 2424 2425 2426 2427 2428 2429 2430 2431 2432 2433 2434 2435 2436 2437 2438 2439 2440 2441 2442 2443 2444 2445 2446 2447 2448 2449 2450 2451 2452 2453 2454 2455 2456 2457 2458 2459 2460 2461 2462 2463 2464 2465 2466 2467 2468 2469 2470 2471 2472 2473 2474 2475 2476 2477 2478 2479 2480 2481 2482 2483 2484 2485 2486 2487 2488 2489 2490 2491 2492 2493 2494 2495 2496 2497 2498 2499 2500 2501 2502 2503 2504 2505 2506 2507 2508 2509 2510 2511 2512 2513 2514 2515 2516 2517 2518 2519 2520 2521 2522 2523 2524 2525 2526 2527 2528 2529 2530 2531 2532 2533 2534 2535 2536 2537 2538 2539 2540 2541 2542 2543 2544 2545 2546 2547 2548 2549 2550 2551 2552 2553 2554 2555 2556 2557 2558 2559 2560 2561 2562 2563 2564 2565 2566 2567 2568 2569 2570 2571 2572 2573 2574 2575 2576 2577 2578 2579 2580 2581 2582 2583 2584 2585 2586 2587 2588 2589 2590 2591 2592 2593 2594 2595 2596 2597 2598 2599 2600 2601 2602 2603 2604 2605 2606 2607 2608 2609 2610 2611 2612 2613 2614 2615 2616 2617 2618 2619 2620 2621 2622 2623 2624 2625 2626 2627 2628 2629 2630 2631 2632 2633 2634 2635 2636 2637 2638 2639 2640 2641 2642 2643 2644 2645 2646 2647 2648 2649 2650 2651 2652 2653 2654 2655 2656 2657 2658 2659 2660 2661 2662 2663 2664 2665 2666 2667 2668 2669 2670 2671 2672 2673 2674 2675 2676 2677 2678 2679 2680 2681 2682 2683 2684 2685 2686 2687 2688 2689 2690 2691 2692 2693 2694 2695 2696 2697 2698 2699 2700 2701 2702 2703 2704 2705 2706 2707 2708 2709 2710 2711 2712 2713 2714 2715 2716 2717 2718 2719 2720 2721 2722 2723 2724 2725 2726 2727 2728 2729 2730 2731 2732 2733 2734 2735 2736 2737 2738 2739 2740 2741 2742 2743 2744 2745 2746 2747 2748 2749 2750 2751 2752 2753 2754 2755 2756 2757 2758 2759 2760 2761 2762 2763 2764 2765 2766 2767 2768 2769 2770 2771 2772 2773 2774 2775 2776 2777 2778 2779 2780 2781 2782 2783 2784 2785 2786 2787 2788 2789 2790 2791 2792 2793 2794 2795 2796 2797 2798 2799 2800 2801 2802 2803 2804 2805 2806 2807 2808 2809 2810 2811 2

Fig. 20

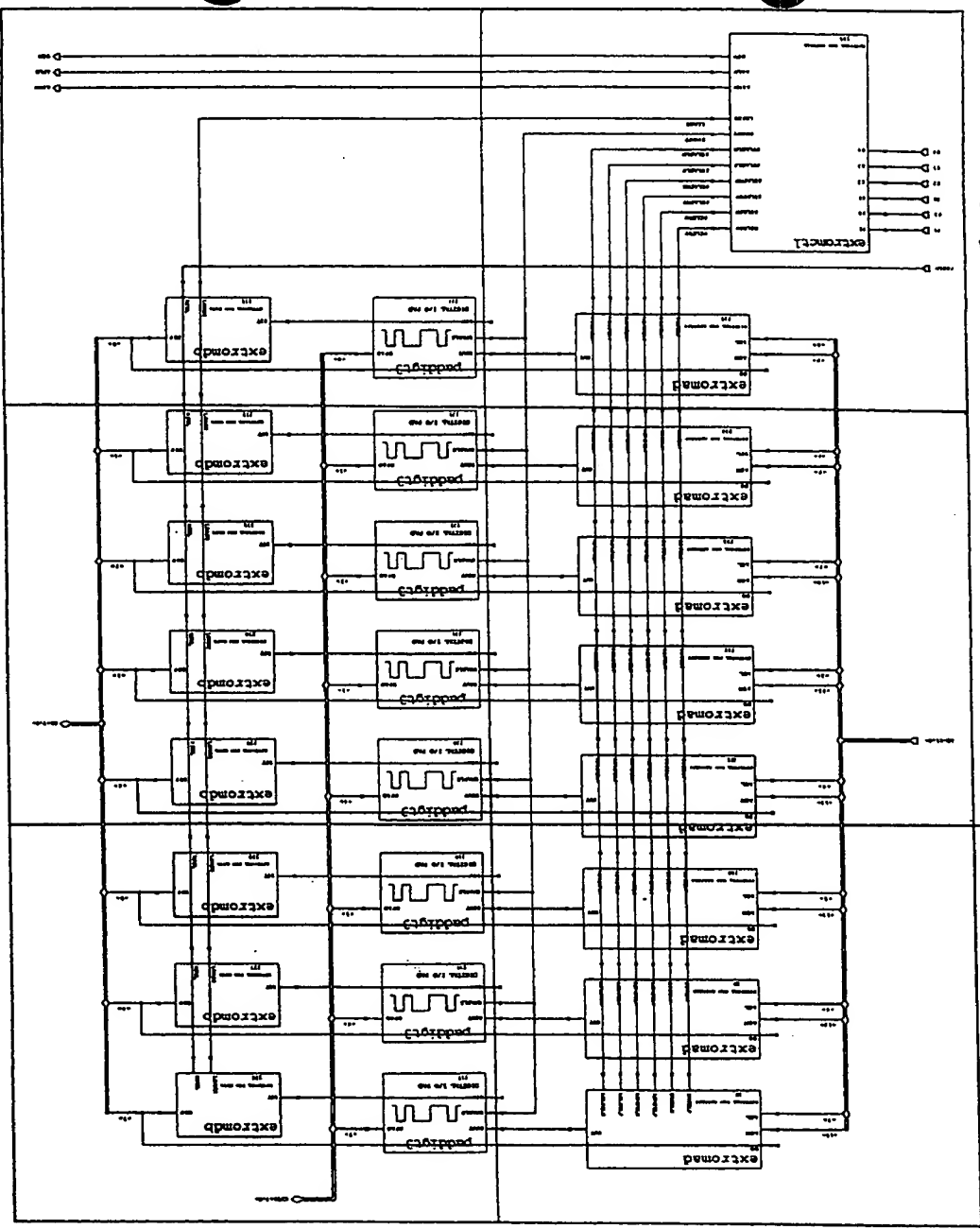


|         |         |
|---------|---------|
| 20.01AA | 20.01AB |
| 20.01BA | 20.01BB |
| 20.01CA | 20.01CB |

Fig 20.01

20.01AA 20.01AB 20.01BA 20.01BB 20.01CA 20.01CB

Fig. 20.01



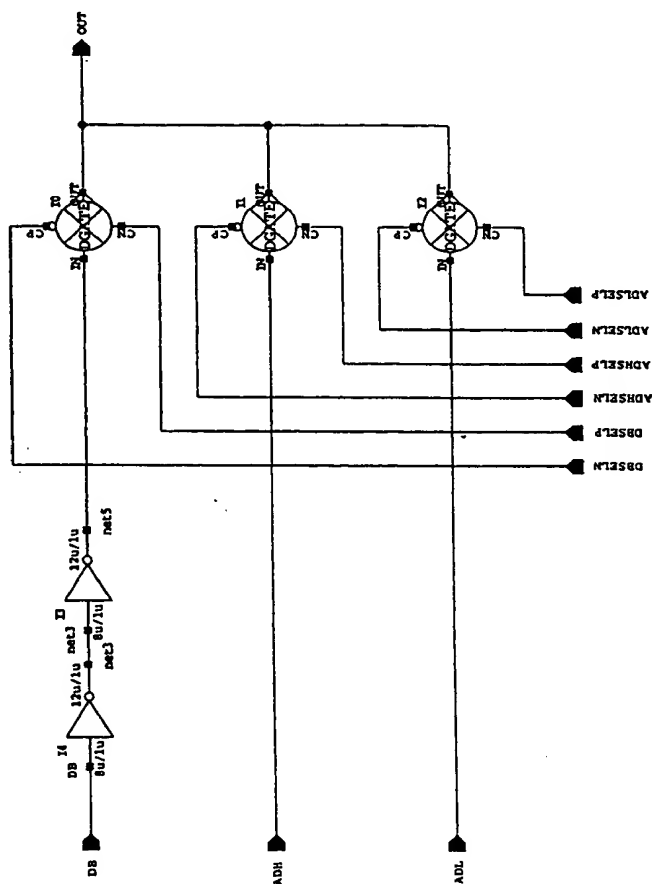
This document contains confidential information and is not to be distributed outside the company.

Page 6 of 6

|           |           |
|-----------|-----------|
| 20.0101AA | 20.0101AB |
| 20.0101BA | 20.0101BB |

Page 6 of 6



[illegible]

|                           |  |                                       |                   |
|---------------------------|--|---------------------------------------|-------------------|
| MICRON                    |  | PROJECT: L03                          | REVISION: Rotzoll |
| COMMUNICATIONS, INC.      |  | TITLE: External ROM Address Interface |                   |
| INTEGRATED CIRCUIT DESIGN |  |                                       |                   |
| CONFIDENTIAL INFORMATION  |  | NAME: 103reva/extromad                | REV: -            |
|                           |  | SIZE: A                               |                   |
|                           |  | DATE: Dec 11 01:09:14 1993            |                   |
|                           |  | SHEET:                                |                   |

FIG. 20,0102

20.0103AA 20.0103AB 20.0103AC

|           |           |           |
|-----------|-----------|-----------|
| 20.0103AA | 20.0103AB | 20.0103AC |
|-----------|-----------|-----------|

20.0103



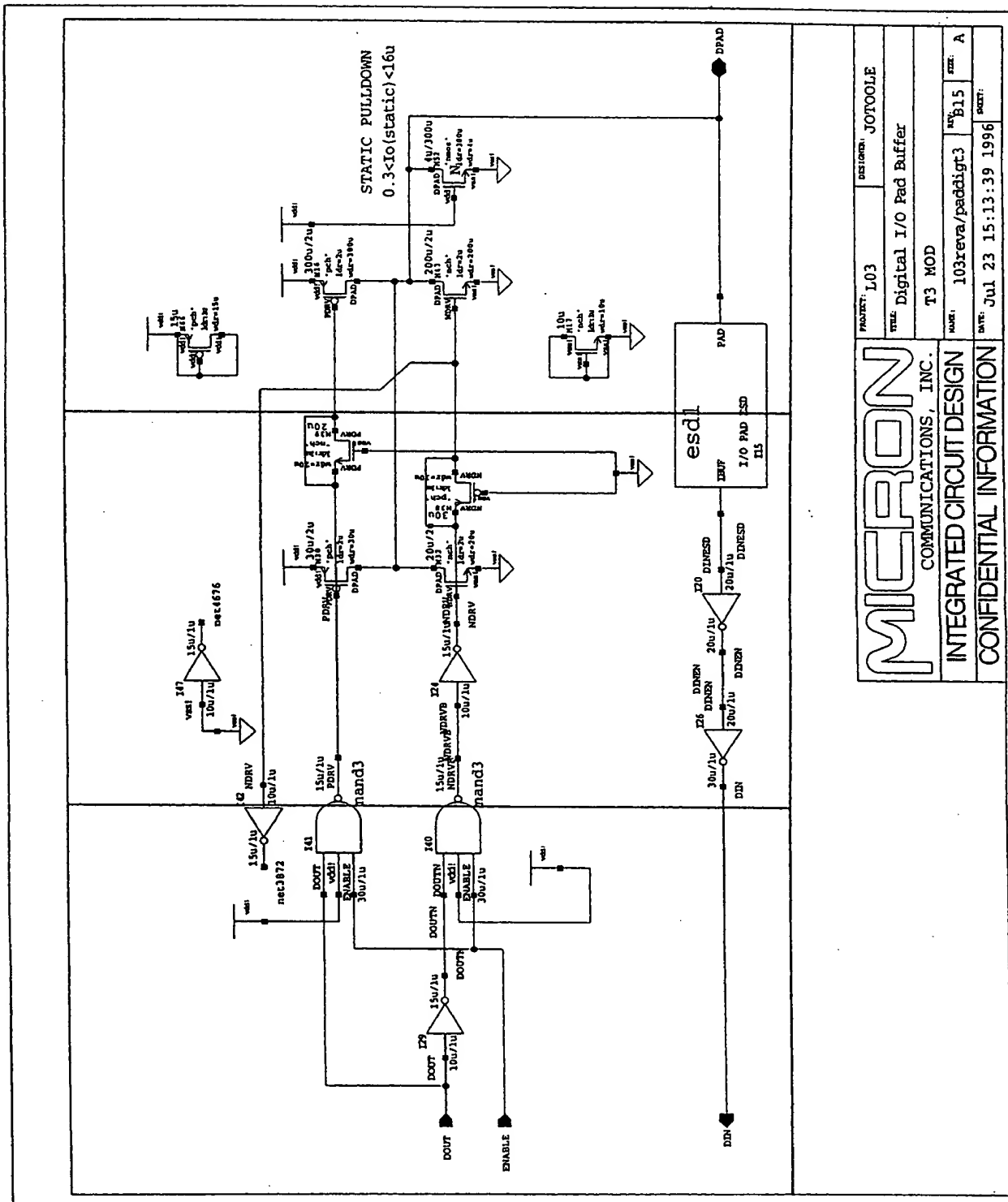
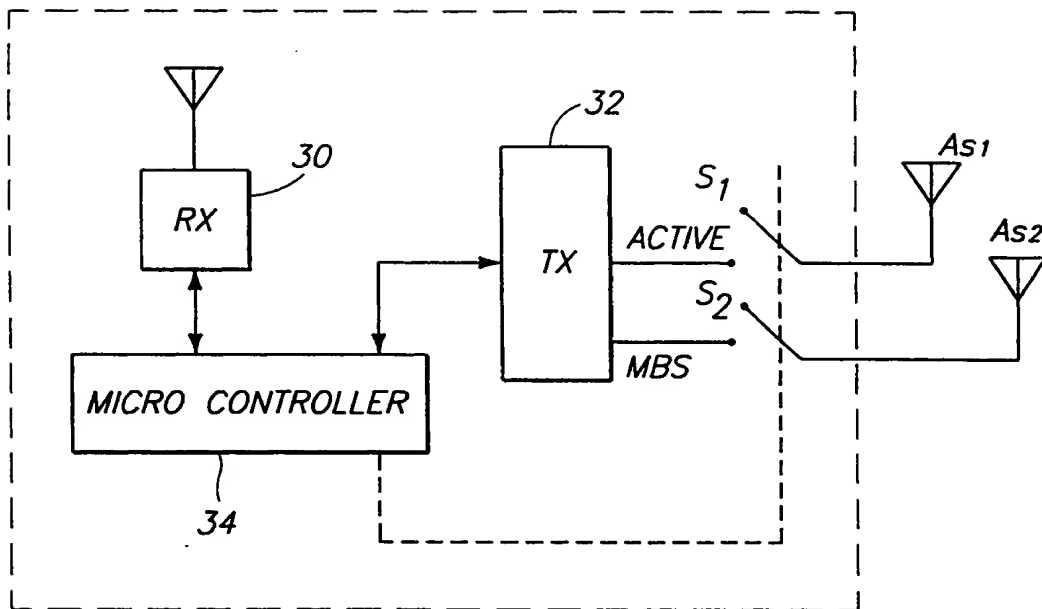
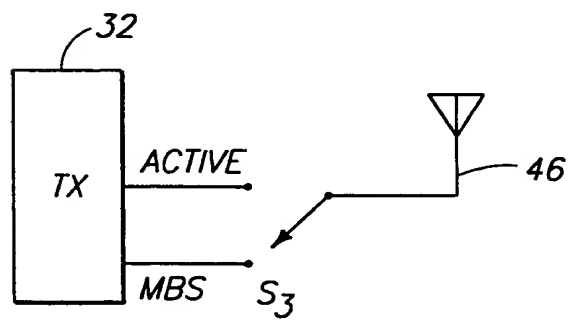
[illegible]

Fig 20.0103M-AC

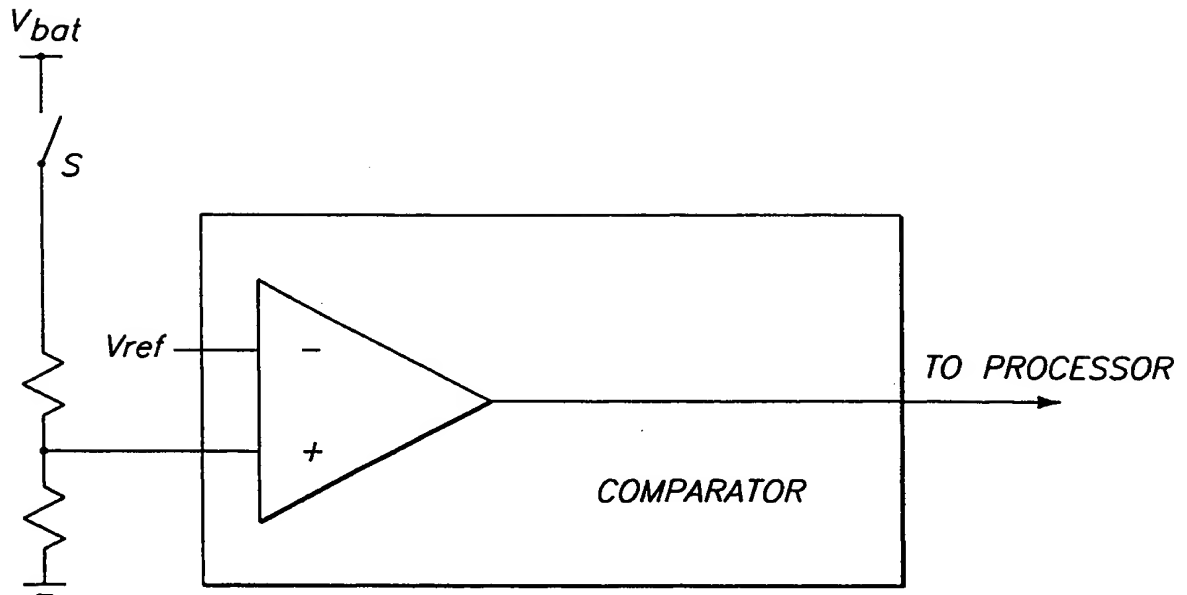




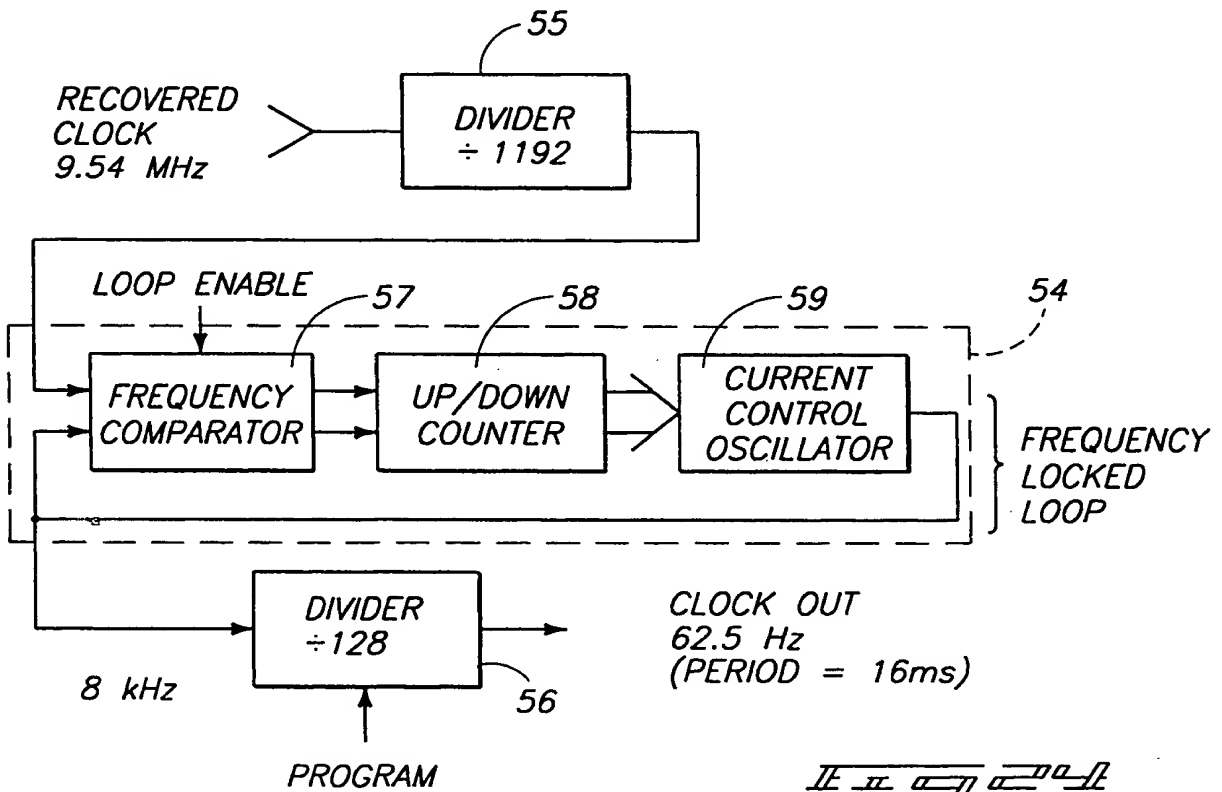
II II II II II

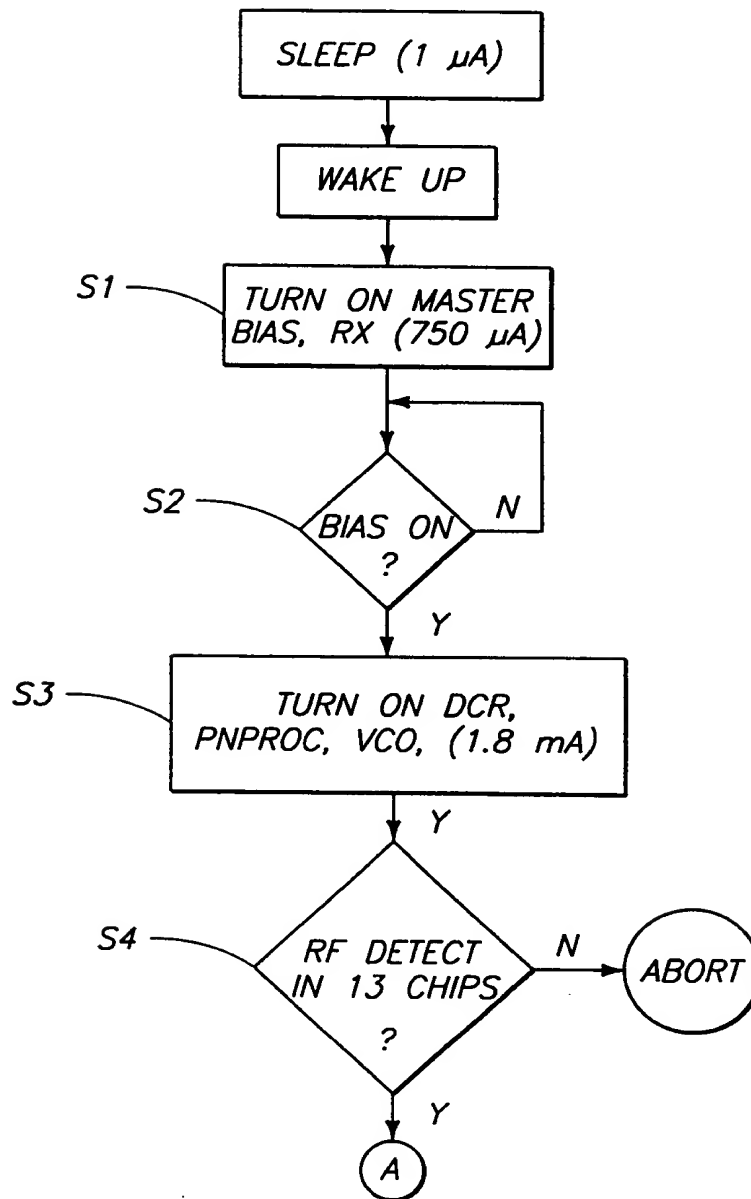


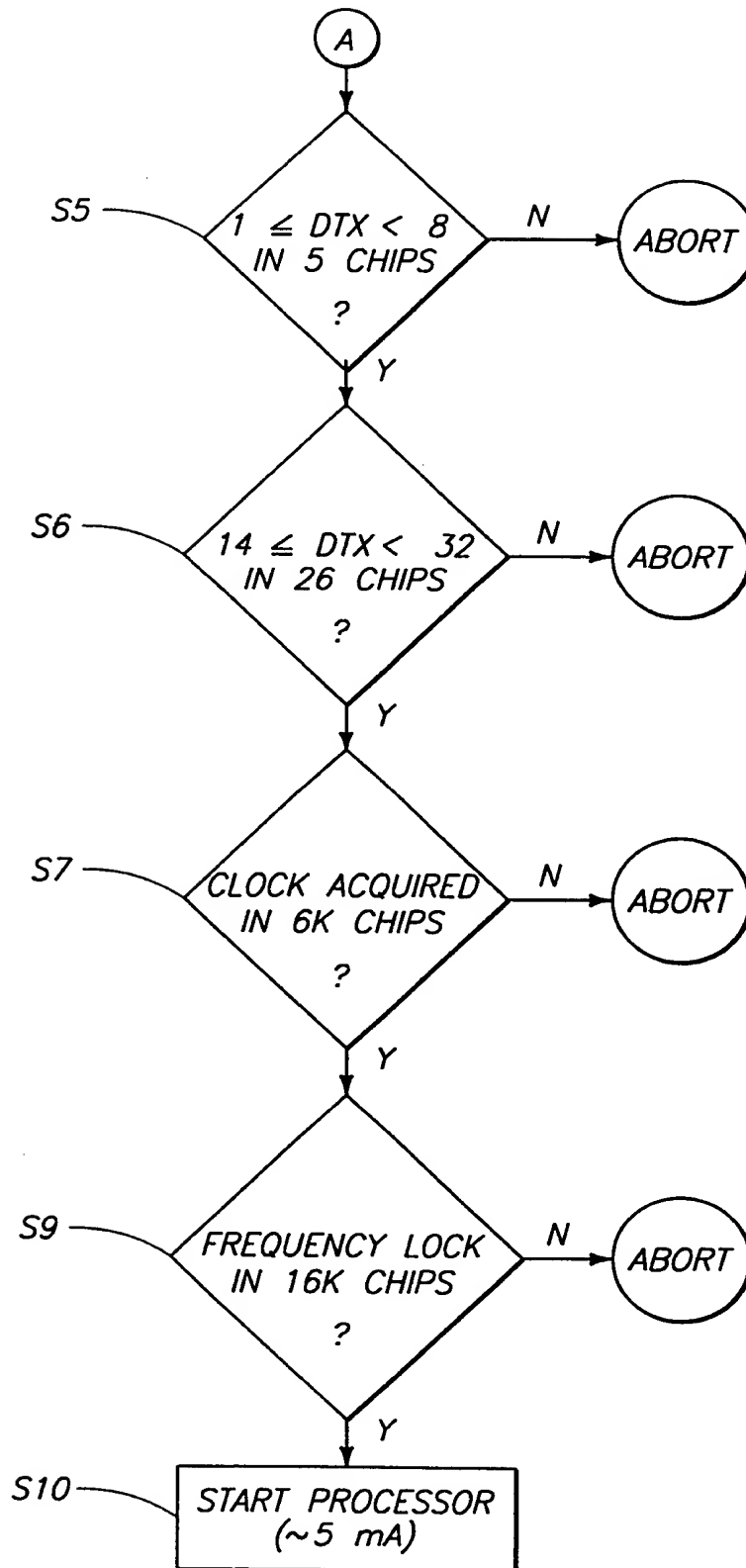
II II II II II



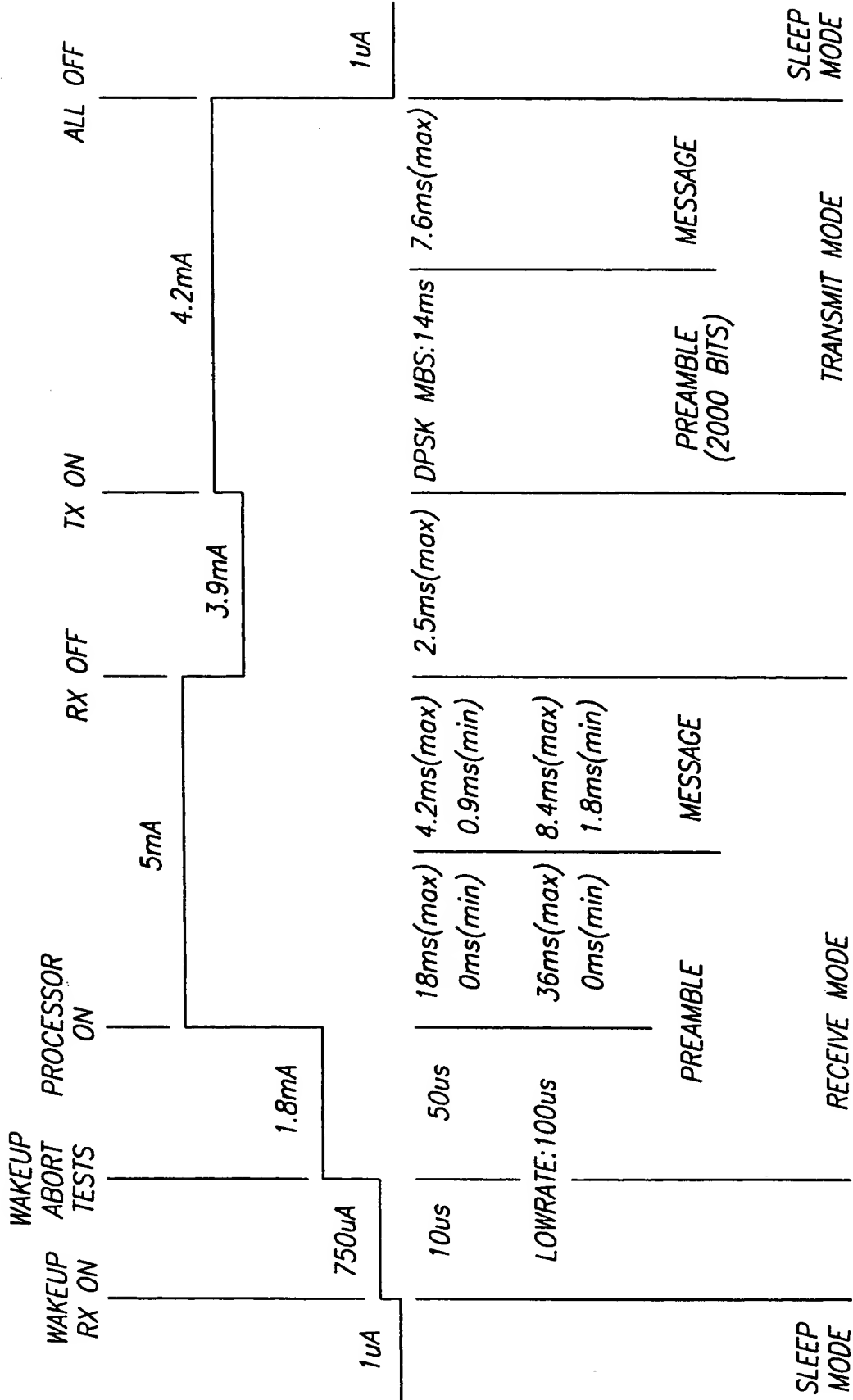
$V_{ref} = \text{bandgap voltage} \approx 1.2 \text{ V for silicon}$



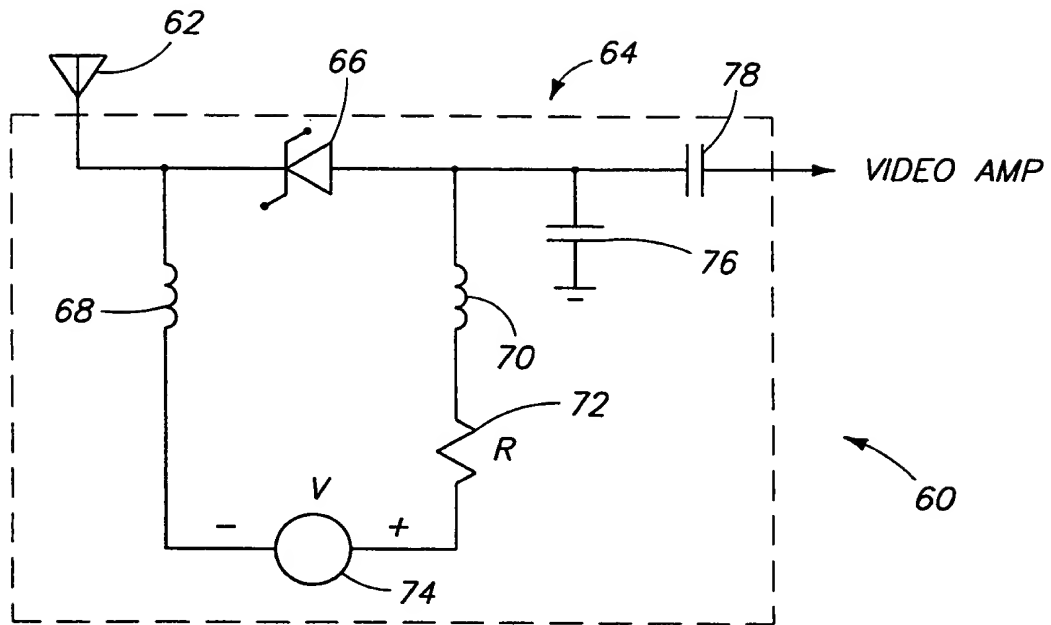
WAKEUP SEQUENCE



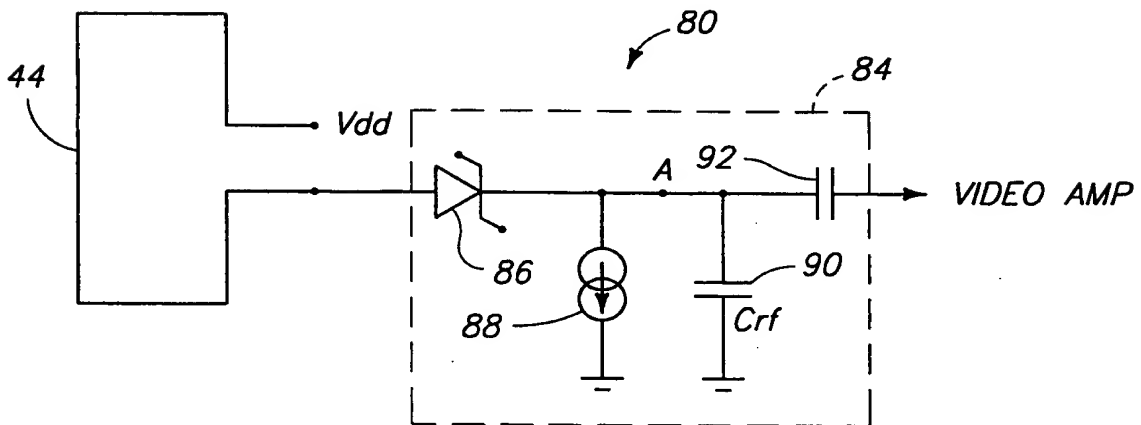
POWER SUPPLY



11 11 11 11



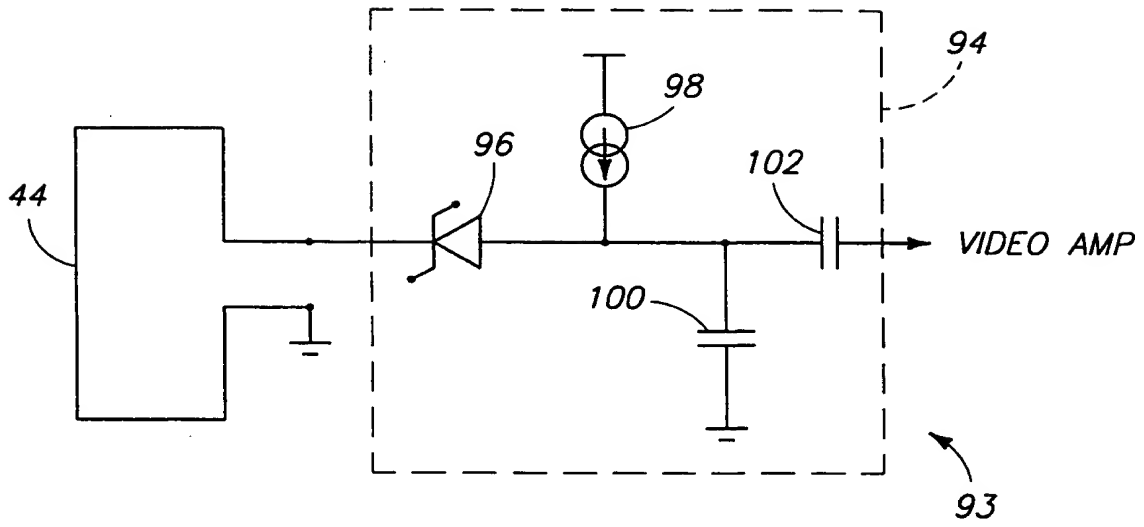
IEEE 288



IEEE 29

FOUO "E902860"



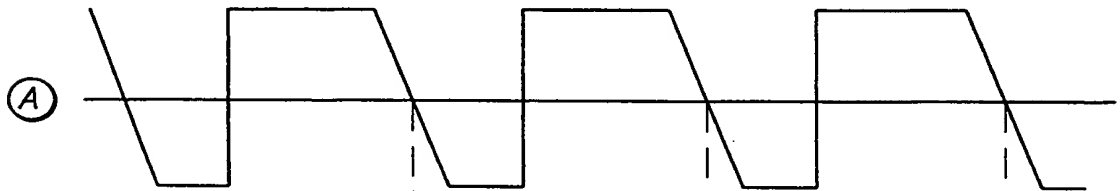


*II II II II*

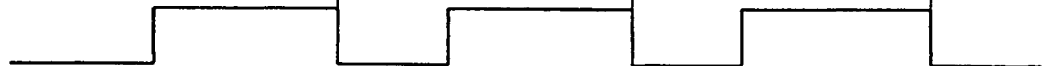
LOW POWER



HIGH POWER

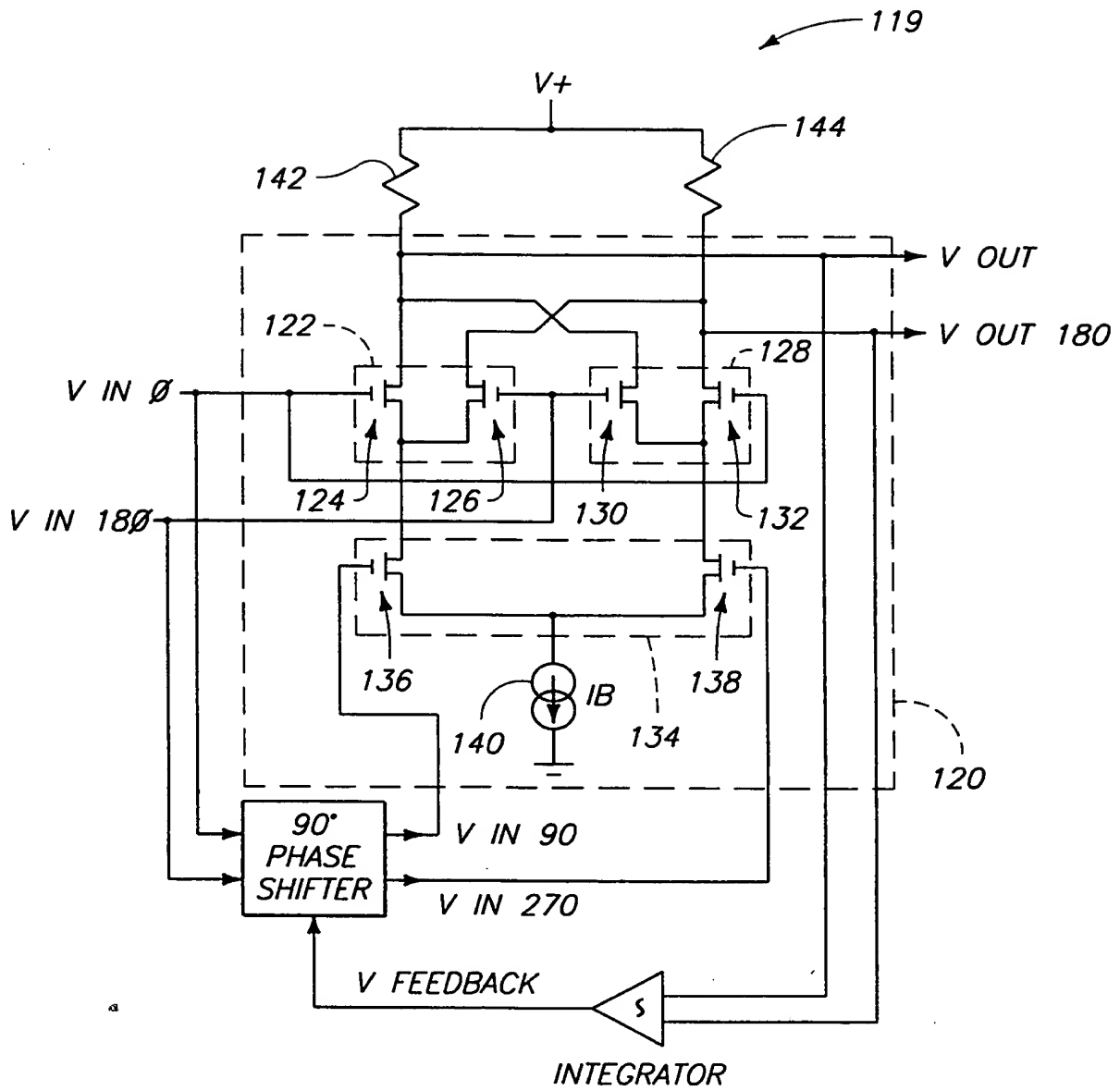


AMPLIFIED  
DIGITAL  
SIGNAL



*II II II II*





IEEE SP4

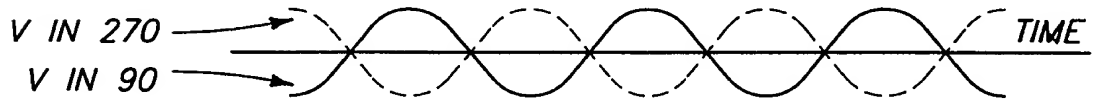
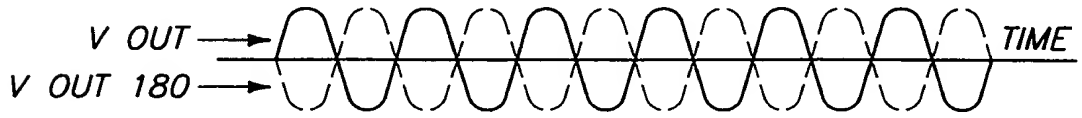
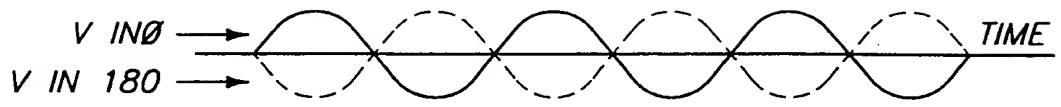


FIG. 35

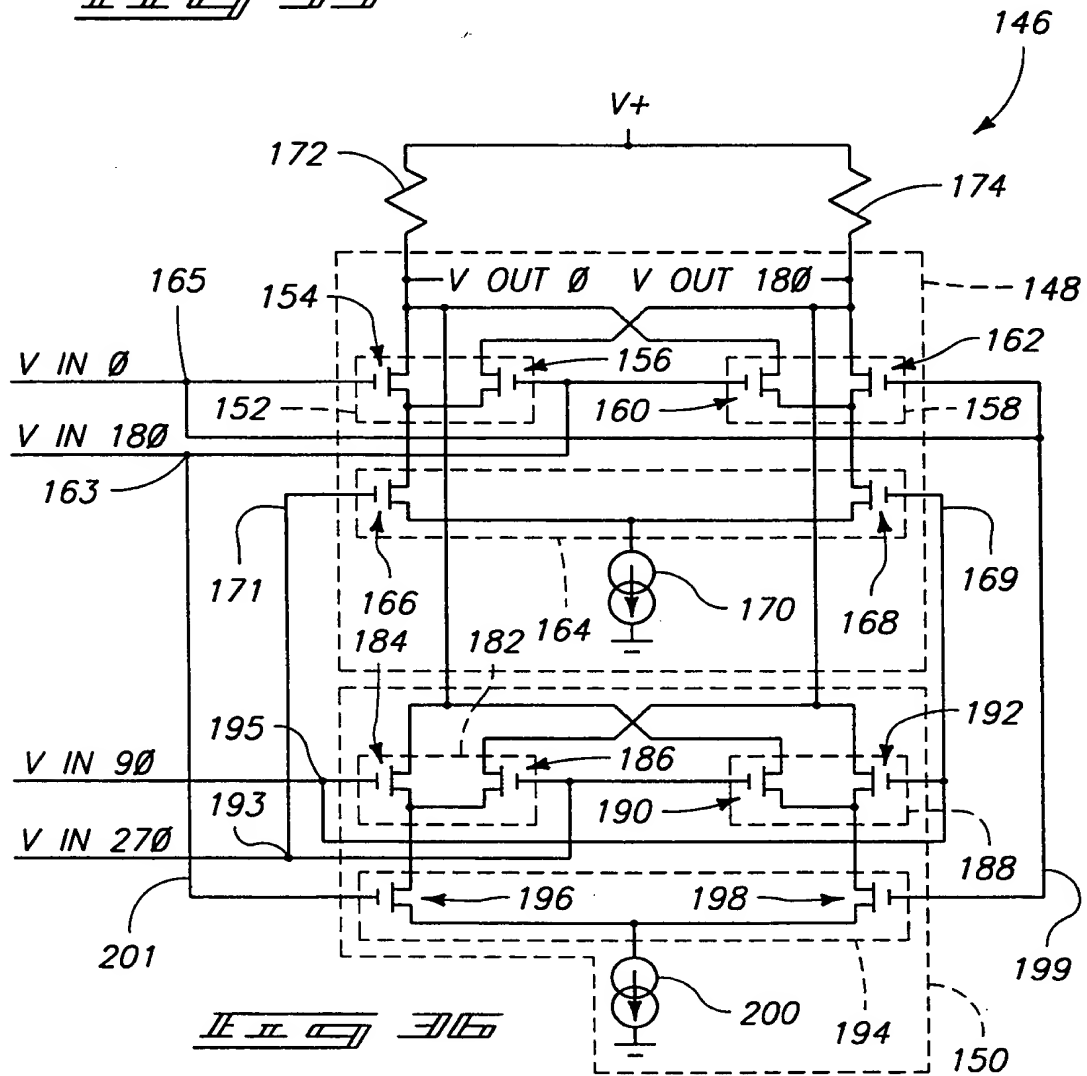
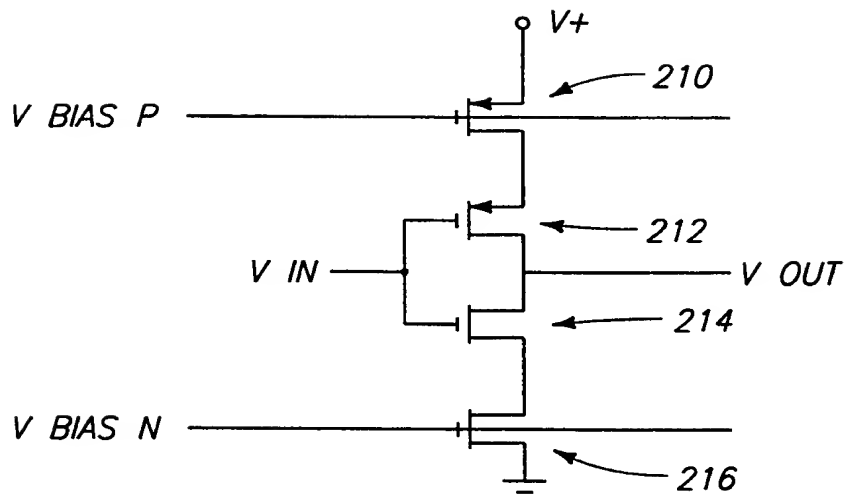
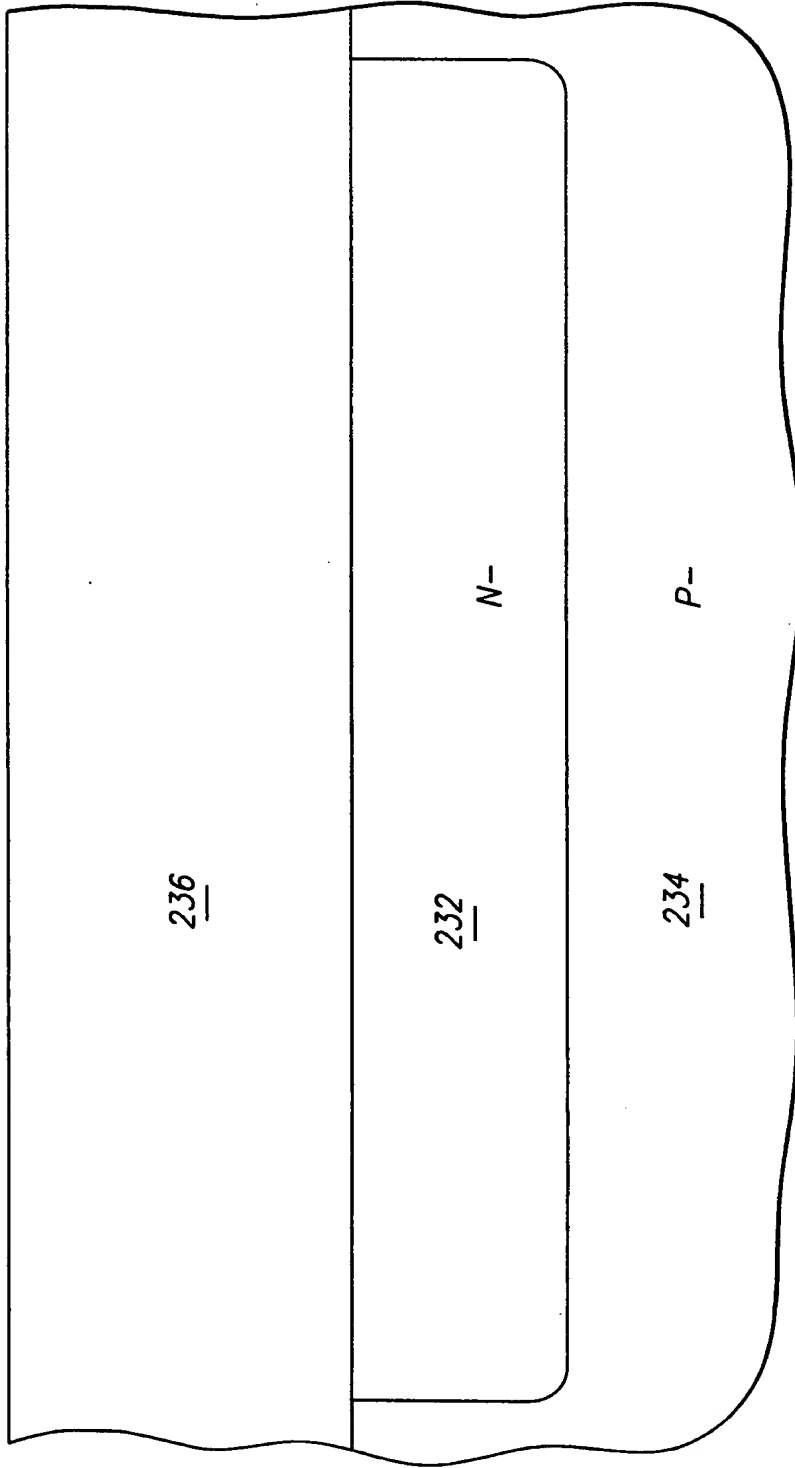


FIG. 36



II II II II

FOUO " 63022800



FE 5788

FIG. 10

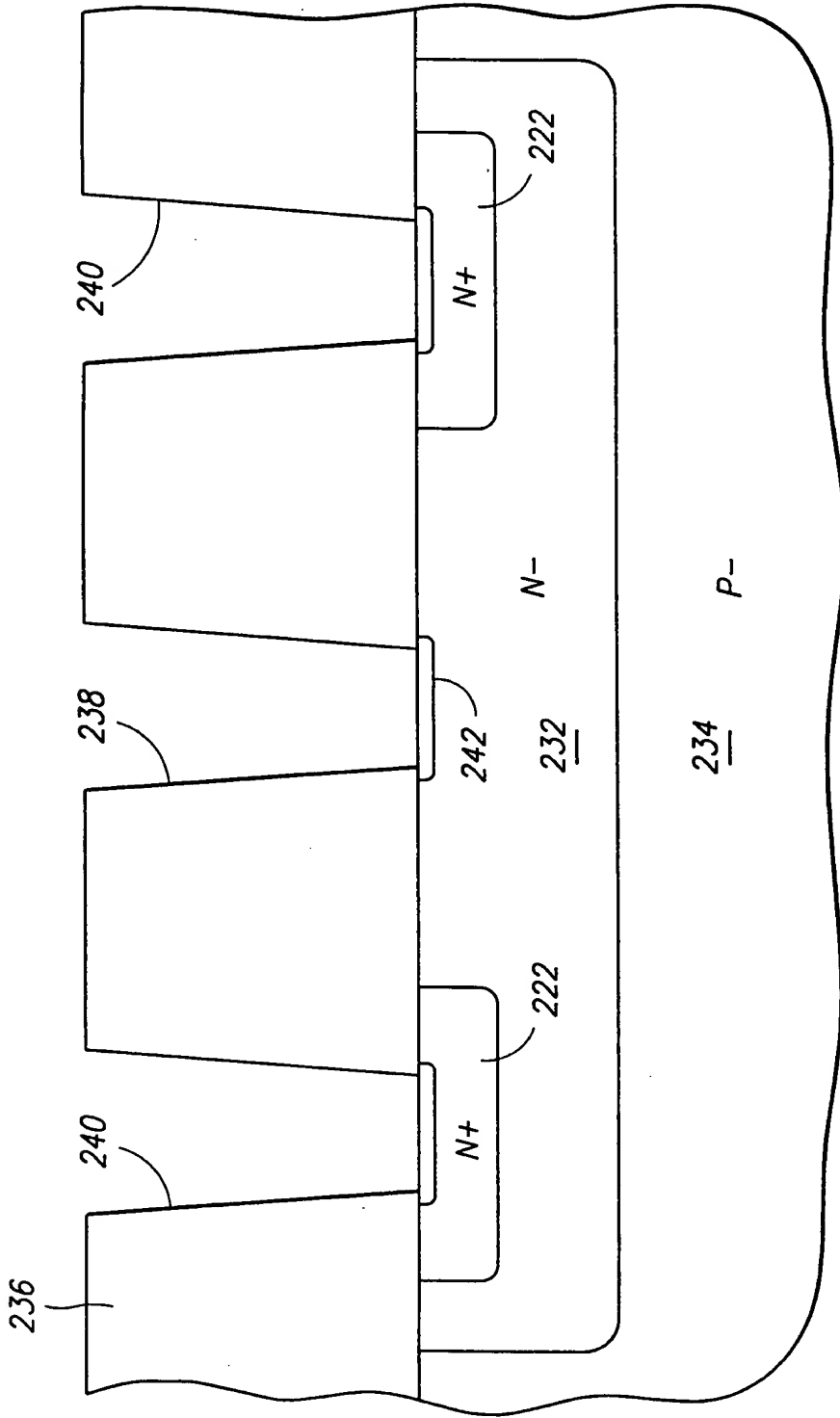


FIG. 11

FIG. 2 is a cross-sectional view of the device.

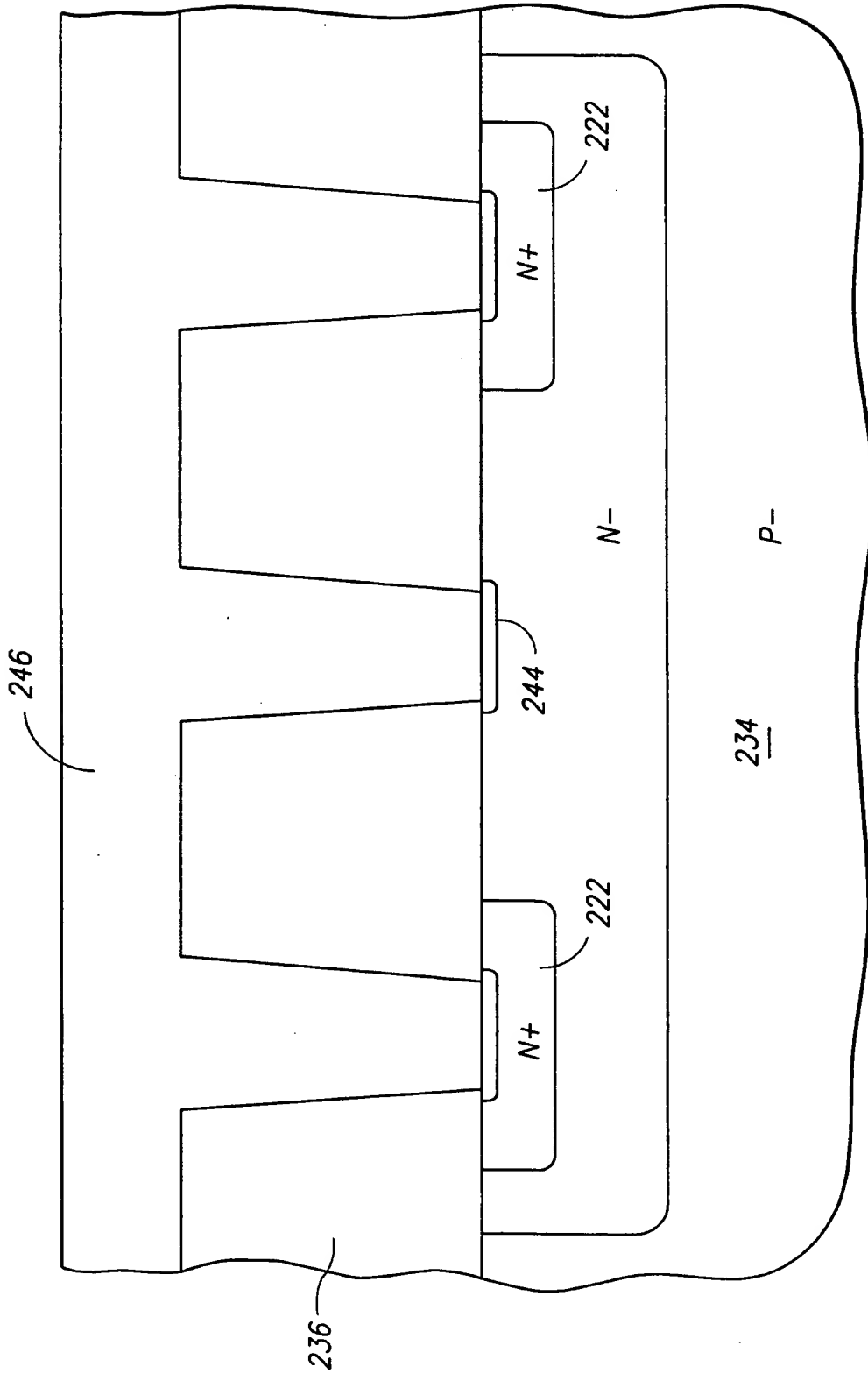


FIG. 3 is a cross-sectional view of the device.



FIG. 3

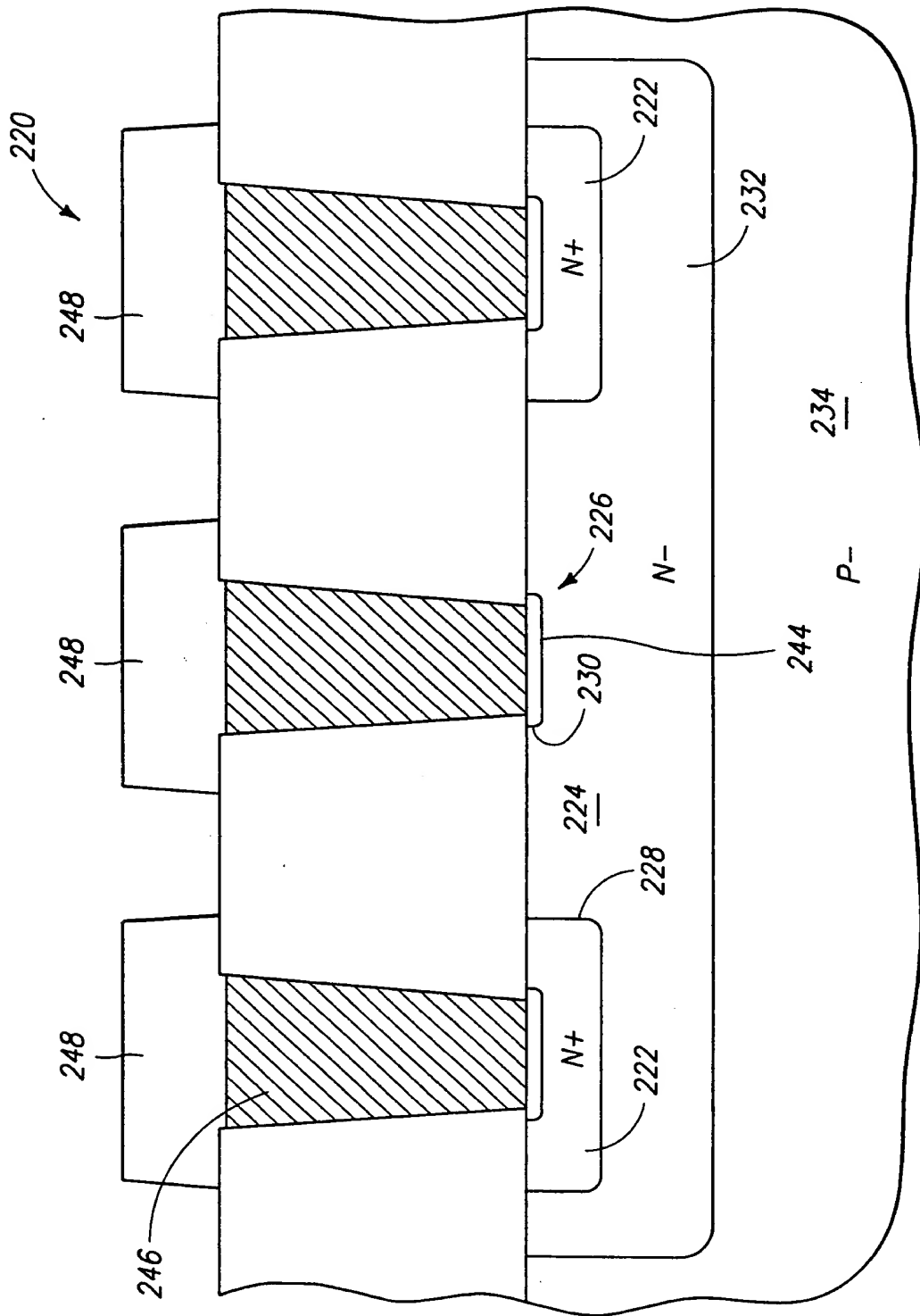


FIG. 3

FIG. 10

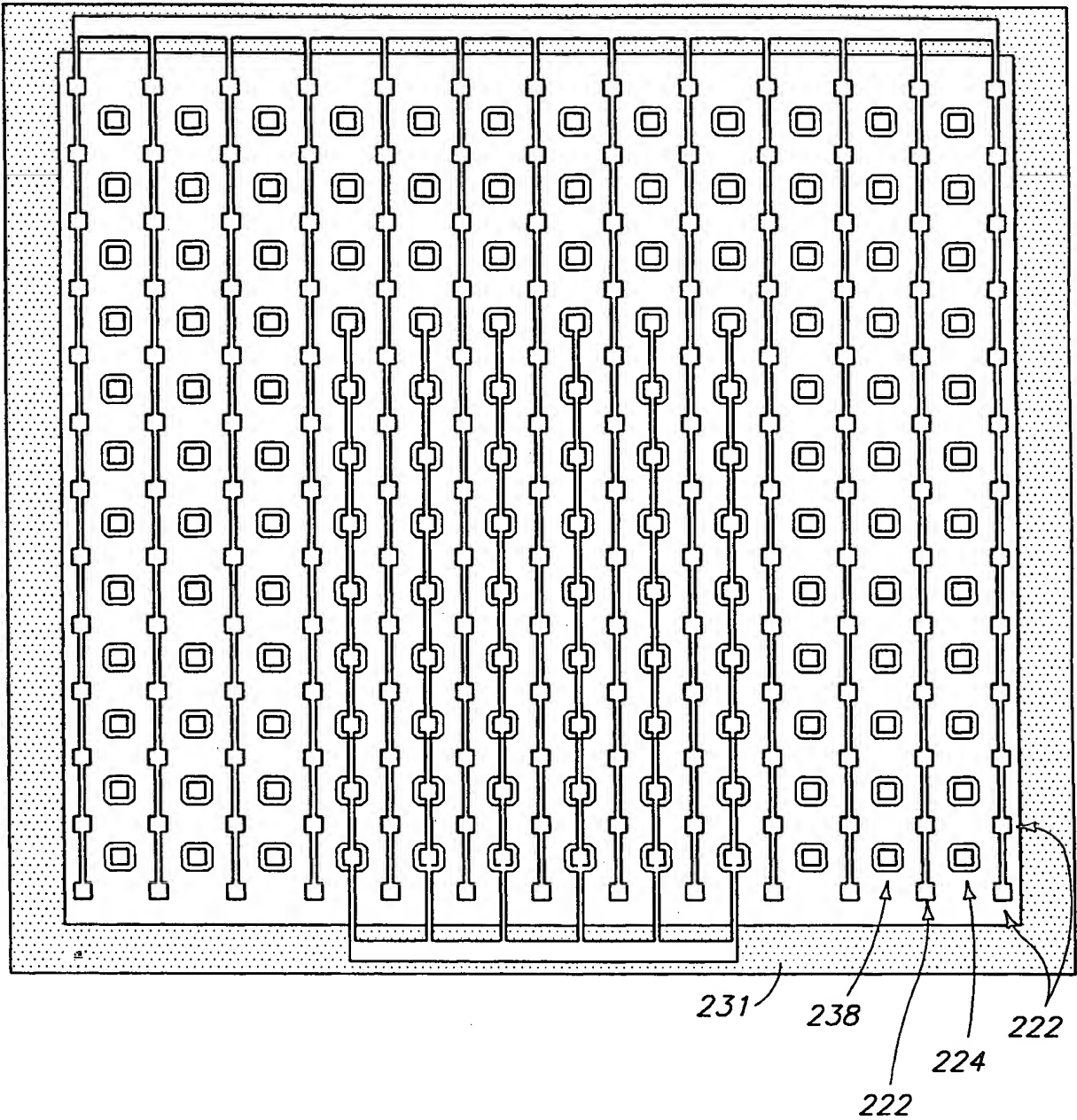
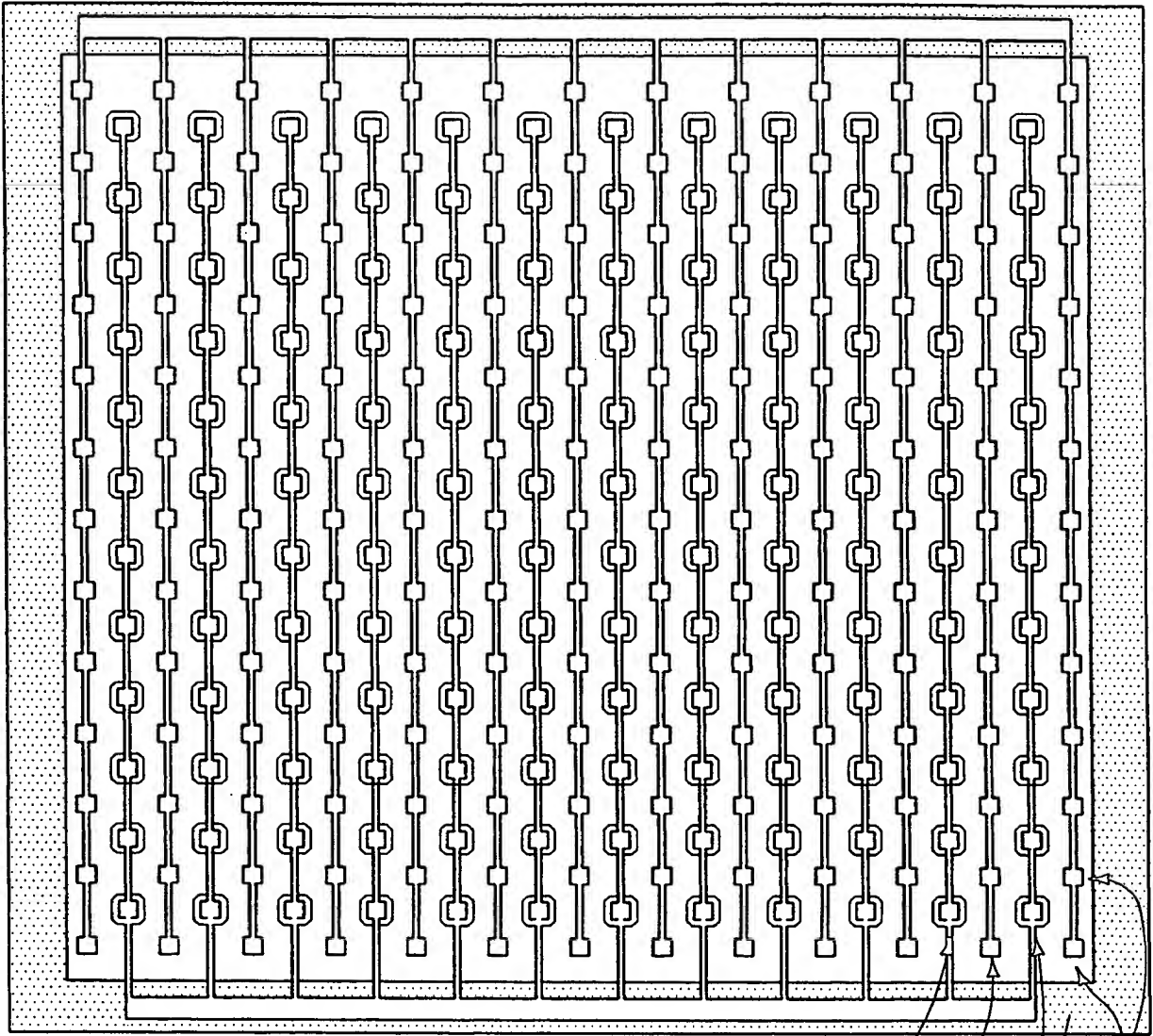


FIG. 11

FIG. 10

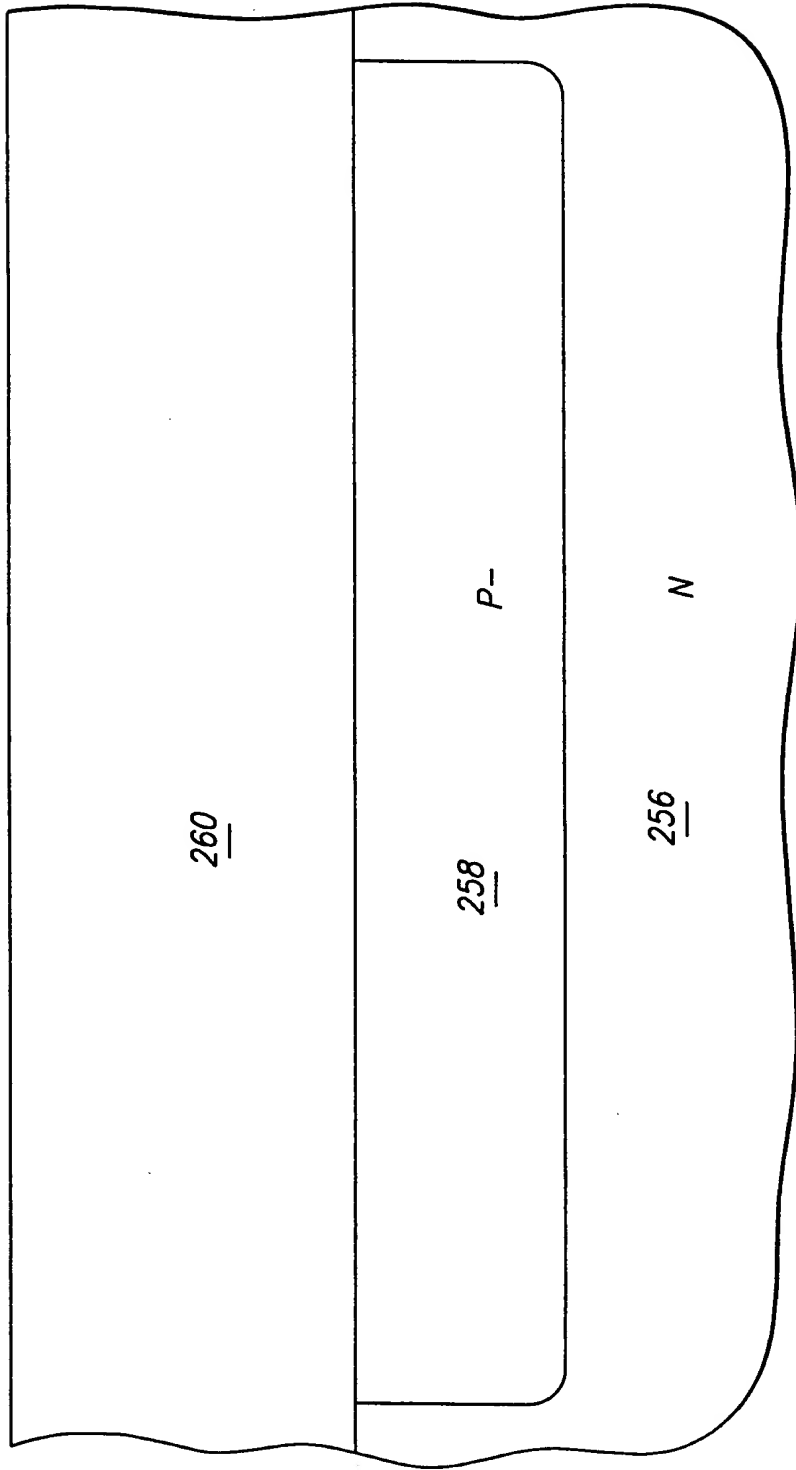


238  
224  
222  
231

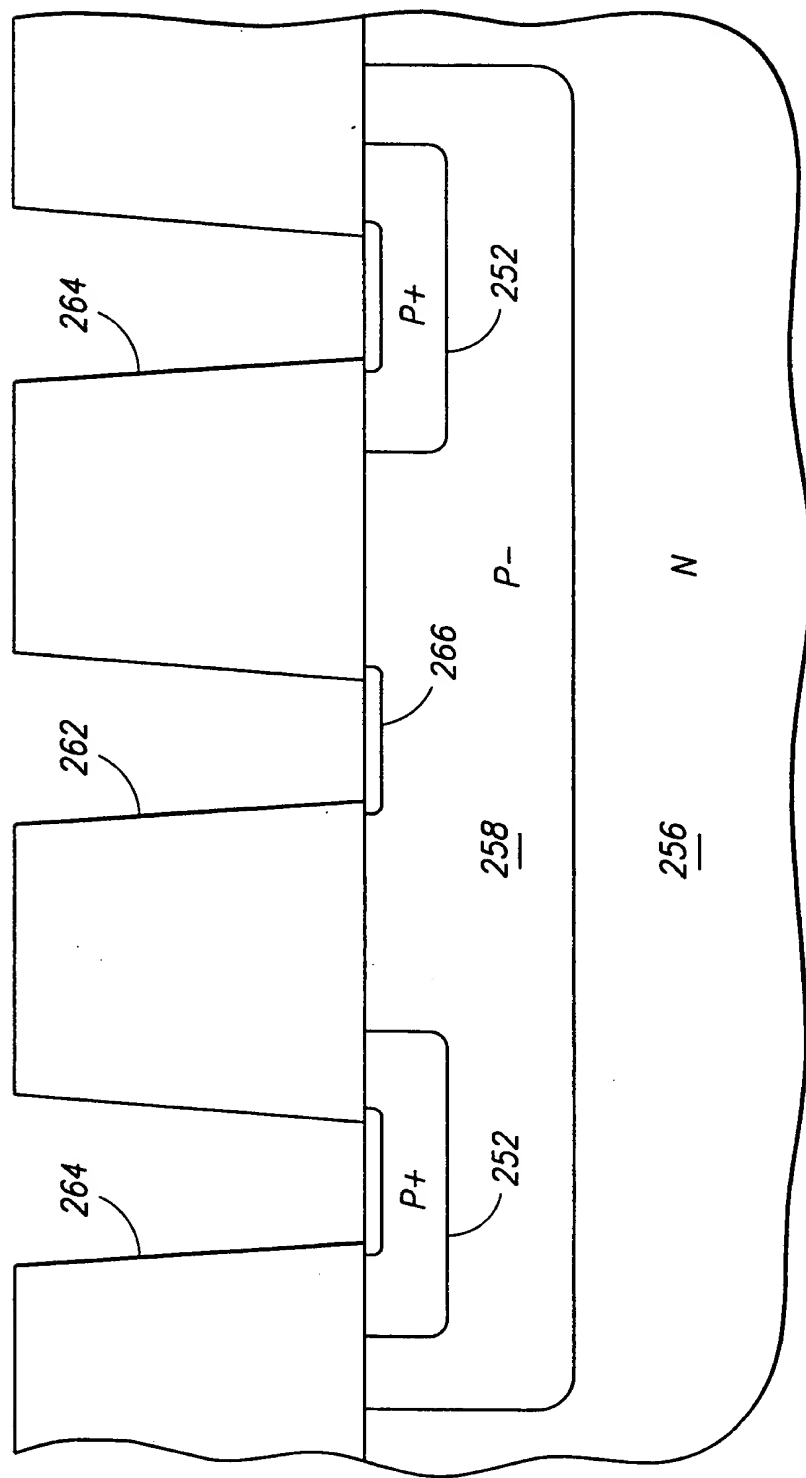
FIG. 10

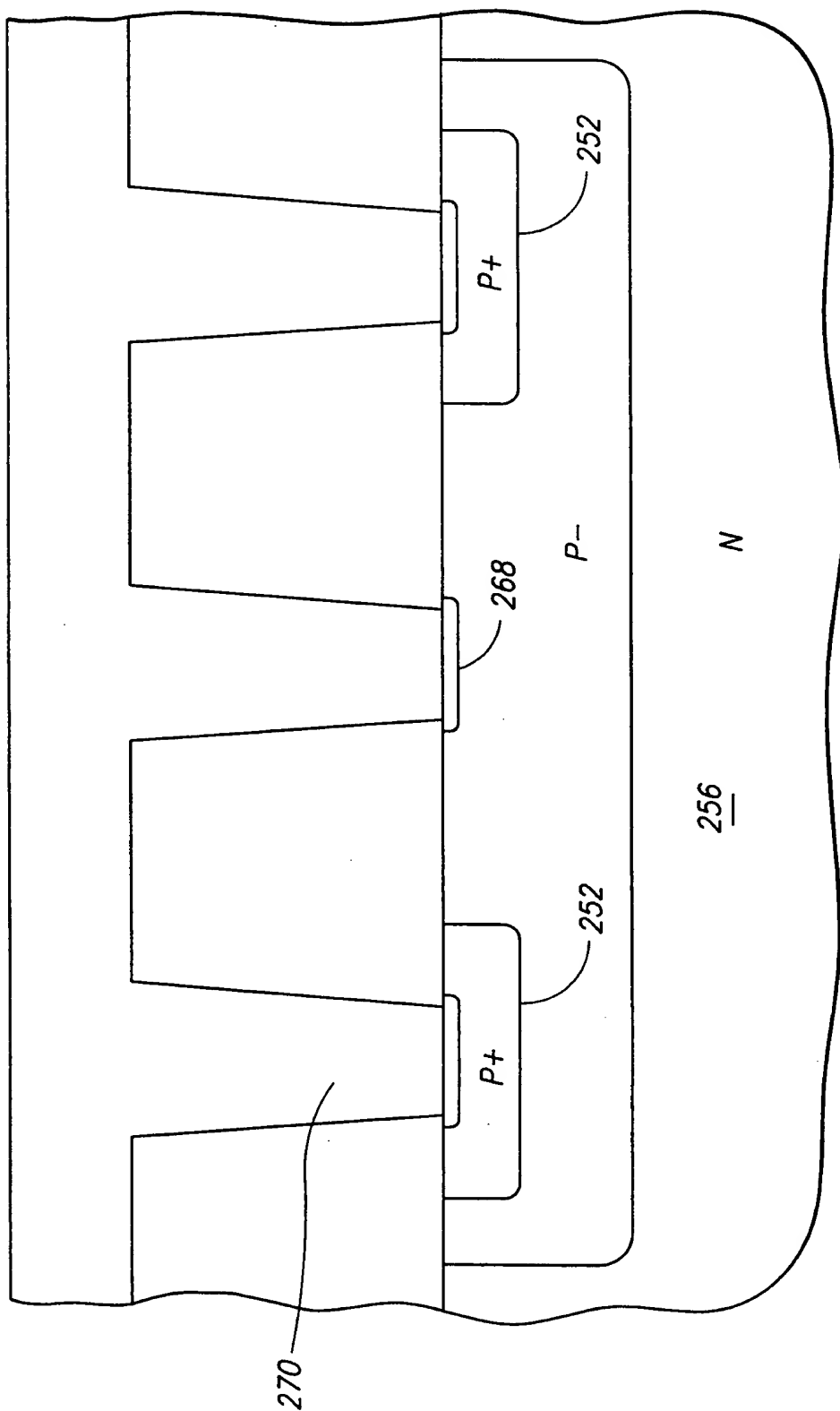
FOREF " 69022860

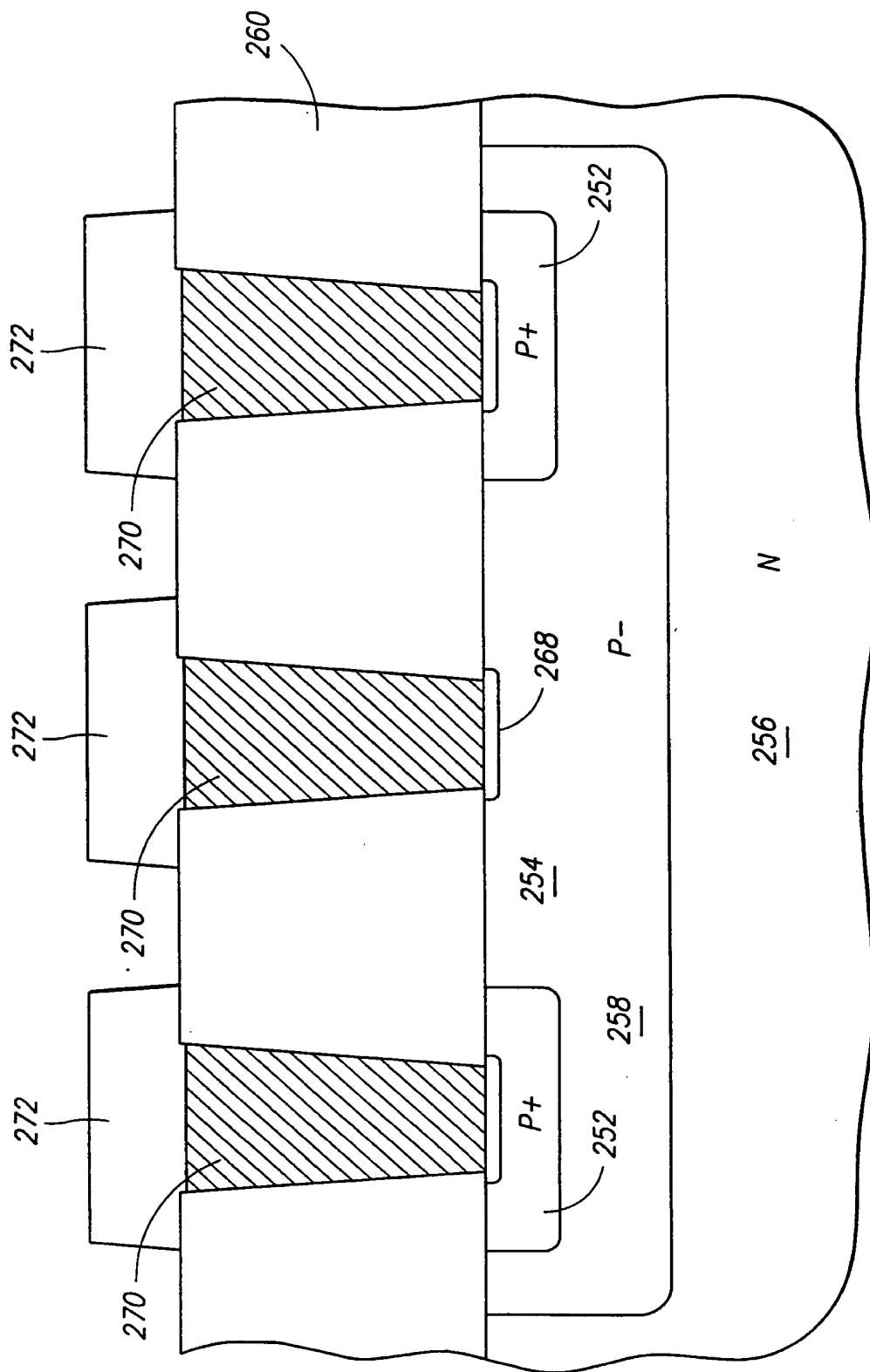
MI40-030



IT 11 11 1111







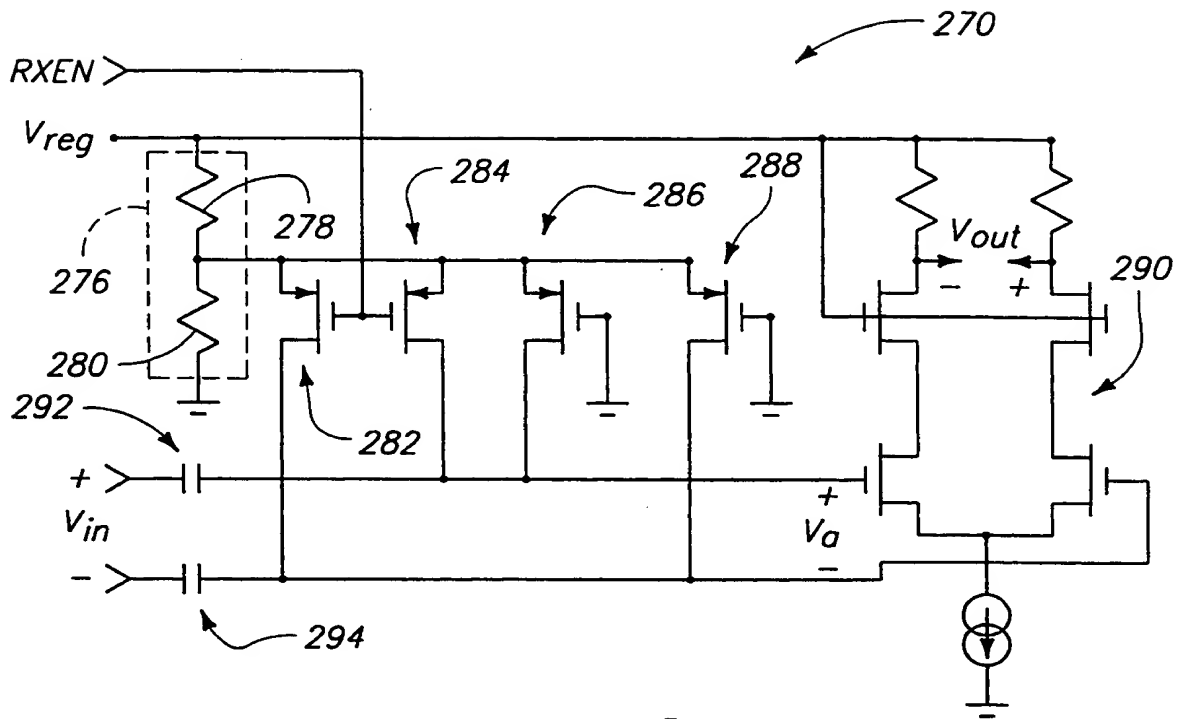


FIG. 4

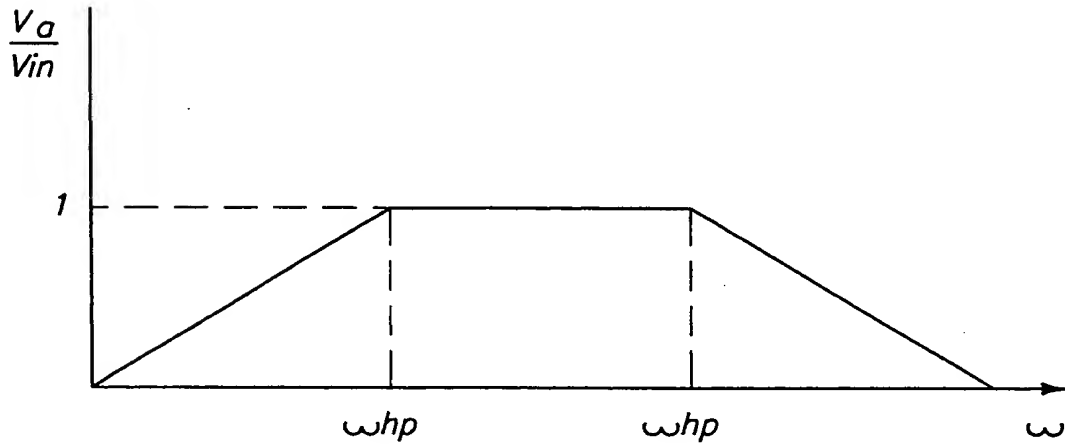
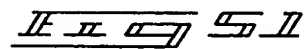


FIG. 5

FIG. 4





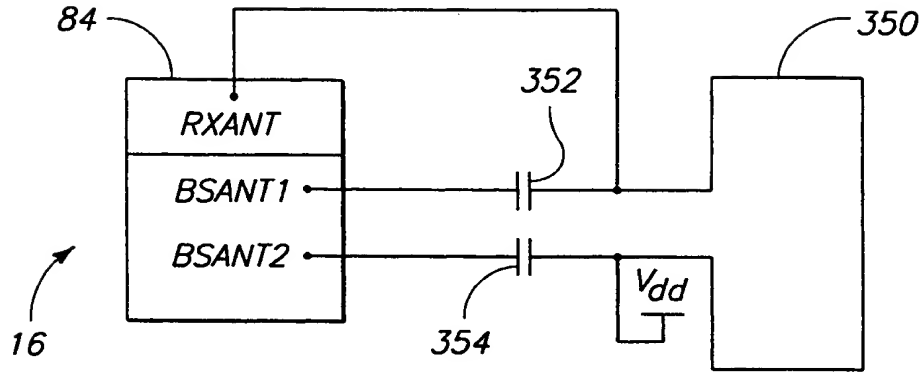


FIG. 52

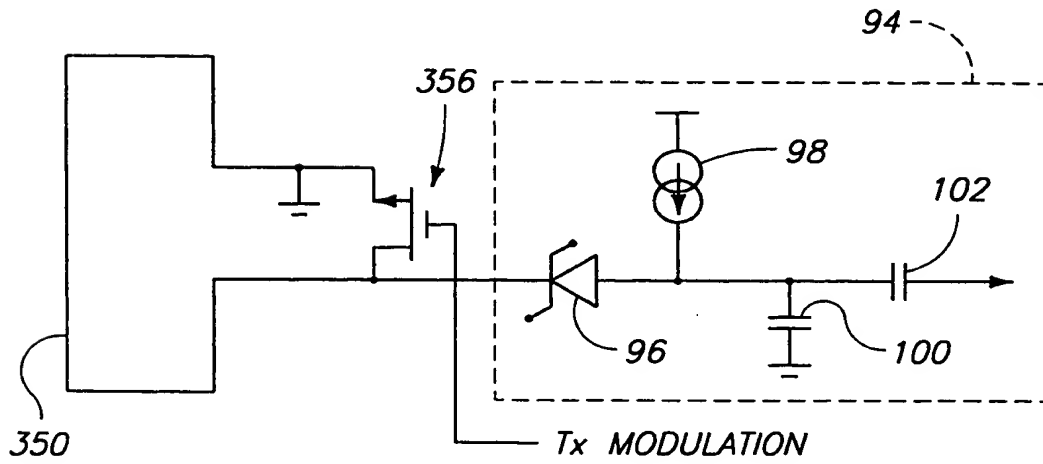
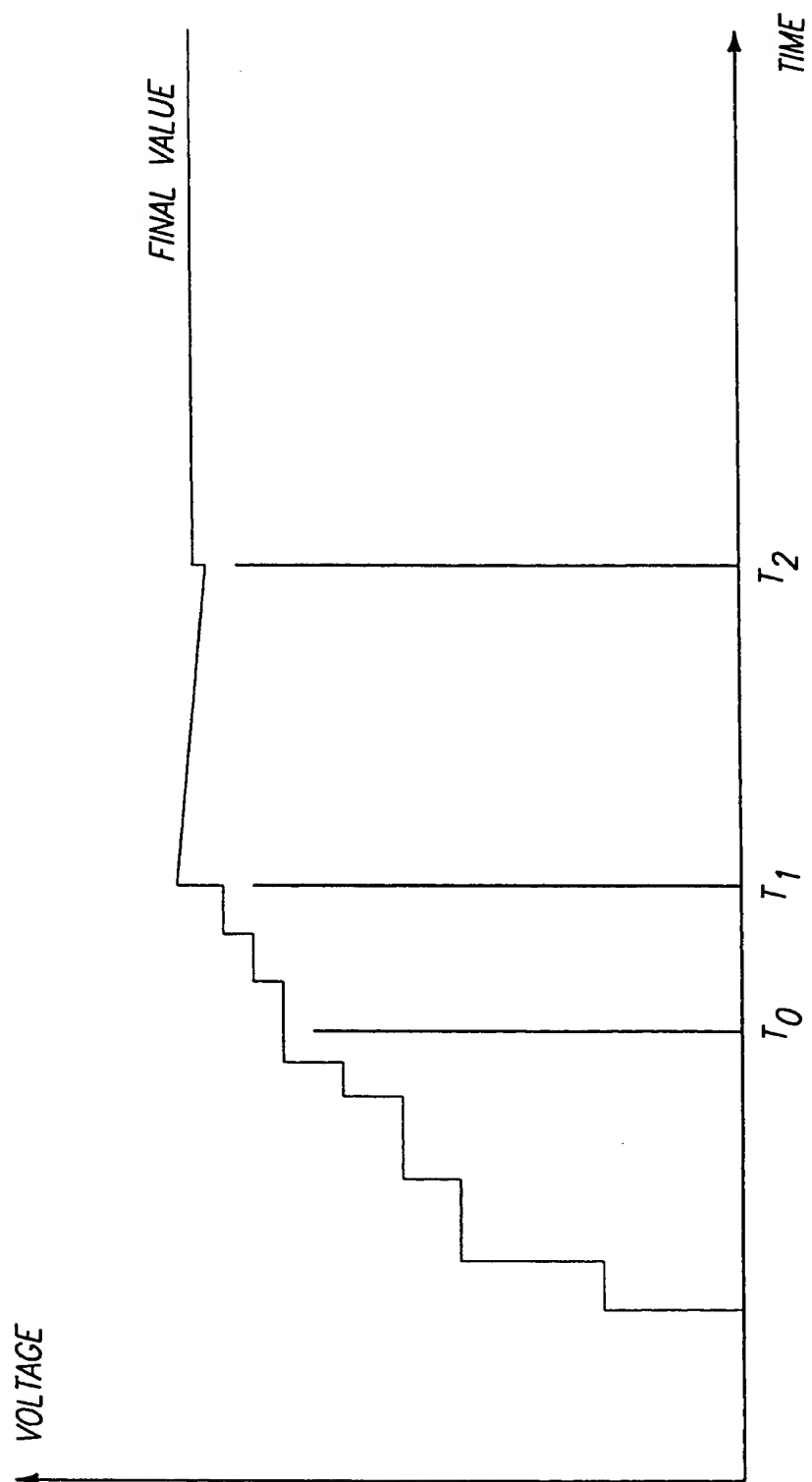
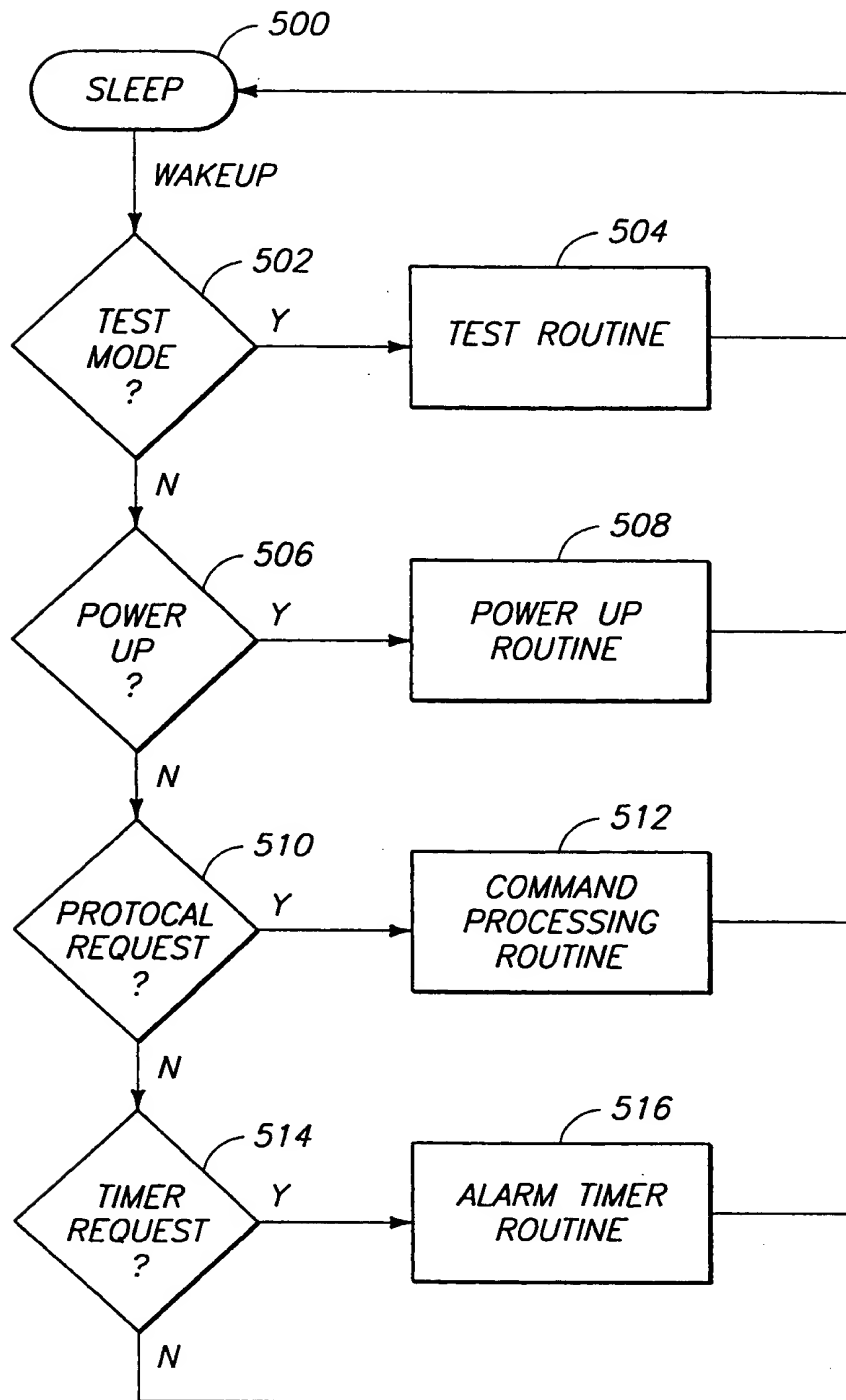


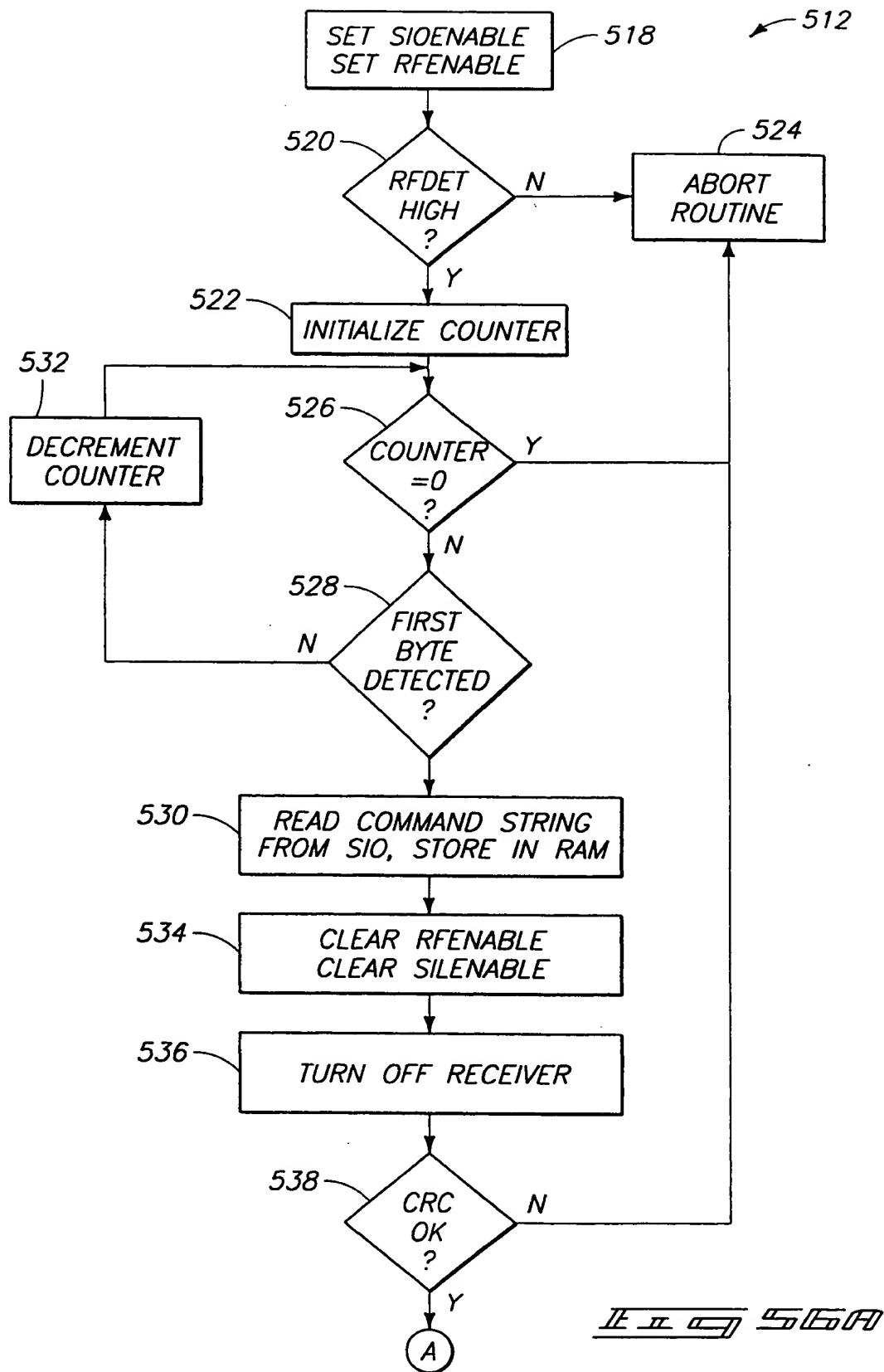
FIG. 53

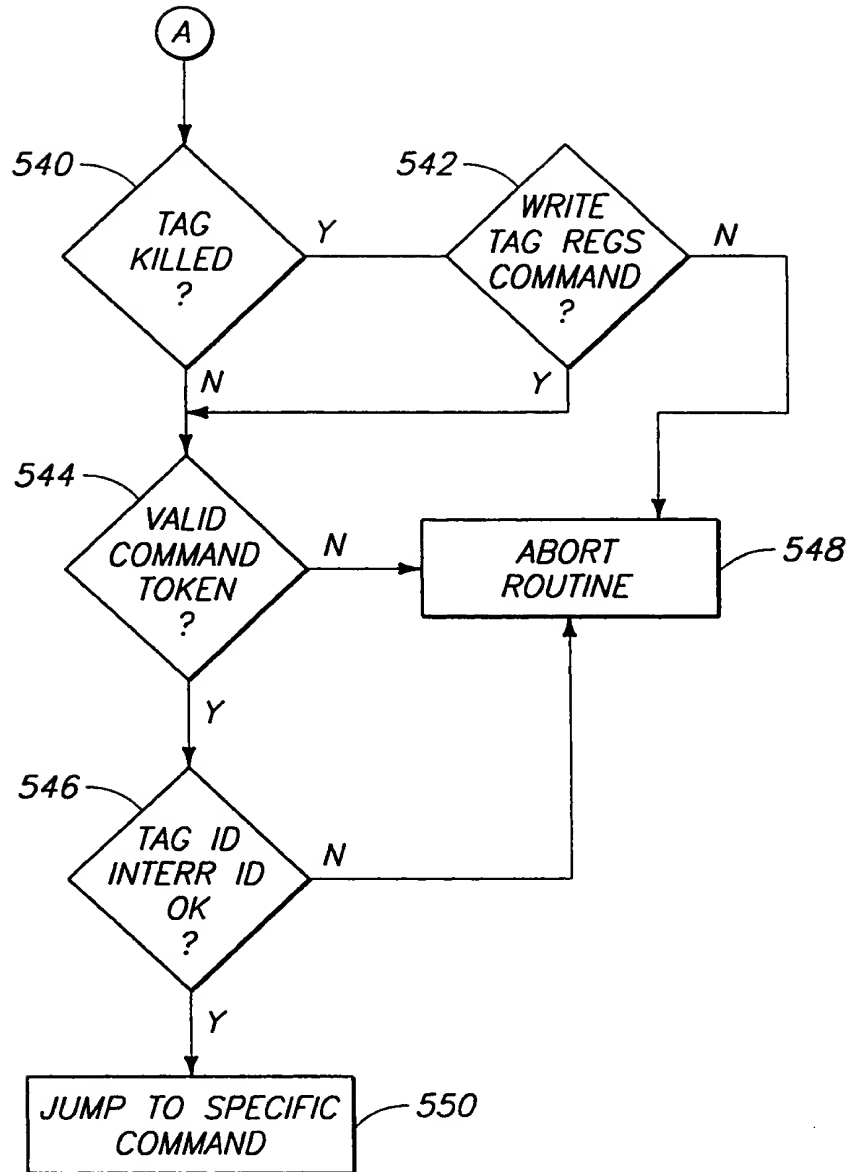
FIG. 52

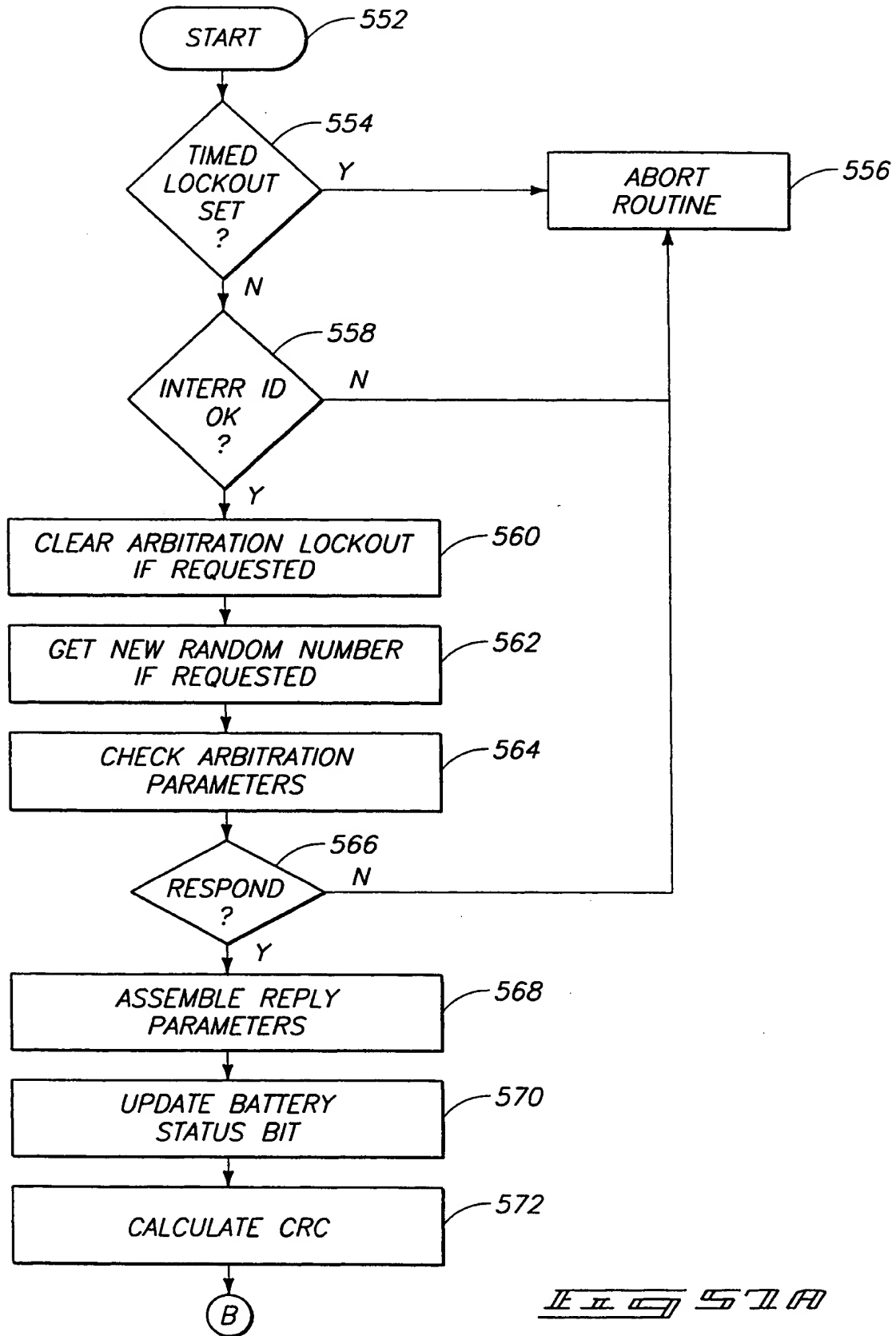
POWER SOURCE

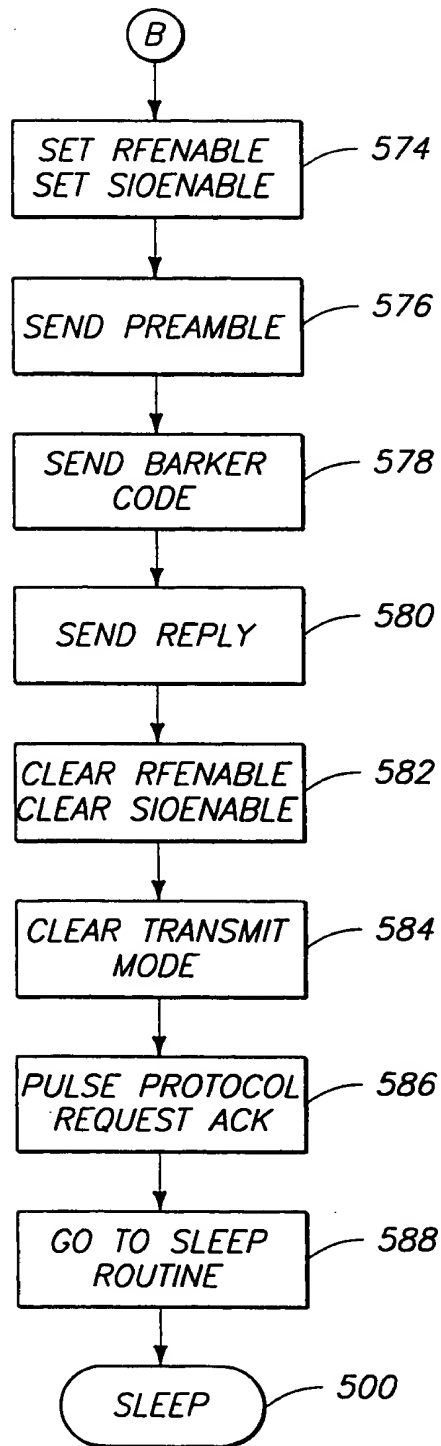
SECRET



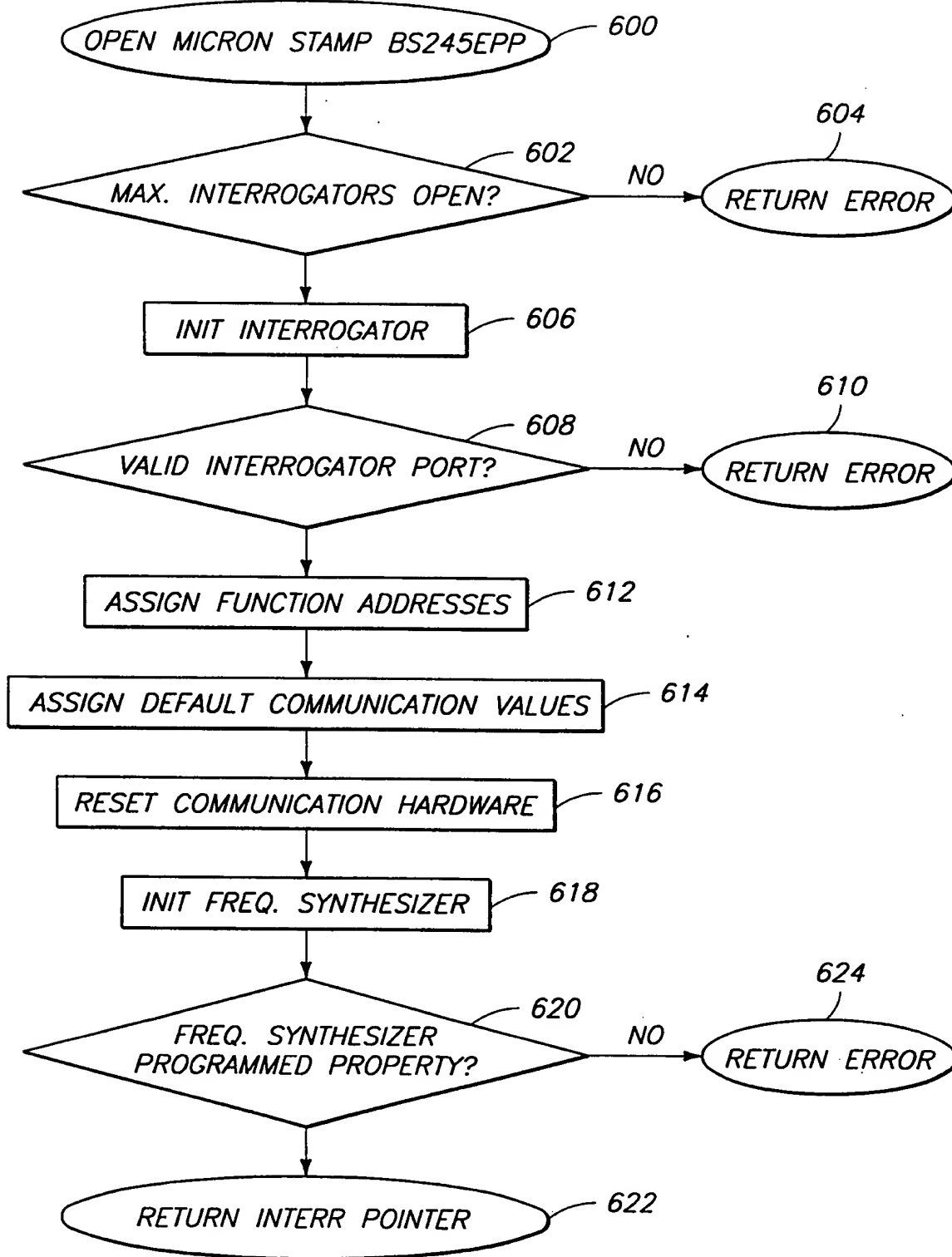


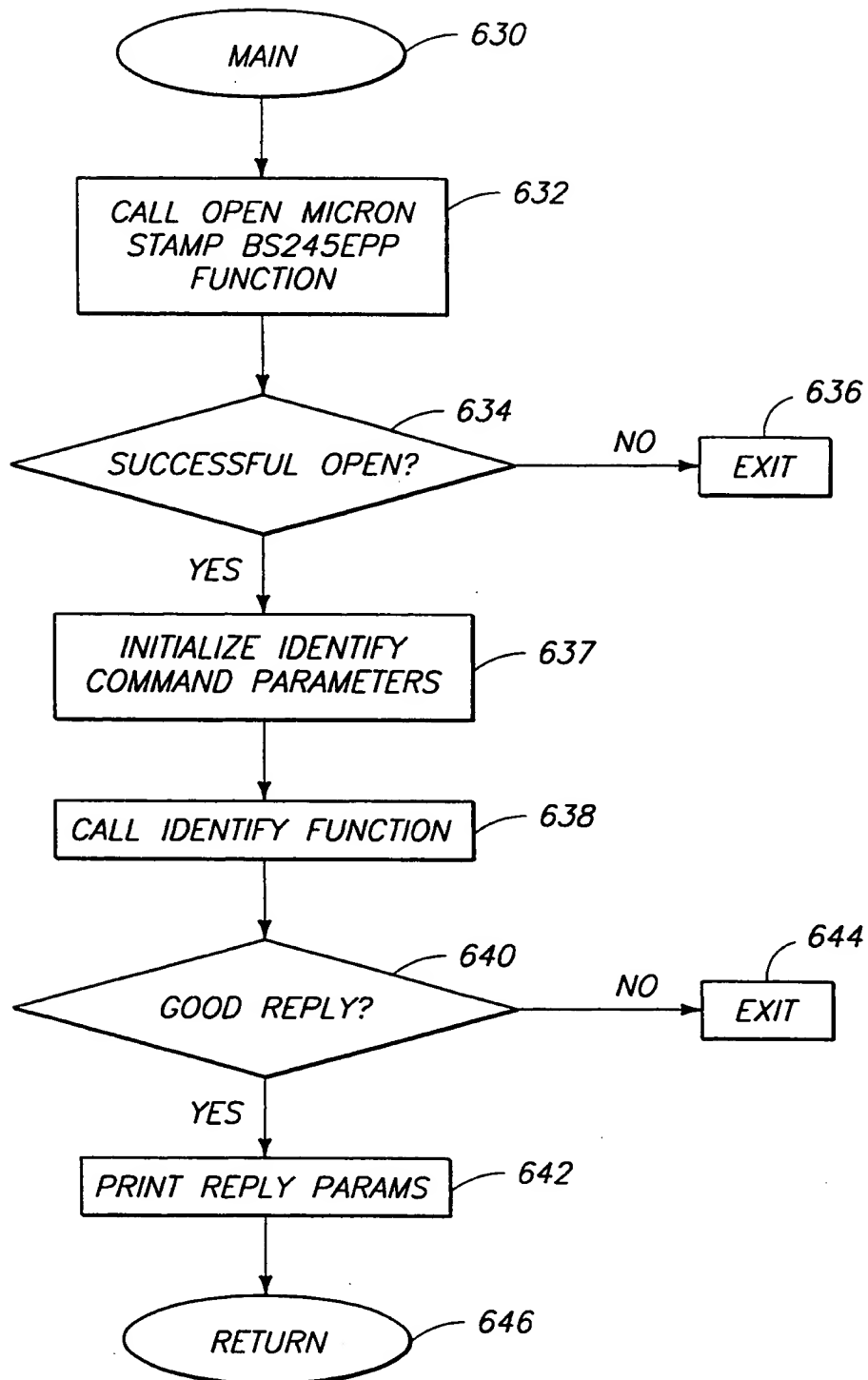
IEEE 561B

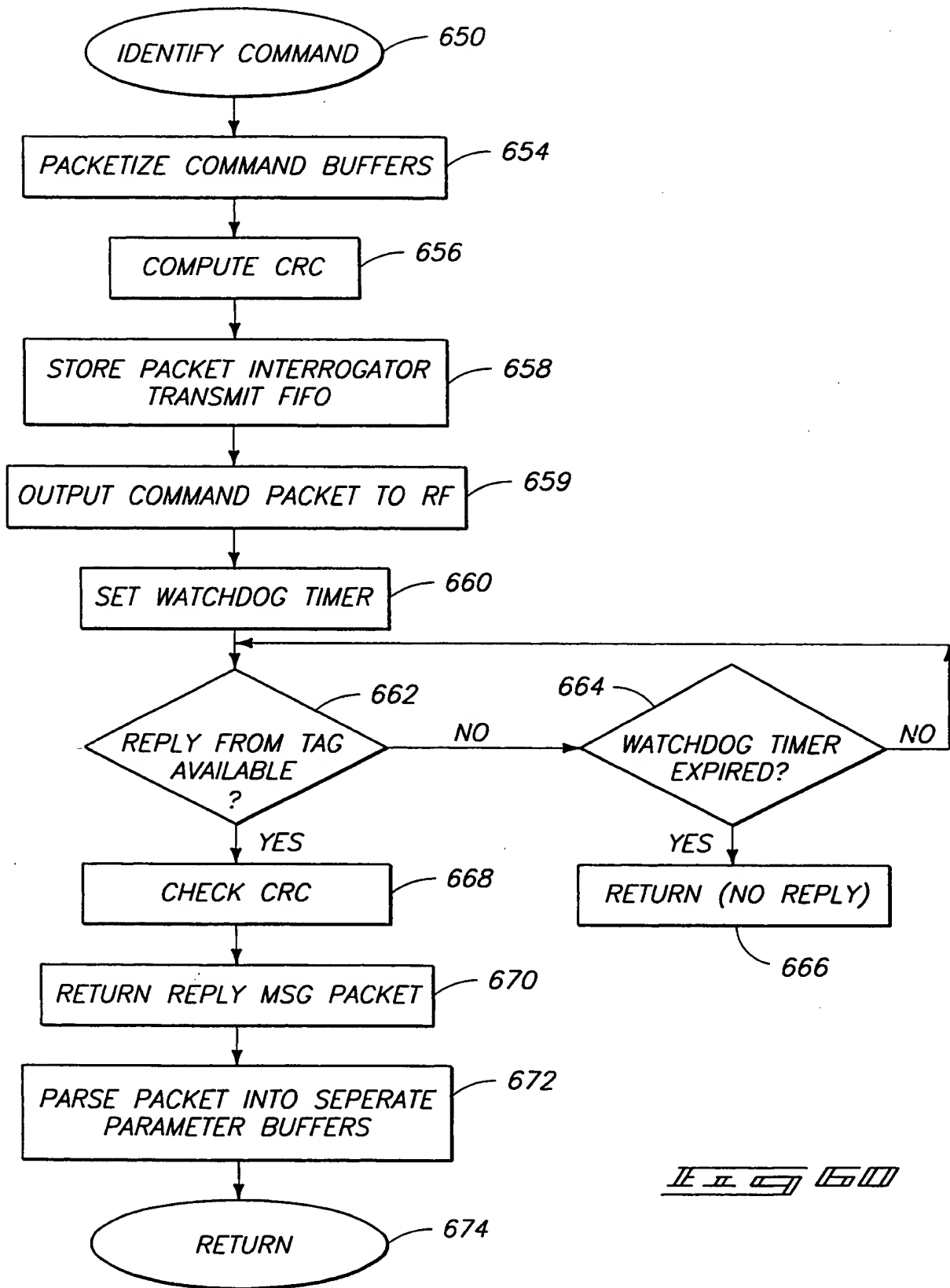
IEEE 5110

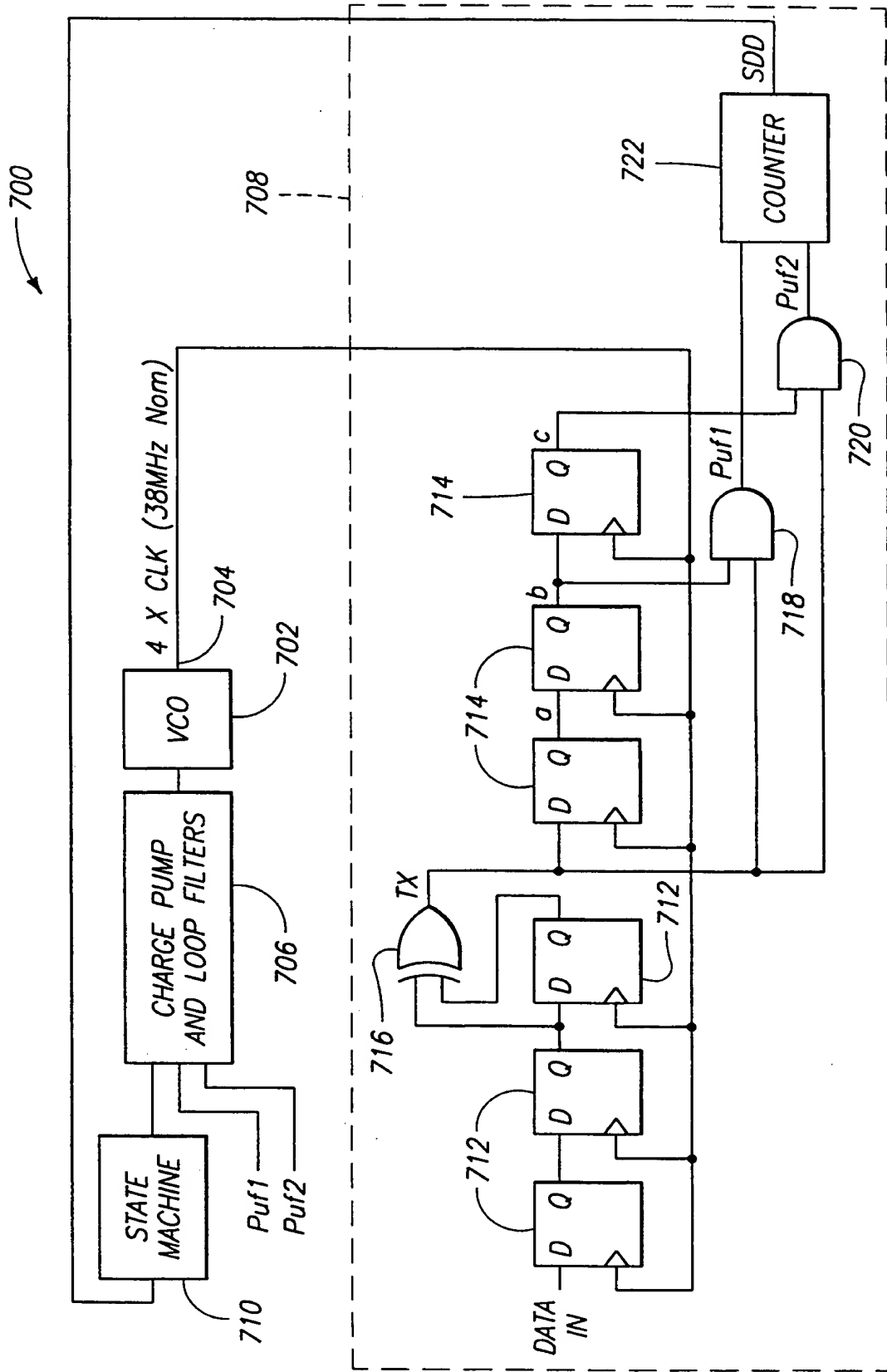




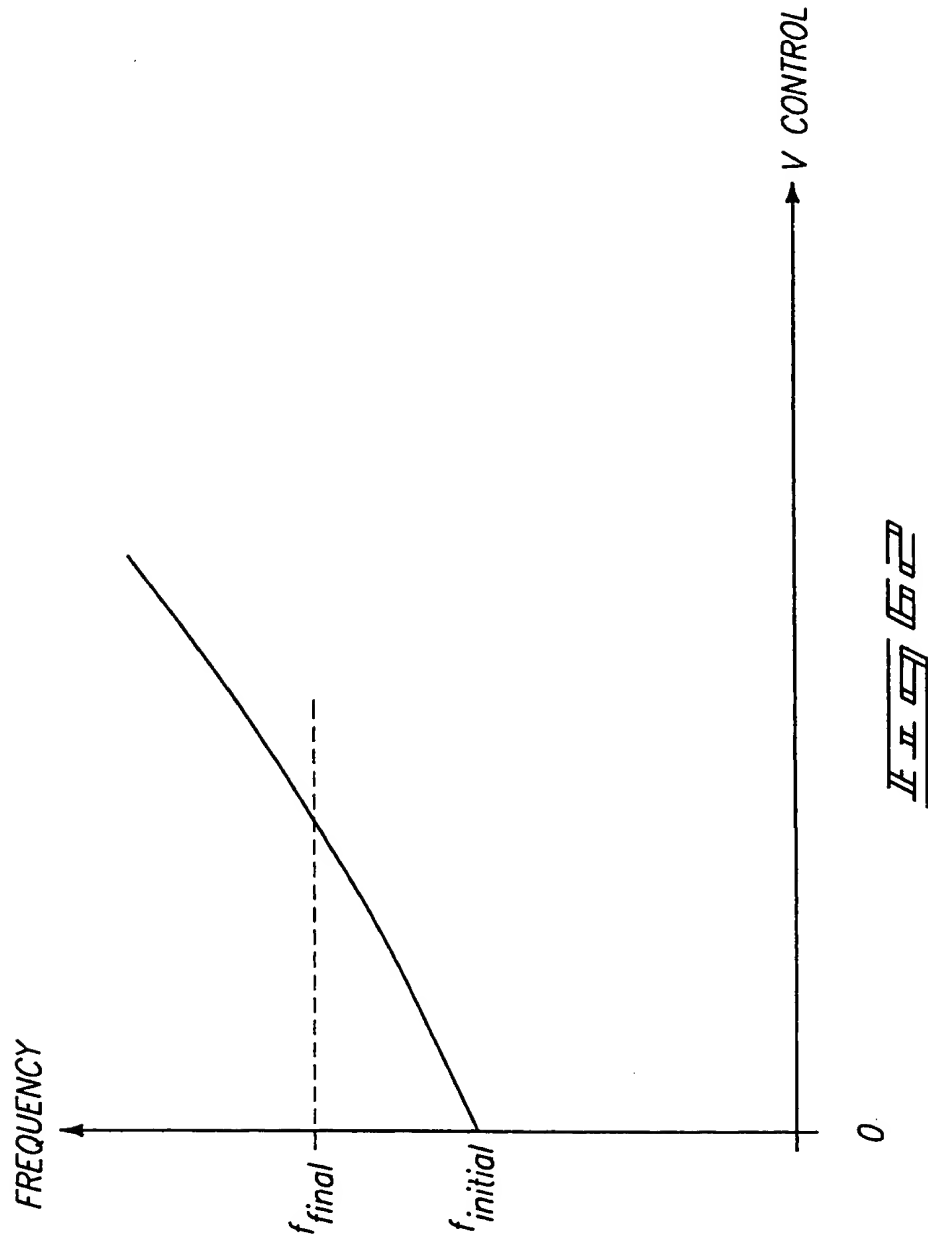








TYPE 3022B

TYPE 3022B



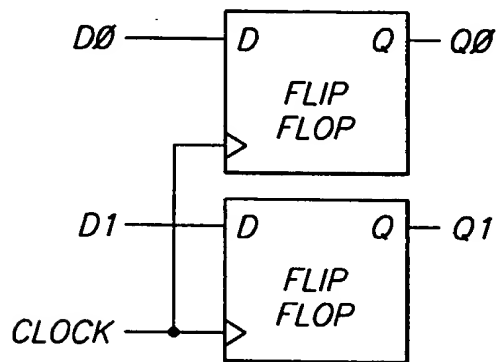
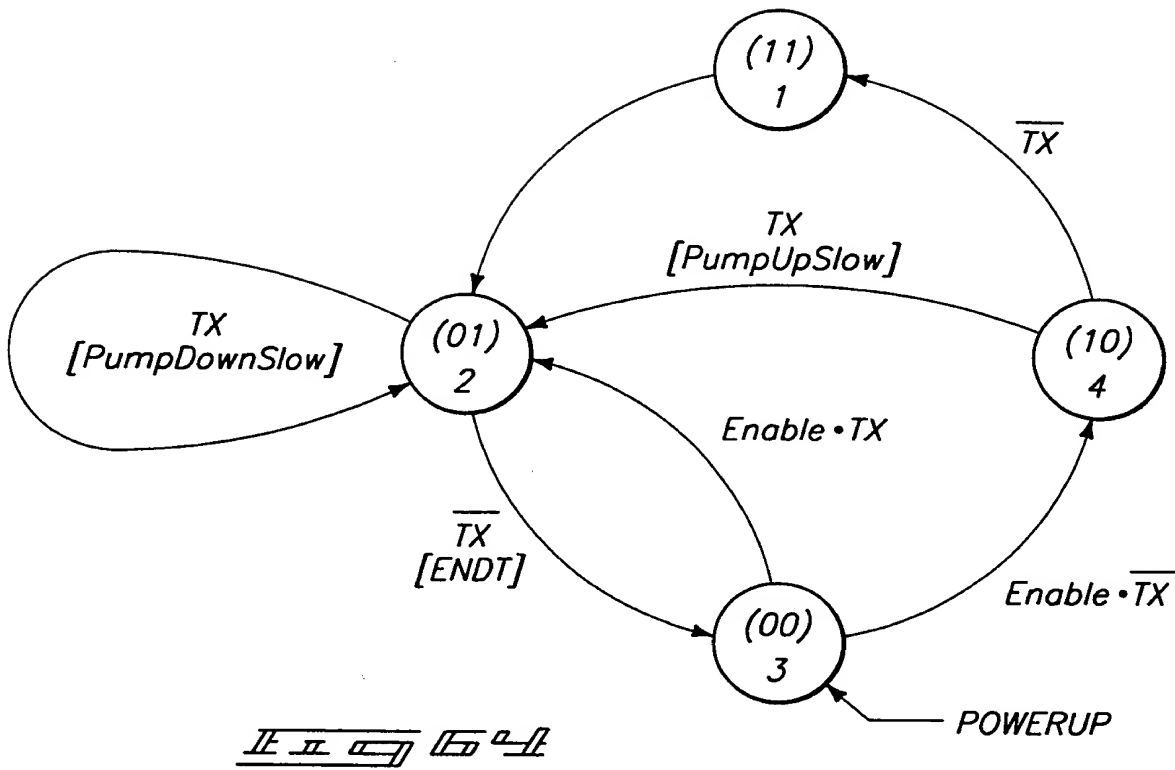


TABLE 1

MI40-030

| PRESENT STATE |    |    |    | NEXT STATE |    |
|---------------|----|----|----|------------|----|
| ENABLE        | TX | Q1 | Q0 | D1         | D0 |
| 0             | 0  | 0  | 0  | 0          | 0  |
| 0             | 1  | 0  | 0  | 0          | 0  |
| 1             | 0  | 0  | 0  | 1          | 0  |
| 1             | 1  | 0  | 0  | 0          | 1  |
| X             | 0  | 0  | 1  | 0          | 0  |
| X             | 1  | 0  | 1  | 0          | 1  |
| X             | X  | 1  | 1  | 0          | 1  |
| X             | 0  | 1  | 0  | 1          | 1  |
| X             | 1  | 1  | 0  | 0          | 1  |

TABLE 1



| En TX |    | Q1 Q0 |    |    |    |
|-------|----|-------|----|----|----|
|       |    | 00    | 01 | 11 | 10 |
| D0:   | 00 | 0     | 0  | 1  | 1  |
|       | 01 | 0     | 1  | 1  | 1  |
|       | 11 | 1     | 1  | 1  | 1  |
|       | 10 | 0     | 0  | 1  | 1  |

IEEE 801.1

| En TX |    | Q1 Q0 |    |    |    |
|-------|----|-------|----|----|----|
|       |    | 00    | 01 | 11 | 10 |
| D1:   | 00 | 0     | 0  | 0  | 1  |
|       | 01 | 0     | 0  | 0  | 0  |
|       | 11 | 0     | 0  | 0  | 0  |
|       | 10 | 1     | 0  | 0  | 1  |

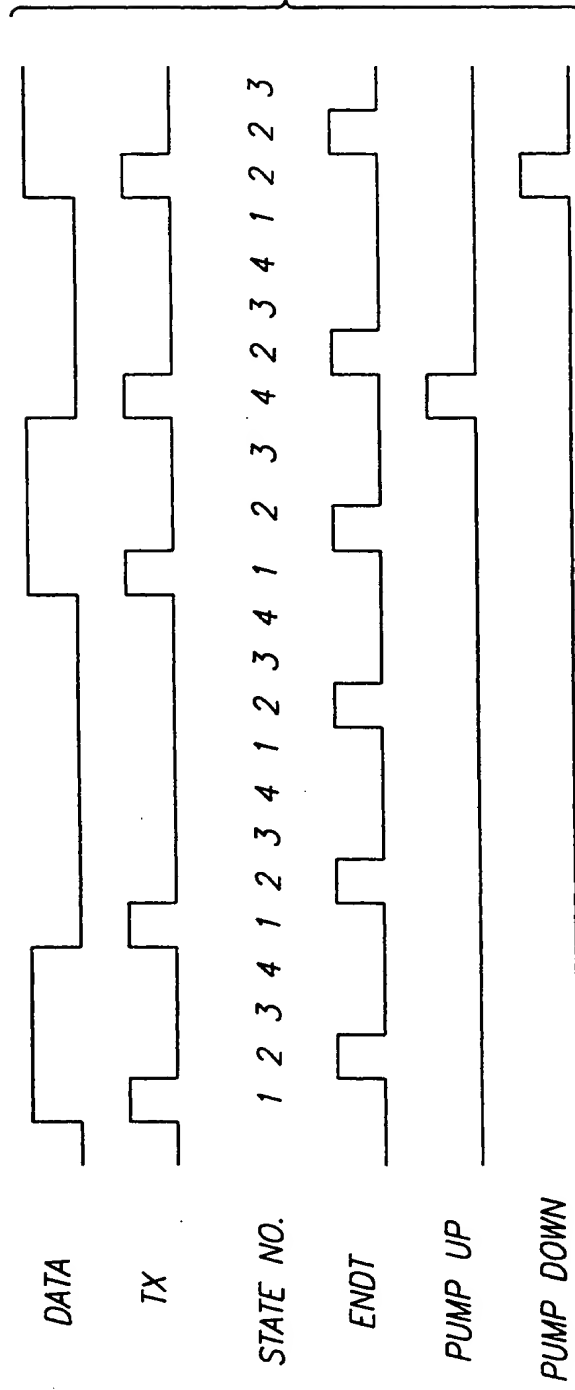
IEEE 801.1

IEEE 801.1



2025-03-07 10:00 AM

CODED 2902960



II II

FORM 8-60 23022300

| NAME        | CURRENT ( $\mu$ A) | $\Delta V$ (mV) | $\Delta V/V$ CONTROL(NOM) X 100 |
|-------------|--------------------|-----------------|---------------------------------|
| COARSE      | 40                 | 160             | 13.3%                           |
| MEDIUM      | 10                 | 40              | 3.3                             |
| MEDIUM FINE | 1                  | 2.6             | 0.22                            |
| FINE        | 0.1                | 0.26            | 0.022                           |

II 9 72

TOP SECRET

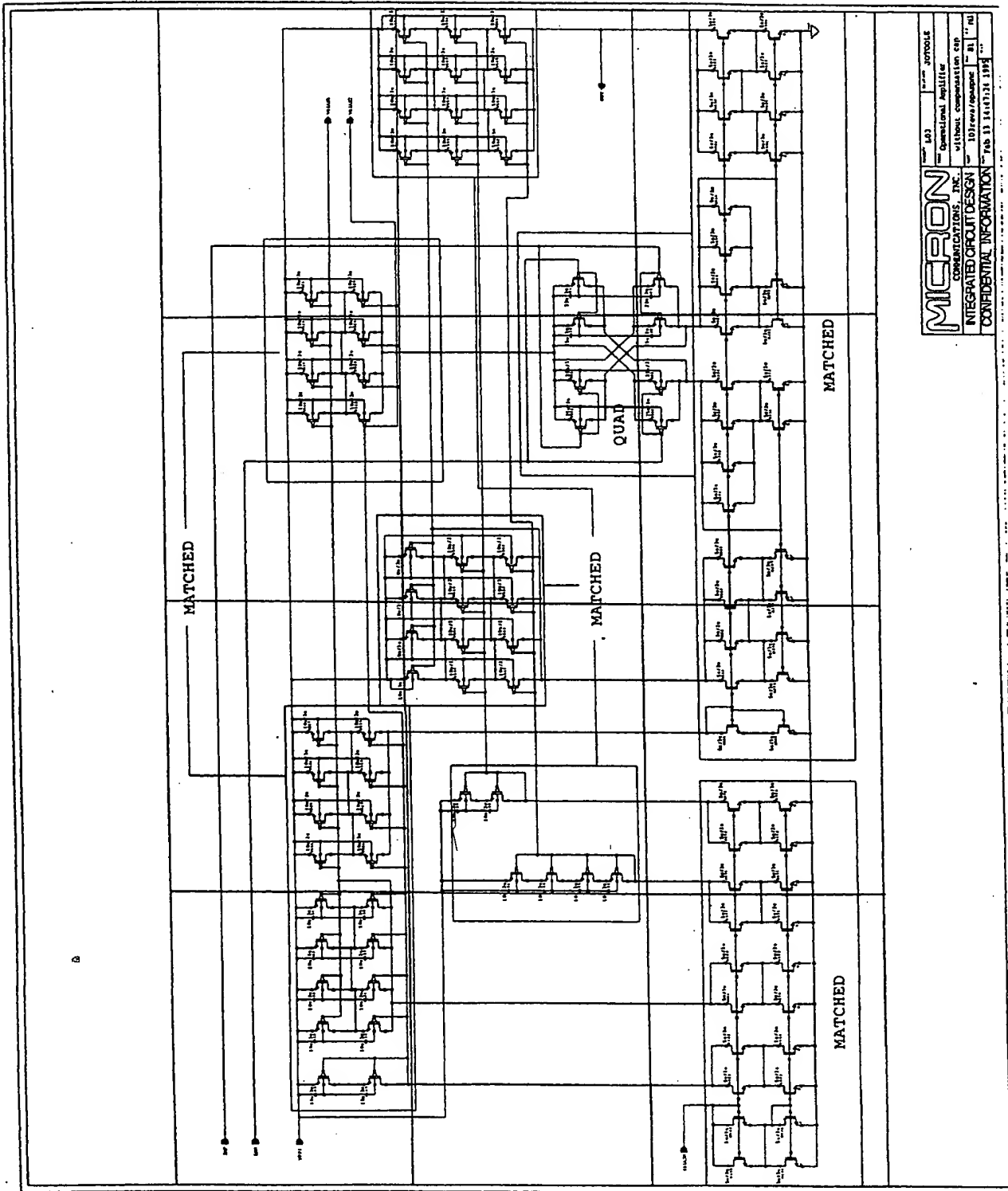


FIG. 9.0501

|                           |  |                          |     |      |
|---------------------------|--|--------------------------|-----|------|
| <b>MICRON</b>             |  | 6400                     | 16K | 1T1R |
| MICRON TECHNOLOGIES, INC. |  | Operational Amplifier    |     |      |
| INTEGRATED CIRCUIT DESIGN |  | without compensation cap |     |      |
| CONFIDENTIAL INFORMATION  |  | 10Tres/opcode            | 21  | 12   |
|                           |  | Feb 13 14:07:34 1983     |     |      |

b6  
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b7TD  
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b7WU  
b7WV  
b7WW  
b7WX  
b7WY  
b7WZ  
b7XA  
b7XB  
b7XC  
b7XD  
b7XE  
b7XF  
b7XG  
b7XH  
b7XI  
b7XJ  
b7XK  
b7XL  
b7XM  
b7XN  
b7XO  
b7XP  
b7XQ  
b7XR  
b7XS  
b7XT  
b7XU  
b7XV  
b7XW  
b7XX  
b7XY  
b7XZ  
b7YA  
b7YB  
b7YC  
b7YD  
b7YE  
b7YF  
b7YG  
b7YH  
b7YI  
b7YJ  
b7YK  
b7YL  
b7YM  
b7YN  
b7YO  
b7YP  
b7YQ  
b7YR  
b7YS  
b7YT  
b7YU  
b7YV  
b7YW  
b7YX  
b7YY  
b7YZ  
b7ZA  
b7ZB  
b7ZC  
b7ZD  
b7ZE  
b7ZF  
b7ZG  
b7ZH  
b7ZI  
b7ZJ  
b7ZK  
b7ZL  
b7ZM  
b7ZN  
b7ZO  
b7ZP  
b7ZQ  
b7ZR  
b7ZS  
b7ZT  
b7ZU  
b7ZV  
b7ZW  
b7ZX  
b7ZY  
b7ZZ

|        |        |        |        |        |
|--------|--------|--------|--------|--------|
| 9.06AA | 9.06AB | 9.06AC | 9.06AD | 9.06AE |
| 9.06BA | 9.06BB | 9.06BC | 9.06BD | 9.06BE |
| 9.06CA | 9.06CB | 9.06CC | 9.06CD |        |
| 9.06DA | 9.06DB | 9.06DC | 9.06DD |        |

9.06

Fig. 9.06a



|        |        |        |        |        |        |        |        |        |
|--------|--------|--------|--------|--------|--------|--------|--------|--------|
| 9.07AA | 9.07AB | 9.07AC | 9.07AD | 9.07AE | 9.07AF | 9.07AG | 9.07AH | 9.07AI |
| 9.07BA | 9.07BB | 9.07BC | 9.07BD | 9.07BE | 9.07BF | 9.07BG | 9.07BH | 9.07BI |
| 9.07CA | 9.07CB | 9.07CC | 9.07CD | 9.07CE | 9.07CF | 9.07CG | 9.07CH |        |
| 9.07DA | 9.07DB | 9.07DC | 9.07DD | 9.07DE | 9.07DF | 9.07DG |        |        |
| 9.07EA | 9.07EB | 9.07EC | 9.07ED | 9.07EE | 9.07EF | 9.07EG |        |        |

II II II II II II II II



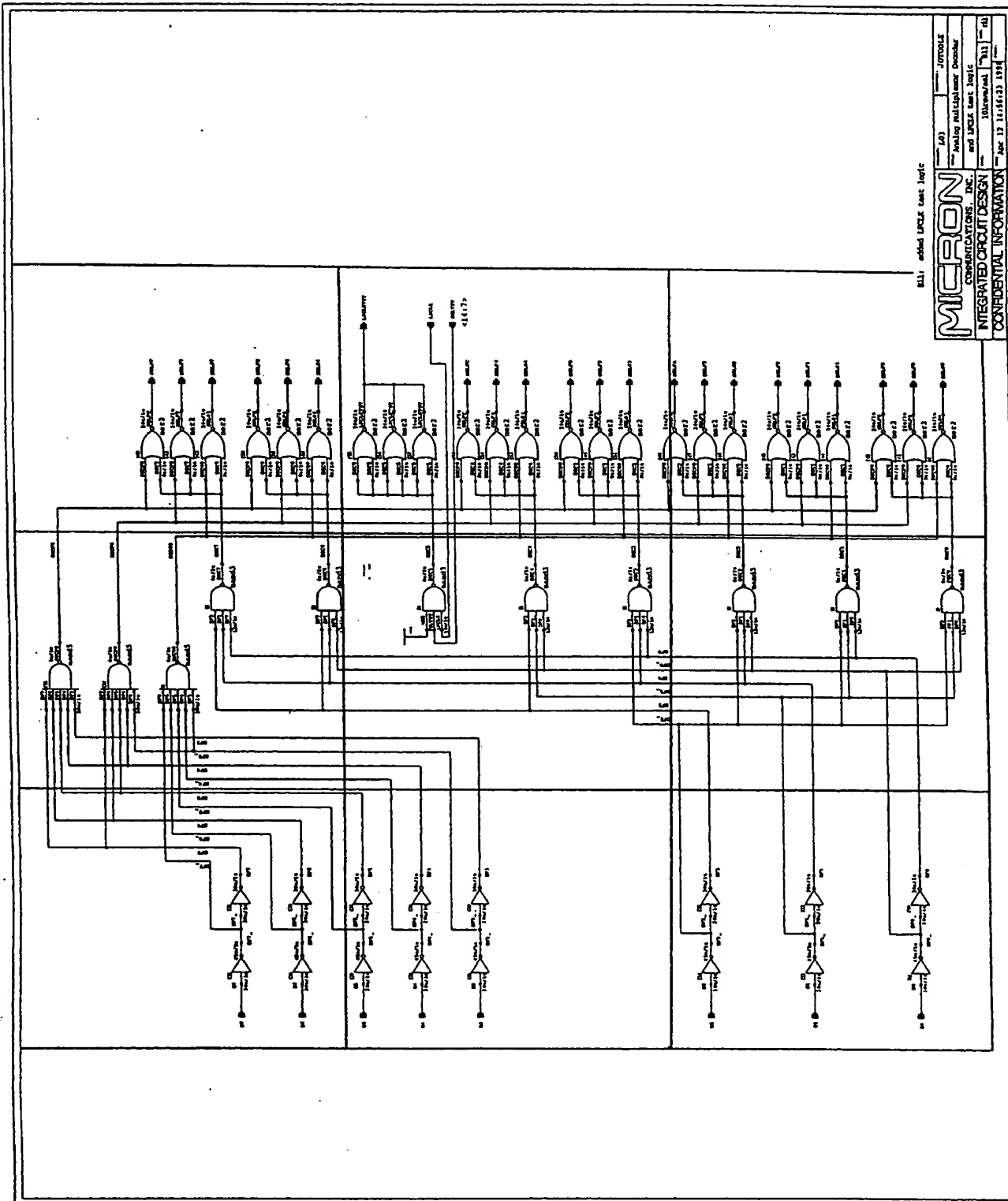
|                                  |         |                      |      |         |
|----------------------------------|---------|----------------------|------|---------|
| <b>MICRON</b>                    | NAME    | L03                  | DATE | JANUARY |
| <b>COMMUNICATIONS, INC.</b>      | FROM    |                      |      |         |
| <b>INTEGRATED CIRCUIT DESIGN</b> | TO      | 6/18694/5904/LIA     |      |         |
| <b>CONFIDENTIAL INFORMATION</b>  | REMARKS | 1012604/apurces      | BY   | BJ      |
|                                  |         | APR 21 09:13:23 1978 |      |         |

FIG. 9.07

TABLE 9.08

|        |        |        |
|--------|--------|--------|
| 9.08AA | 9.08AB | 9.08AC |
| 9.08BA | 9.08BB | 9.08BC |
| 9.08CA | 9.08CB | 9.08CC |

TABLE 9.08



10000, added 10008 logic

|                           |  |       |       |
|---------------------------|--|-------|-------|
| MICRON                    |  | 10000 | 10008 |
| COMMUNICATIONS, INC.      |  | 10000 | 10008 |
| INTEGRATED CIRCUIT DESIGN |  | 10000 | 10008 |
| CONFIDENTIAL INFORMATION  |  | 10000 | 10008 |

TABLE 2360

|        |        |
|--------|--------|
| 9.09AA | 9.09AB |
| 9.09BA | 9.09BB |

EX 9.09

403660 23022300

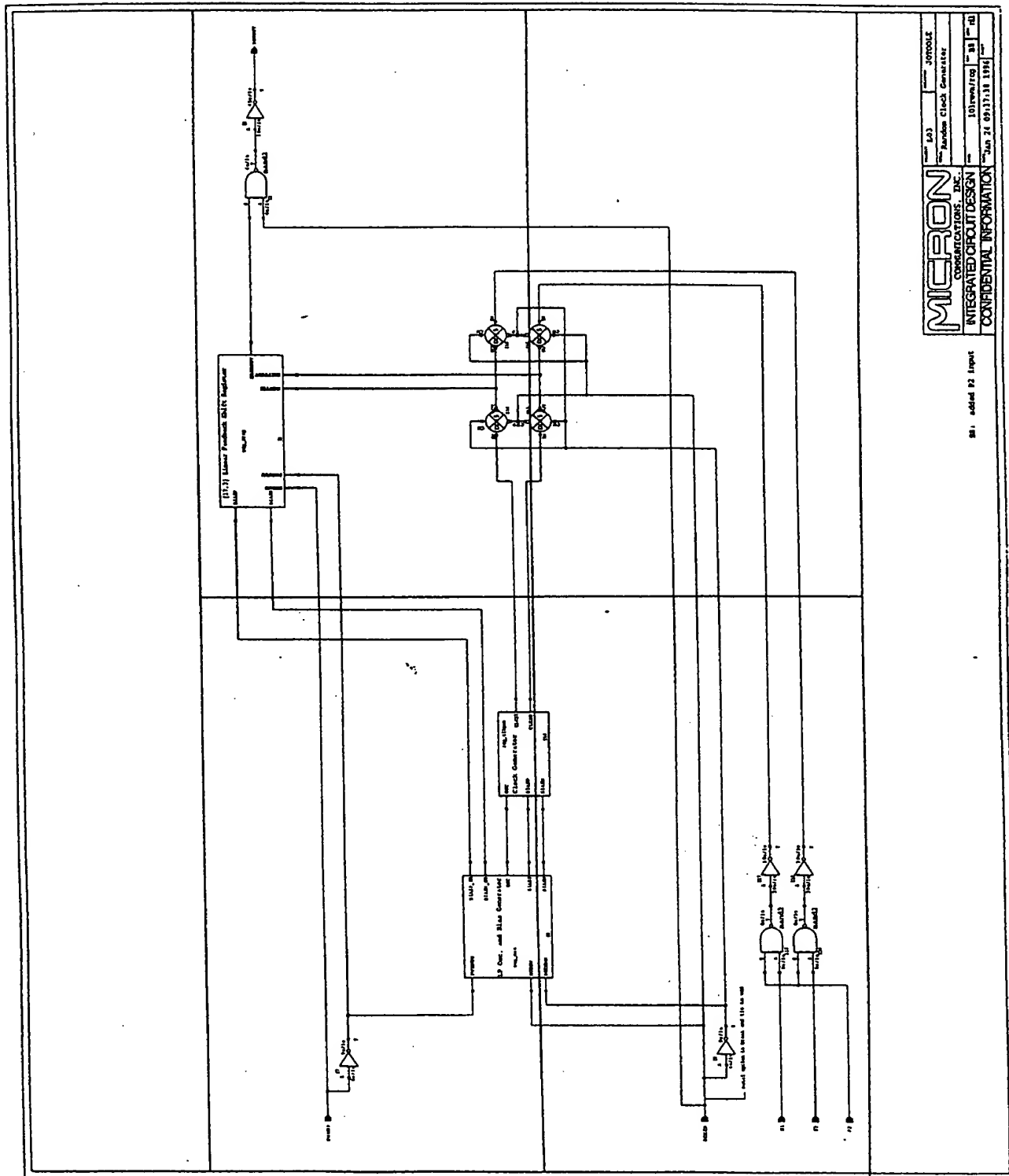


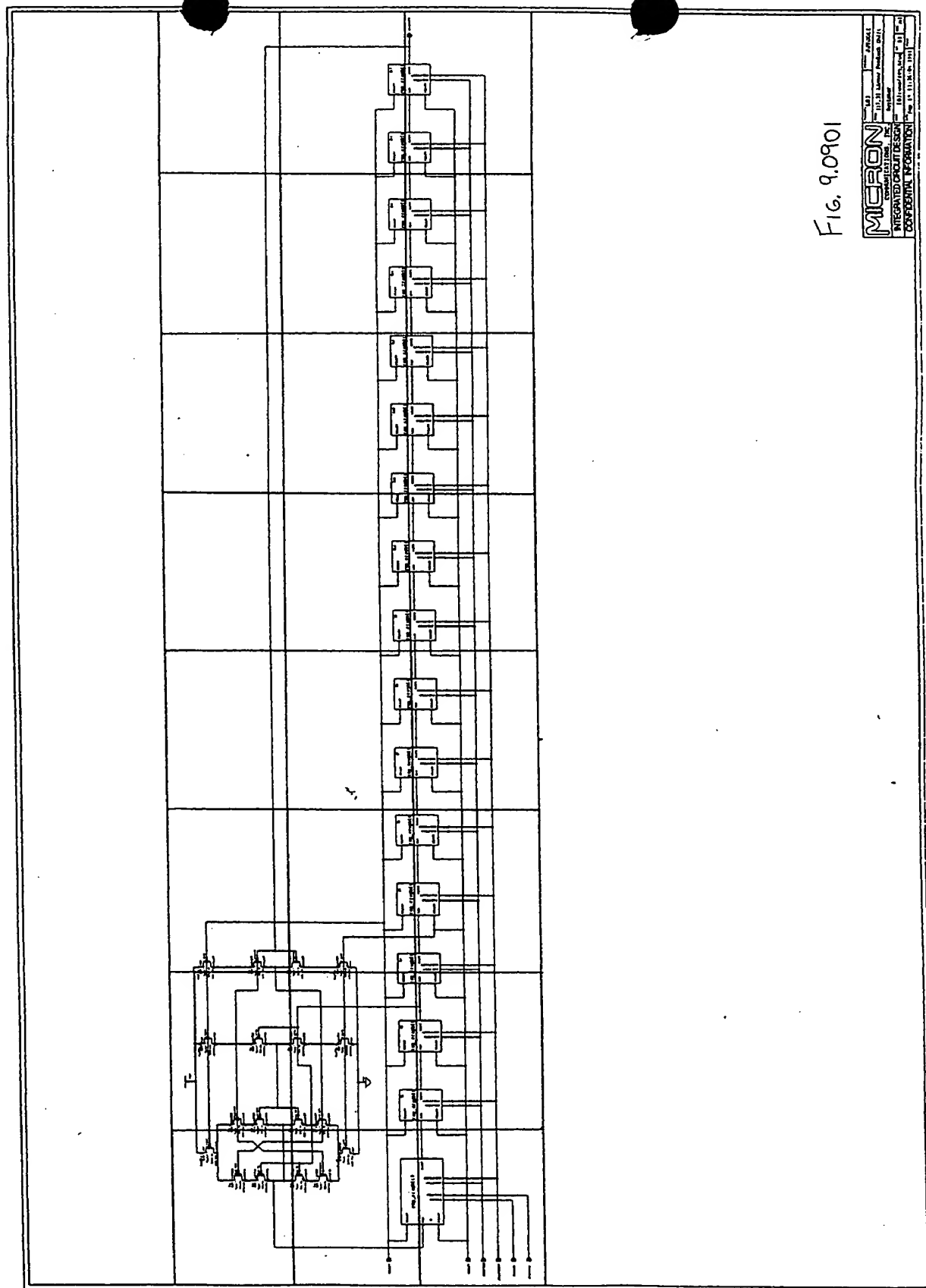
Fig. 9.09

|                           |  |                        |
|---------------------------|--|------------------------|
| MICRON                    |  | JOHNSON                |
| COMMUNICATIONS, INC.      |  | Random Clock Generator |
| INTEGRATED CIRCUIT DESIGN |  | 101mm/100              |
| CONFIDENTIAL INFORMATION  |  | Rev. 2.0 09/11/18 1995 |

881, added P2 Input

|          |          |          |          |          |          |          |          |
|----------|----------|----------|----------|----------|----------|----------|----------|
| 9.0901AA | 9.0901AB | 9.0901AC | 9.0901AD | 9.0901AE | 9.0901AF | 9.0901AG | 9.0901AH |
| 9.0901BA | 9.0901BB | 9.0901BC | 9.0901BD | 9.0901BE | 9.0901BF | 9.0901BG | 9.0901BH |
| 9.0901CA | 9.0901CB | 9.0901CC | 9.0901CD | 9.0901CE | 9.0901CF | 9.0901CG | 9.0901CH |

II 9.0901

[illegible]

FORM 9000

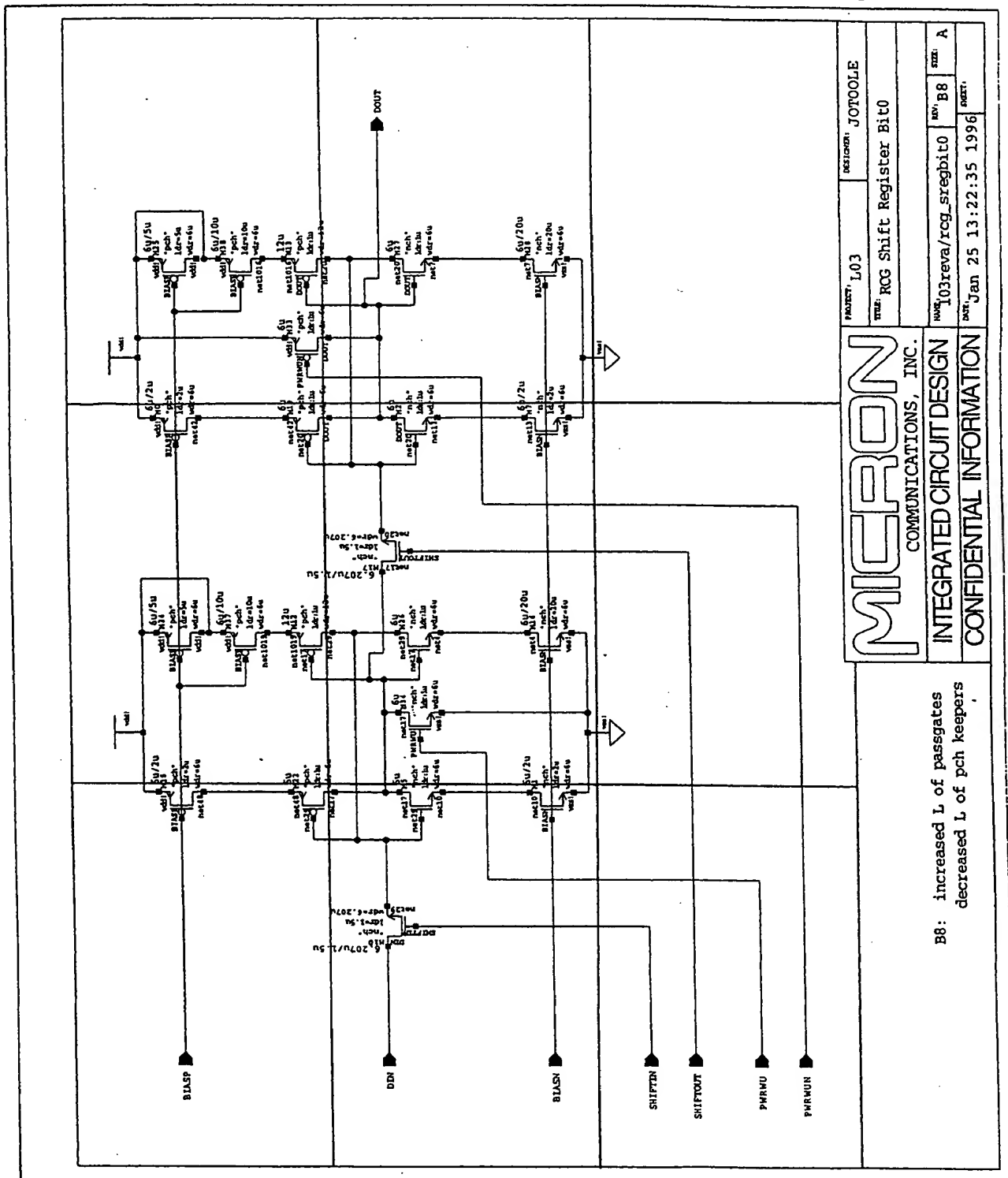
MI40-030

|            |            |            |
|------------|------------|------------|
| 9.090101AA | 9.090101AB | 9.090101AC |
| 9.090101BA | 9.090101BB | 9.090101BC |
| 9.090101CA | 9.090101CB | 9.090101CC |

FORM 9000



FIG. 9.090101



**MICRON**  
COMMUNICATIONS, INC.

B8: increased L of passgates  
decreased L of pch keepers

|                               |                 |
|-------------------------------|-----------------|
| PROJECT: L03                  | DESIGN: JOTOOLE |
| NAME: RCG Shift Register Bit0 |                 |
| DATE: Jan 25 13:22:35 1996    |                 |

|                            |         |         |
|----------------------------|---------|---------|
| NAME: 103reva/rcg_sregbit0 | REV: B8 | SIZE: A |
| CONFIDENTIAL INFORMATION   |         |         |

FIG. 9.090101